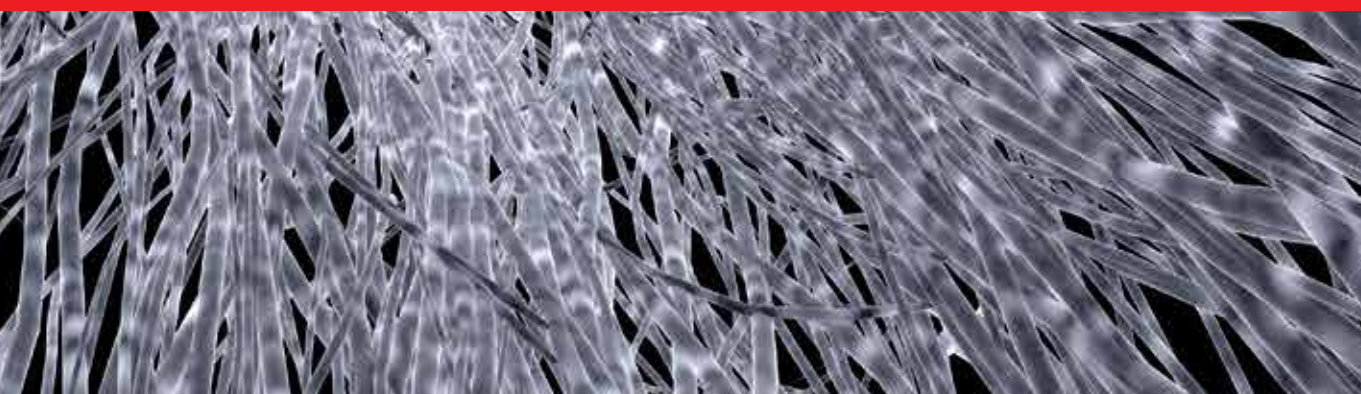


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# Nanowires

Synthesis, Properties and Applications

*Edited by Simas Rackauskas*





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# **NANOWIRES - SYNTHESIS, PROPERTIES AND APPLICATIONS**

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Edited by **Simas Rackauskas**

## **Nanowires - Synthesis, Properties and Applications**

<http://dx.doi.org/10.5772/intechopen.75337>

Edited by Simas Rackauskas

### **Contributors**

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# Meet the editor



Simas Rackauskas secured his PhD in Physics at Aalto University, Finland, in 2011. He was a Marie Curie Fellow at the University of Turin (Italy). He also held post-doctoral positions in Aalto University (Finland) and the State University of Campinas (Brazil). He had fellowships at the Swiss Federal Institute of Technology in Lausanne (EPFL, Switzerland), Technical University of Denmark (DTU), and University of Nagoya (Japan). His research interests are mainly focused on non-catalytic growth of metal oxide nanowires, application in sensing, carbon nanomaterials, and flexible electronics.





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# Preface

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Nanowires are attracting wide scientific interest due to the unique properties associated with their one-dimensional geometry. Nanowires, with diameters reaching the quantum regime, have been the focus of research for several decades and remain at the forefront of both scientific research and developing nanotechnology applications. The benefits of 1D properties lie in various applications for future quantum devices, nanoelectronics, nanophotonics, nanobiointerfaces, and energy harvesting. Developments in the understanding of the fundamental principles of nanowire growth mechanisms and mastering functionalization provide tools to control crystal structure, morphology, and interactions at the material interface, and create characteristics that are superior to those of planar geometries. This book provides a comprehensive overview of the most important developments in the field of nanowires, from their synthesis, to their properties, to their nanowire applications.

The book consists of two parts: the first is devoted to the synthesis of nanowires and characterization, and the second investigates the properties of nanowires and their applications in future devices.

The synthesis and characterization section starts with Chapter 1 dealing with nanowires of tunneling magnetoresistance (TMR) synthesis and characterization. Nanowires of Fe/MgO/Fe encapsulated in carbon nanotubes were synthesized using glancing angle deposition. Nanowires of TMR were synthesized using magnetron DC/RF sputtering by filling Fe/MgO/Fe inside vertically grown and substrate-supported carbon nanotubes. TMR is a macroscopic quantum phenomenon that allows electrical current to flow across an insulator between two electrodes with the application of an external magnetic field. The coherent TMR effect is paramount in making spintronic devices. Changing the geometry from planar trilayer nanometric thin film to a nanowire with cylindrical geometry of nanometric diameter introduces shape anisotropy, which can play an important role in coherence.

A review of diamond nanowire (DNW) synthesis methods is given in Chapter 2. Due to superior hardness, Young's modulus, and biocompatibility, optical and fluorescence nanodiamond seems to be outstanding among carbon nanomaterials. The development of DNW is known to be a significantly innovative field due to its diverse applications such as sensors, semiconductors, and electrochemical utilities. However, DNW synthesis in a reproducible way is still a challenging task. Detailed studies on DNW structures may help researchers to use them in diverse applications. In this chapter, up-to-date applications of DNW along with its synthesis, structures, and properties are presented.

Chapter 3 is dedicated to the synthesis of  $\text{LiMn}_2\text{O}_4$  nanowires and their application as a battery cathode material. Nanowires offer the following advantages: large surface-to-volume ratio, efficient electron conducting pathways, and facile strain relaxation. To enhance activi-

ty and stability, flexible spinel nanowires were synthesized via an  $\alpha$ - $\text{MnO}_2$  nanowire precursor method. Ultrathin  $\text{LiMn}_2\text{O}_4$  nanowires with cubic spinel structures were synthesized by using a solvothermal reaction to produce  $\alpha$ - $\text{MnO}_2$  nanowires followed by solid-state lithiation. The  $\text{LiMn}_2\text{O}_4$  nanowires are used as a stabilizing support during electrochemical redox processes. The unique nanoporous material effectively accommodates structural transformation during  $\text{Li}^+$  ion insertion and effectively reduces  $\text{Li}^+$  diffusion distances. Electrochemical and spectrochemical interrogation techniques have demonstrated that  $\text{LiMn}_2\text{O}_4$  nanowires are promising cathode materials for lithium ion batteries as opposed to  $\text{LiMn}_2\text{O}_4$  powders.

Section 2 is devoted to nanowire applications. The section starts with Chapter 4, dealing with nanowires for room-temperature mid-infrared emission. This chapter reviews InAs-based nanowire crystal structures for faceted quantum wells along with an analysis of their optical emission characteristics, which show quantum confinement and localization of the carriers on the quantum well nanostructure. This enables tuning of the emission wavelength and enhanced emission intensity up to the technologically important room-temperature operation point. The chapter details the growth development of advanced faceted multi-quantum well structures within InAs nanowires using molecular beam epitaxy.

Application in future electronics is investigated in Chapter 5, which is dedicated to the effects of the parasitic capacitances on scaling lateral nanowires beyond 7 nm node dimensions targeting 5 nm node technology. The gate-all-around silicon nanowire transistor has manifested itself as one of the most fortunate candidates for advanced node integrated circuits. The presence of a vast metal line forms a strong parasitic capacitance. While scaling down sub-7 nm node dimensions, these capacitances strongly influence overall device performance. In this chapter, the effects of various parasitic capacitances on scaling device dimensions are discussed, including performance at high-frequency operations.

The book ends with Chapter 6, dedicated to the original work for characterization of LSMO nanowire networks, prepared by electrospinning techniques, concentrating on the parameters influencing magnetoresistance and morphology. The nanowires form a nonwoven fabric-like arrangement, allowing the attachment of electric contacts for magnetoresistance measurements. Magnetization measurements reveal the soft magnetic character of the samples. A thorough analysis of the microstructure of these nanowire networks is performed, including scanning electron microscopy and transmission electron microscopy.

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# Synthesis and Characterization

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# Nanowires of Fe/MgO/Fe Encapsulated in Carbon Nanotubes

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Dereje Seifu

Additional information is available at the end of the chapter

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## Abstract

Nanowires of tunneling magnetoresistance (TMR) were synthesized using magnetron DC/RF sputtering by filling Fe/MgO/Fe inside vertically grown and substrate-supported carbon nanotubes. Nanocolumns of Fe/MgO/Fe TMR were synthesized using glancing angle deposition. The magnetic properties of nanowires, nanocolumns and planar nanometric thin films of Fe/MgO/Fe showed similarities including twofold magnetic symmetry. Nanowires of Fe/MgO/Fe showed improved magnetic properties, in particular its coercive field, which is 754% higher than planar thin films of Fe/MgO/Fe. A macroscopic phenomenon that can be explained only by quantum mechanics is TMR, where electrical current can flow across a nanometric thin insulator layer between two electrodes when an external magnetic field is applied parallel to the trilayer system. Coherence in the TMR effect is paramount to make spintronic devices. Nanowires possess shape anisotropy, which can play an important role in coherence.

**Keywords:** nanowires, Fe/MgO/Fe, magnetoresistance, anisotropy, sputtering

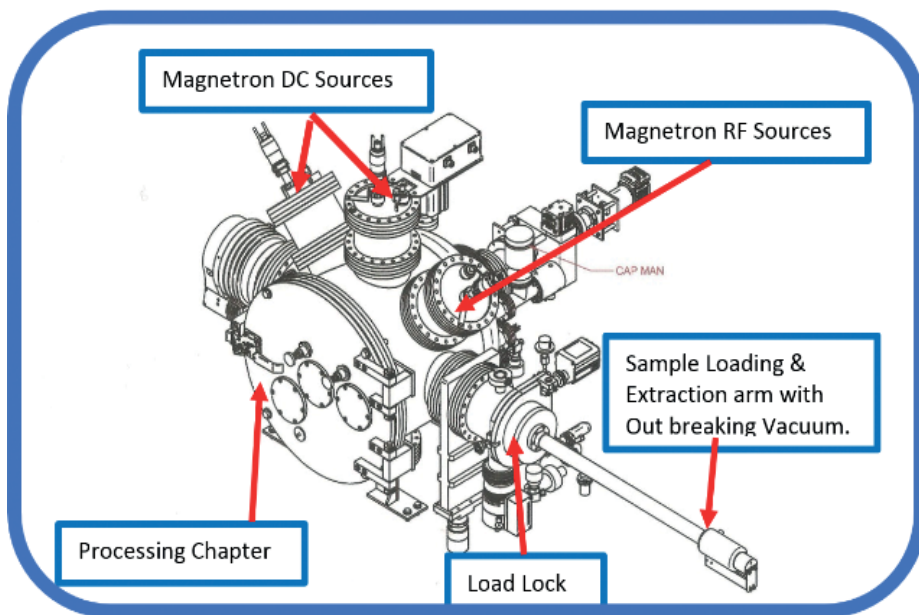
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## 1. Introduction

In TMR, electrical current flows across a barrier of nanometric thin insulator layer between two ferromagnetic metal electrodes when an external magnetic field is applied parallel to the trilayer surface. TMR is one of the few examples of macroscopic quantum mechanical phenomena that have no classical explanation. TMR has a wide array of applications including magnetic random access memory for futuristic quantum computer and ultrasensitive sensors [1]. TMR is the basic building block of magnetic tunnel junctions (MTJs); specifically Fe/MgO/Fe TMR having one of the highest magnetoresistance (MR) ratio is of current interest as evidenced by

several studies of its magnetic properties [2–7]. Theory predicts several thousand percent MR ratio for Fe/MgO/Fe TMR trilayers when an interface is modeled to possess an abrupt change from Fe to MgO without any diffusion at the interface [3]. When diffusion at the interface is introduced in the theoretical modeling by way of interface oxidation, the MR ratio dropped to 1000% [2]. First-principle modeling showed that diffusion at the interface plays a major role in decreasing the calculated value of the MR ratio to the extent when only 16% of Fe is replaced by Mg and vice versa; the MR ratio agreed with experimental values for higher values of the insulator (MgO) thickness [4]. The noncollinear nature of the atomic moments with the bulk magnetization at the interface of Fe and MgO will perhaps affect the net anisotropy, or it may be a spin scattering locality [5]. Experimental measurements showed that single-crystal Fe/MgO/Fe has an MR ratio of 180% at room temperature and 247% at 20 K [6]. It was also shown experimentally that for crystalline MgO (001), the barrier between the electrodes resulted in a 220% MR ratio at room temperature [7]. The symmetry of electronic wave functions plays a paramount role in coherent spin-polarized tunneling, which gives rise to enormous TMR effects. Shape anisotropy plays an important role in coherent spin-polarized tunneling of electrons across the insulator barrier; thus, switching the geometry of TMR from planar films to nanowires will increase the coherence thus increasing the MR ratio [8, 9]. The importance of shape anisotropy in ferromagnet/insulator/ferromagnet trilayer structures in particular for Fe/MgO/Fe and the effect of encapsulation of a TMR by carbon tubes are discussed in this chapter based mainly on magnetic measurement results and subsequent interpretations.

TMR-based magnetic tunnel junctions are theoretically expected to exhibit an extremely high MR ratio due to coherent tunneling [2]. When the coherency of electron wave functions is conserved during tunneling, only conduction electrons whose wave functions are totally



**Figure 1.** Magnetron sputtering tool with 3 DC/1 RF sources and a load-lock.

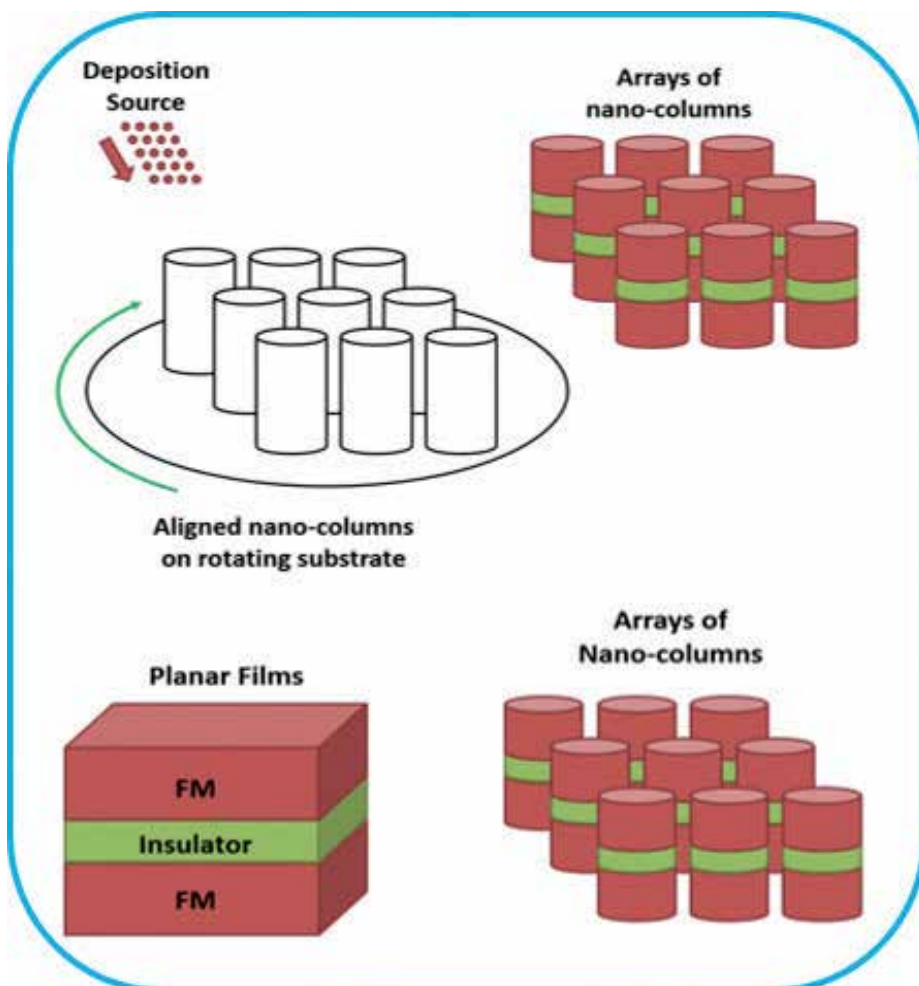


symmetrical with respect to the barrier-normal axis are connected to the electronic states in the barrier region and have significant tunneling probability. The MR ratio is defined as follows:

$$\text{MR ratio} = (R_{\text{ap}} - R_{\text{p}}) / R_{\text{p}}$$

where  $R_{\text{p}}$  and  $R_{\text{ap}}$  are the tunnel resistance when the magnetizations of the two electrodes are aligned in parallel and antiparallel, respectively (**Figure 1**).

A TMR system reveals unique properties with attractive effects for technological applications. Besides giant and tunneling magnetoresistance, it presents other remarkable effects such as antiferromagnetic exchange coupling, oscillatory behavior of exchange coupling and biquadratic exchange couplings. Nanometric ultrathin films exhibit an out-of-plane uniaxial surface



**Figure 2.** Schematic of planar TMR and array of TMR structures. Both sets of samples were grown using the same magnetron sputtering deposition tool. The planar films will establish initial growth conditions for arrays of nanocolumns.

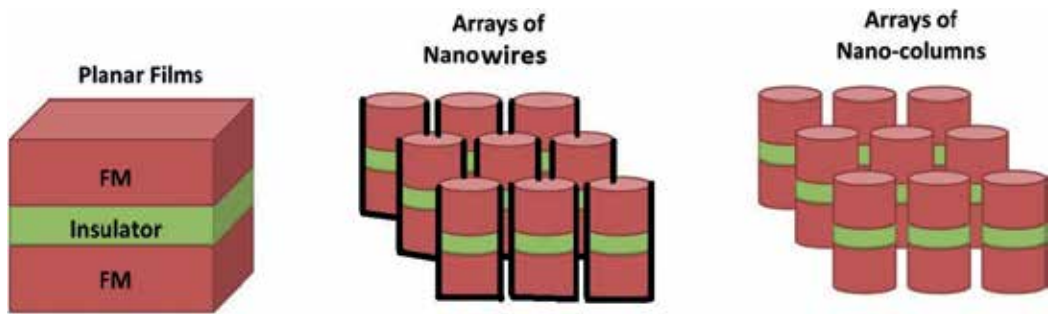
anisotropy sufficient to overcome the demagnetizing field. This feature makes it important for high-density magnetic media, highly sensitive sensors and parts for a quantum computer. This system in addition to its enormous potential for technological applications is an attractive research object in nanomagnetism.

Samples were prepared in the form of nanometric thin films and nanocolumns as shown in **Figure 2**. Nanocolumn arrays were fabricated with different shapes and in-plane orientations by glancing angle deposition (GLAD). This will provide shape anisotropy, which will compete with surface and volume anisotropy. Many unique and fascinating properties have already been demonstrated by nanocolumns synthesized using GLAD, such as superior mechanical toughness, higher luminescence efficiency, enhancement of thermoelectric figure of merit and lowered lasing threshold. Homogeneous nanowires and nanowire networks have been previously used as chemical sensors, field-effect transistors and inverters, photodetectors, light-emitting diodes, lasers and logic gates. Very recently, by altering the compositions of the nanostructures during fabrication, super-lattice nanowire has been demonstrated, which can greatly increase the versatility and application of these building blocks in nanoscale electronic, photonic, and biological applications. Fe nanocolumns were used to synthesize metal-assisted protein crystallization [10]. Possible applications, including thermoelectrics, nanobarcodes, injection lasers and one-dimensional waveguides, could be implemented through these super-lattice nanostructure building blocks. One very important issue associated with these studies is how to assemble one-dimensional nanostructures in an effective and controllable way. GLAD produces columnar structures through the effect of shadowing during film growth, while the substrate rotation controls the shape of the columns.

## 2. Experiment

Nanowires, planar nanometric thin films and nanocolumns of Fe/MgO/Fe were synthesized using magnetron DC/RF sputtering, AXXIS sputtering tool from Kurt J. Lesker Company. Fe target of purity 99.9% and MgO of purity 99.95% were sputtered using high-purity Ar. Nanowires were synthesized by magnetron DC and RF sputtering in the interior cylindrical space of carbon nanotubes by magnetron DC and RF sputtering.

Thermal chemical vapor deposition method (TCVD) was used to grow vertically aligned CNTs on the SiO<sub>2</sub> substrate thus filled with Fe/MgO/Fe in this experiment [11]. TCVD involves exposing silica structures to a mixture of ferrocene and xylene at 770°C for 600 s. In TCVD, the furnace is pumped down to about 200 mTorr in an argon bleed, and then, the temperature is raised to 770°C. The solution of ferrocene dissolved in xylene of concentration about 0.01 g/ml and was preheated in a bubbler to 175°C and then passed through the tube furnace. The furnace is cooled down to room temperature, and tips of the CNTs were exposed to plasma to open the nanotubes for the next procedure. The open-ended CNT tips were filled with Fe/MgO/Fe using DC magnetron for the Fe and RF sputtering method for the insulator (MgO) at a substrate temperature of 100°C. This substrate temperature, 100°C, yielded in planar samples the highest value of coercive field compared to several other synthesis substrate temperatures [12].



**Figure 3.** Schematic of planar TMR and array of TMR structure.

Nanowires were synthesized by magnetron DC and RF sputtering in the nanometric interior cylindrical volume of carbon nanotubes at a substrate temperature of 100°C.

Nanometric thin films were epitaxially grown on the MgO (100) substrate of dimensions 5 mm × 5 mm × 0.5 mm using magnetron DC and RF sputtering at several temperatures. All substrates were degassed at 350°C in vacuum of 0.1 μTorr for 1800 s, and samples were pre- and post-annealed at a preselected deposition temperature for 1800 s in vacuum. The source substrate distance was kept fixed at 30 cm, and the substrate surface normal was kept at 45° with a line connecting the center of the sample to the center of the target, while being rotated at a constant rate of 20 rpm for uniform deposition. Under these conditions, epitaxial Fe grows on MgO (100) due to a good lattice match of MgO and Fe, and weak interface interaction [13, 14] free standing Fe is formed. The deposition rate for Fe was 0.17 nm/s as calibrated by the deposition time versus thickness measurements for Fe films several hundred nm thick. In my previous research on thin films of Fe/MgO/Fe, several planar samples were synthesized at several substrate temperatures [12]. The film synthesized at 100°C has the highest saturation magnetization.

Nanocolumns of Fe/MgO/Fe were synthesized at a glancing angle of 70°, and other parameters remained the same as in the synthesis of thin film (**Figure 3**).

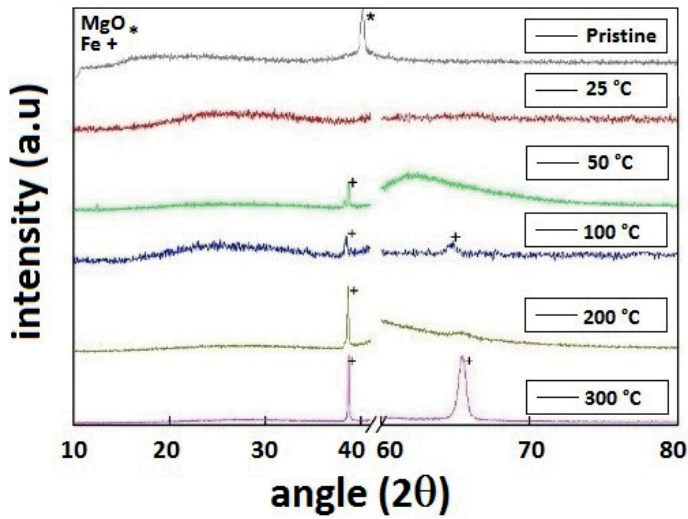
All sets of samples were grown using the same magnetron sputtering deposition tool. The planar films established initial growth conditions for the arrays of nanowires and nanocolumns.

### 3. Structural characterization

Thin film samples of Fe/MgO/Fe on MgO (100) substrates were characterized by XRD (miniflex Rigaku X-ray diffraction of 40 kV/40 mA) using CuK $\alpha$  radiation in  $\theta$ -2 $\theta$  geometry (**Figure 4**).

Surface morphologies of nanowires of Fe/MgO/Fe grown in the interior cylindrical space of CNTs were characterized by SEM/STEM in a previous study [12]. The SEM/STEM scan depicts a uniform composition.

X-ray absorption spectroscopy (XAS) measurements on thin films and nanowires of Fe/MgO/Fe carried out at beamline 4UB at the National Synchrotron Light Source (NSLS) in



**Figure 4.** XRD of Fe/MgO/Fe/MgO (100) synthesized at several substrate temperatures and of pristine MgO (100).

Brookhaven National Laboratory (BNL) by the author revealed the existence of a missing shoulder at the Fe  $L_3$  edge in the nanowire spectrum at 717 eV. At the Fe  $L_2$  edge, the double peaks at 723.5 eV are of equal height for the nanowires and a leading peak in the film's spectra.

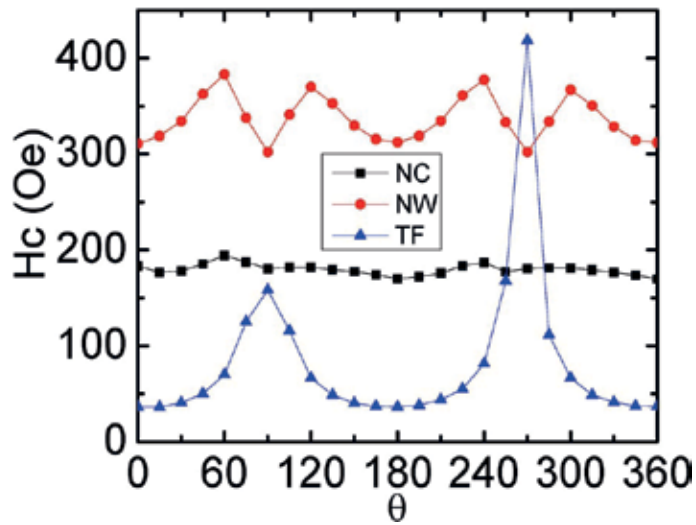
#### 4. Results from magnetic measurements

In a previous work, it was shown that magnetic force microscopy (MFM) of nanowires of Fe/MgO/Fe revealed the presence of stripe domains [12]; stripe domain was totally absent in films, and it was slightly present in nanocolumns. This shows that the moments are completely aligned in plane in case of thin films. In nanowires, the magnetic moment has a significant out-of-plane component.

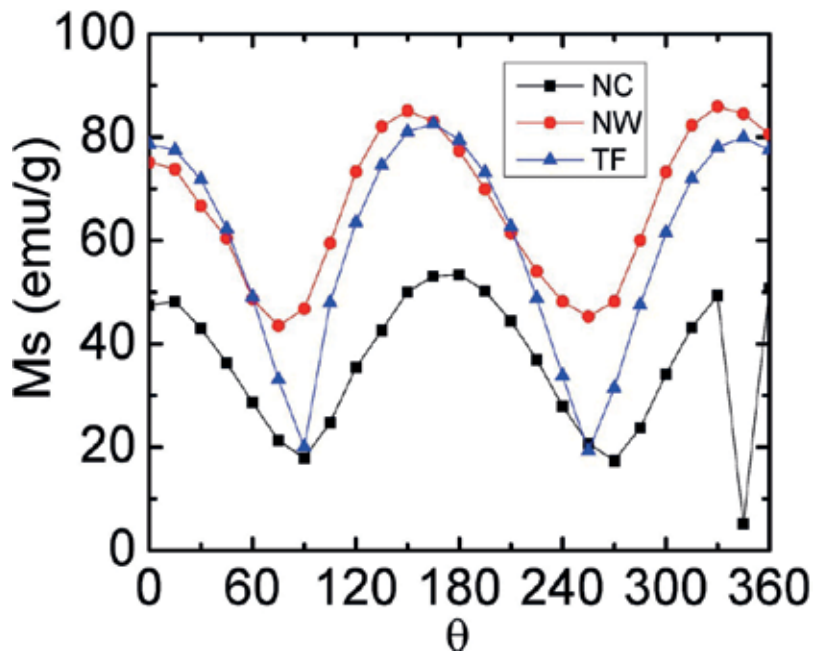
X-ray magnetic circular dichroism (XMCD) measurements of nanowires of Fe/MgO/Fe were carried out at beamline 4UB at NSLS in BNL [12]. Background-corrected XMCD signal shows that the nanowires' XMCD signal at the Fe  $L_3$  and  $L_2$  edges is larger [12]. Moreover, a switching between the two edges occurs, and at around 712 eV of photon energy, the intensity of the nanowires' XMCD is smaller [12].

Vibrating sample magnetometer (VSM) measurements were carried out using Vector Magnetometer Model 10 VSM system from MicroSense equipped with 3 T electromagnet (Figures 5–7). In these figures, the coercive field, saturation and remanent magnetization values of thin films, nanowires and nanocolumns of Fe/MgO/Fe synthesized at the substrate temperature of 100°C versus the angle between the applied magnetic field and the surface normal are shown.

In Figure 5, the coercive field ( $H_c$ ) of nanocolumns (NC), nanowires (NW) and thin films (TF) of Fe/MgO/Fe at several angles between the applied field and the sample surface varying

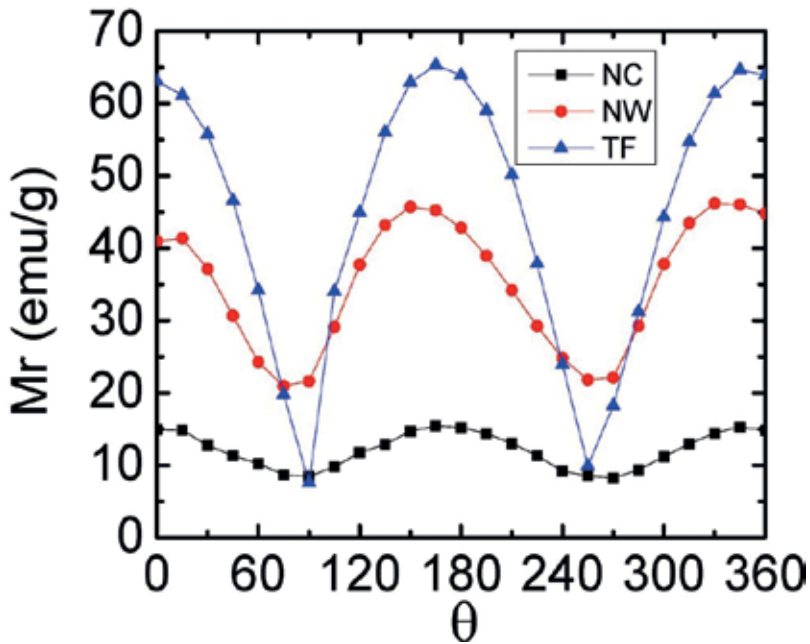


**Figure 5.** Coercive field ( $H_c$ ) of nanocolumns (NC), nanowires (NW) and thin films (TF) of Fe/MgO/Fe at several angles between applied field and the sample surface varied between 0 and 360°.



**Figure 6.** Saturation magnetization ( $M_s$ ) of nanocolumns (NC), nanowires (NW) and thin films (TF) of Fe/MgO/Fe at several angles between applied field and the sample surface varied between 0 and 360°.

between 0 and 360° is depicted. The coercive field value of nanowires is higher than both nanocolumns and thin films. At an angle of 270°, there is an outlier value for thin films that is higher than both nanowires and nanocolumns value of  $H_c$  at 270°. The  $H_c$  values of nanowires



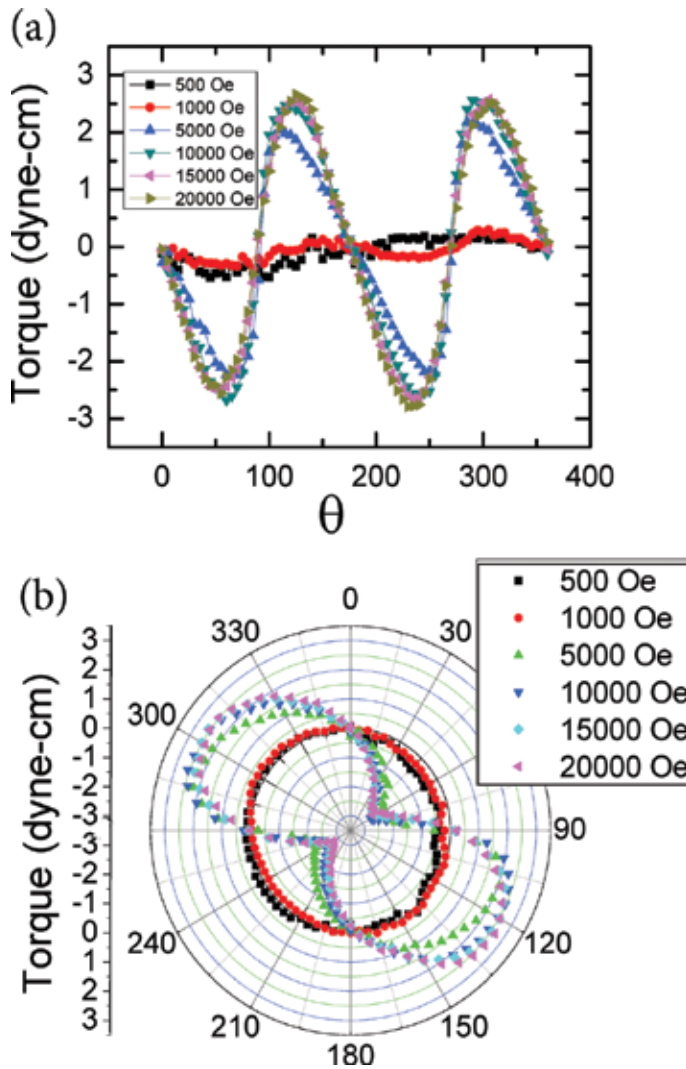
**Figure 7.** Remanent magnetization ( $M_r$ ) of nanocolumns (NC), nanowires (NW) and thin films (TF) of Fe/MgO/Fe at several angles between applied field and the sample surface varied between 0 and 360°.

oscillate with a period of 90°, whereas those of thin films oscillate with a period of 180°. For nanocolumns, the coercive field values are constant at all angles. The in-plane value of the coercive field of nanowires is higher by 754% than planar thin films.

Saturation magnetization ( $M_s$ ) of nanocolumns (NC), nanowires (NW) and thin films (TF) of Fe/MgO/Fe at several angles between the applied field and the sample surface varying between 0 and 360° is depicted in **Figure 6**. The saturation magnetization of all three oscillates with a period of 180°. The saturation magnetization of nanowires is slightly higher than nanocolumns and thin films at all angles.

Remanent magnetization ( $M_r$ ) of nanocolumns (NC), nanowires (NW) and thin films (TF) of Fe/MgO/Fe at several angles between the applied field and the sample surface varying between 0 and 360° is shown in **Figure 7**. The remanent magnetization of all three oscillates with a period of 180°. The remanent magnetization of thin films is higher than nanowires and nanocolumns at all angles except at 90 and 270°.

Magnetic torque measurements for nanocolumns of Fe/MgO/Fe synthesized at 100°C for several applied fields were carried out using the EV7 torque magnetometer (TMM) system equipped with a 2 T electromagnet (**Figure 8**). Torque magnetometer measurements of thin films and nanowires results from the previous study show a similar trend such as a twofold symmetry [12]. TMM measurements of nanocolumns of Fe/MgO/Fe synthesized at 100°C for several applied fields are shown in **Figure 8(a)** and in the polar plot in **Figure 8(b)**. TMM measurements for planar film and nanowires of Fe/MgO/Fe both synthesized at 100°C were

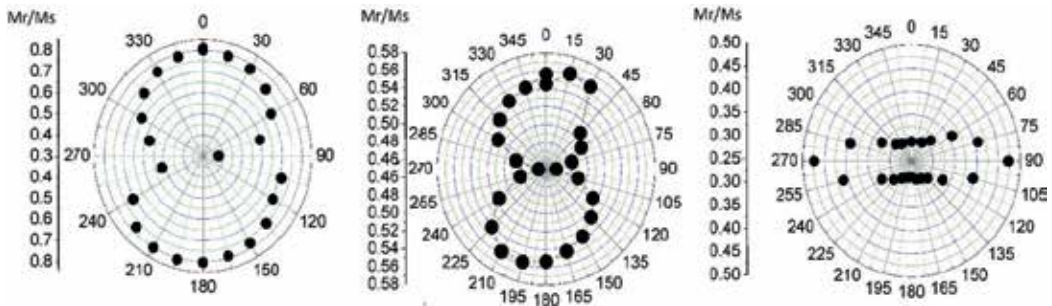


**Figure 8.** (a) TMM measurements of nanocolumns of Fe/MgO/Fe at several fields while the angle between applied field and the sample surface rotate between 0 and 360°. (b) The same figure in part a in polar plot.

shown to have pronounced twofold symmetry in the rotational hysteresis loop that appeared for an applied field of 5000 Oe and higher [12]. Here, it is shown for the first time that nanocolumns also show pronounced twofold symmetry in the rotational hysteresis loop for an applied field of 5000 Oe and higher (**Figure 8**). Torque on the film is 20 times higher than on nanowires for the same applied field [12]. **Figure 8** shows that TMM measurements for nanocolumns in the polar plot along equivalent crystallographic direction yields torque on the nanocolumns are 10 times higher than on nanowires for the same applied field.

**Figure 9** depicts  $M_r/M_s$  ratio measured using VSM at several angles with respect to an applied field for thin films, nanowires and nanocolumns of Fe/MgO/Fe synthesized at 100°C. External



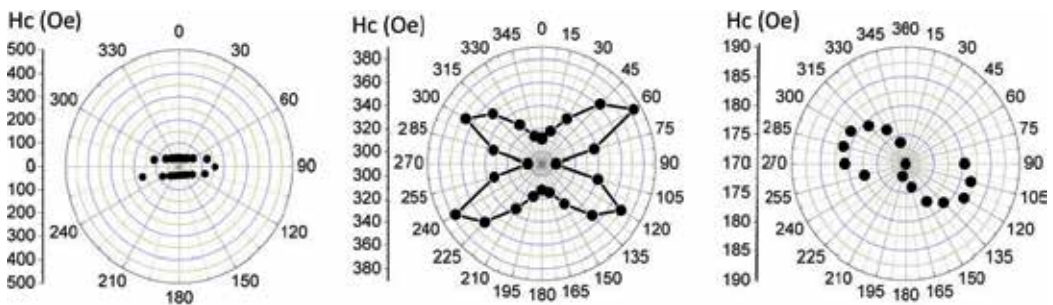


**Figure 9.** (a)  $M_r/M_s$  measurements of thin films Fe/MgO/Fe at several fields. (b)  $M_r/M_s$  measurements of nanowires of Fe/MgO/Fe at several fields. (c)  $M_r/M_s$  measurements of nanocolumns of Fe/MgO/Fe at several fields.

magnetic field applied along equivalent crystallographic directions did not produce equivalent hysteresis loops. This is obvious when comparing  $M_r/M_s$  ratio obtained for symmetric orientations, every  $15^\circ$  from  $0$  to  $360^\circ$  as shown in **Figure 9**.  $M_r/M_s$  ratio extracted from VSM measurements at every  $15^\circ$  between  $0$  and  $360^\circ$  for planar film, nanowires and nanocolumns of Fe/MgO/Fe synthesized at  $100^\circ\text{C}$  is shown in **Figure 9**. In all three cases, a twofold symmetry is observed.

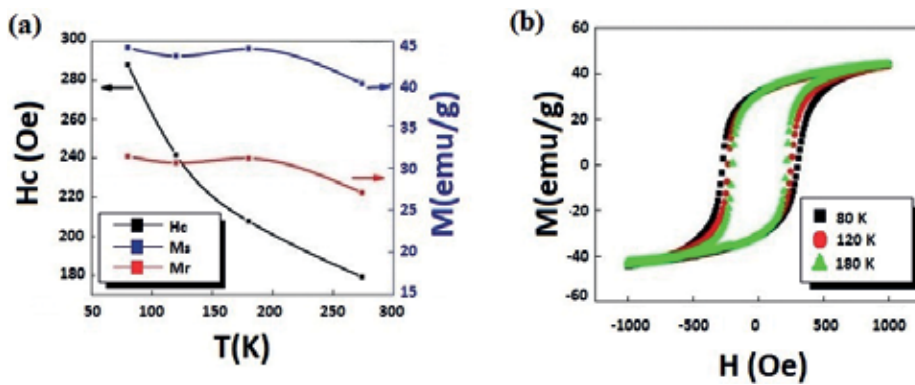
**Figure 10** depicts a coercive field measured using VSM at several angles with respect to an applied field for thin films, nanowires and nanocolumns of Fe/MgO/Fe synthesized at  $100^\circ\text{C}$ . **Figure 10** depicts a coercive field measured using VSM at several angles with respect to an applied field for thin films, nanowires and nanocolumns of Fe/MgO/Fe synthesized at  $100^\circ\text{C}$ . A strong dependence on an angle was observed in the films than in the nanowires. Coercive field ( $H_c$ ) value extracted from VSM measurements at every  $15^\circ$  between  $0$  and  $360^\circ$  for planar films, nanowires and nanocolumns of Fe/MgO/Fe synthesized at  $100^\circ\text{C}$  is shown in **Figure 10**. In all three cases, different symmetries are observed. Nanowires depict a fourfold symmetry, whereas a well-defined twofold symmetry is observed in nanocolumns. The conspicuous symmetry difference in nanowires could be due to shape anisotropy and also due to the hybridization that occurs between the  $\pi$ -electronic states of carbon and 3d bands of the Fe surface.

**Figure 11** depicts temperature-dependent magnetization measurements of planar nanometric thin films of Fe/MgO/Fe/MgO (100) synthesized at  $100^\circ\text{C}$ . The planar thin film with the highest



**Figure 10.** (a)  $H_c$  measurements of thin films of Fe/MgO/Fe at several fields. (b)  $H_c$  measurements of nanowires of Fe/MgO/Fe at several fields. (c)  $H_c$  measurements of nanocolumns of Fe/MgO/Fe at several fields.





**Figure 11.** (a) Low-temperature VSM measurements of Hc, Ms and Mr measured at several temperatures for thin films of Fe/MgO/Fe synthesized at 100°C. The lines are drawn connecting the points as a guide. (b) Hysteresis loops of Fe/MgO/Fe/MgO (100) thin film synthesized at 100°C measured at several low-temperature values.

value of Hc as shown in **Figure 11(a)** is the sample synthesized at 100°C. Low-temperature VSM measurements of thin films of Fe/MgO/Fe synthesized at 100°C are shown in **Figure 11**; as expected, all three magnetization parameters Hc, Mr, and Ms increased as temperature is lowered; more pronounced increase was observed in Hc. As shown in **Figure 11(a)**, the value of Hc increased by 61%, Mr increased by 16% and Ms increased by 11% at 80 K compared to their room temperature values, respectively. **Figure 11(b)** depicts hysteresis loops of the Fe/MgO/Fe planar film at low temperatures.

## 5. Discussion

As shown in **Figure 5**, the in-plane coercive field (Hc) of nanowires is higher than thin films' Hc by 754%, and nanocolumns' Hc is higher than thin films' Hc by 403%. These higher values of Hc for nanowires are due to magneto crystalline shape anisotropy since both nanowires' and nanocolumns' Hc values are higher than Hc of planar films Magneto crystalline anisotropy according to density functional theory is due to change in the relative occupancy of the 3d orbitals of Fe atoms at the interface of Fe/MgO [15]. The reason nanowires' Hc is higher than nanocolumns' Hc is encapsulation by carbon nanotubes and subsequent interaction of C atoms with Fe atoms. This interaction between C and Fe atoms is between the  $\pi$ -electronic states of carbon and 3d bands of the Fe surface. First-principle calculation predicts weak interaction between the Fe layer and the MgO substrate making the Fe film to act as a free-standing Fe monolayer ( $3.10 \mu_B$ ), with enhanced magnetic moment [13]. Mössbauer measurement has also shown higher hyperfine field attributed to the interface region between the epitaxial Fe and MgO layers [16]. This study on the hybrid interface between carbon-based organic molecules and ferromagnetic surfaces is very important in the development of wearable spintronics and environmentally friendly sensors based on organic spintronics [17]. Nanowires of  $\alpha$ -Fe synthesized inside alumina templates and single nanowires inside dense nickel nanowire arrays [18, 19] depict a B-H loop that narrows when the field is perpendicular

to the nanowire axis; this deformation in the B-H loop indicates small dipole interactions. The small dipole interactions in these systems are due to the large spacing between the nanowires. In this chapter, nanowires of Fe/MgO/Fe for all orientations of the applied field depict B-H loops that have the same width without narrowing deformation in the B-H loops indicating high dipole interactions. The reason for this high dipolar interactions is the densely grown nanowires with close proximity to each other.

## 6. Conclusions

In conclusion, this chapter reports on the magnetic study of Fe/MgO/Fe of planar nanometric thin films, nanocolumns and nanowires encapsulated inside carbon nanotubes. Deposition conditions in particular geometry and most notably encapsulation influence both structural and magnetic properties of Fe/MgO/Fe TMR. Nanowires of Fe/MgO/Fe showed superior magnetic properties than planar thin films. The in-plane value of the coercive field of nanowires is higher by 754% than planar thin films.

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# Diamond Nanowire Synthesis, Properties and Applications

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Additional information is available at the end of the chapter

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## Abstract

Due to the superior hardness and Young's modulus, biocompatibility, optical and fluorescence nanodiamond seems to be outstanding among carbon nanomaterials. In this footpath, the development of diamond nanowires (DNWs) is known to be a significantly innovative field due to their diverse applications such as sensors, semiconductors, and electrochemical utilities. Compared to carbon nanotubes, DNWs theoretically have energetic and mechanically viable structures. However, DNW synthesis in a reproducible way is still a challenging task. In fact, most of the DNWs can be successfully synthesized by chemical vapor deposition (CVD) and reactive-ion etching (RIE) techniques. By contrast, solution-based DNW synthesis has also emerged recently. A detailed study on DNW structures may help the emerging researchers to direct toward diverse applications. In this chapter, we comprehensively presented the up-to-date applications of DNWs along with their synthesis, structures and properties.

**Keywords:** diamond nanowires, nanowire synthesis, reactive-ion etching, chemical vapor deposition, semiconductors, electrochemical studies, sensors

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## 1. Introduction

The development of nanowires for the effective applications still seems to be interesting and a challenging task [1–3]. So far, many kinds of nanowires have been constructed from various sources such as metals, polymers, inorganic-organic hybrid systems and semiconductor materials [4–7]. Those nanowires have the diverse mechanical, electrical, thermal and multi-functional properties [8, 9]. Further, nanowires can be effectively utilized in electrical transport studies, electrochemical studies and solar energy conversions [10, 11]. In this footpath,

diamond nanowires (DNWs) are also known as a material of extremes, in which, its properties are exceptional in terms of band gap, electron affinity, chemical inertness, resistance to particle bombardment, hardness and thermal conductivity [12]. Moreover, upon tuning the n- or p-doping on DNWs, the diverse field emission, semiconductor and sensory applications can be attained. The diverse applications may be attributed to the lattice structures of those DNWs as well as the carbon-carbon bond or existence of  $sp^2/sp^3$  ratio [13, 14]. However, the presence of defects such as nitrogen vacancy center ( $NV^-$ ) and impurity channels also enhances the benefits of DNW-based extensive research [15]. As a consequence of those defects or impurity channels, DNWs have the color centers, which enable their photonic applications such as single-photon emission [16]. Moreover, the toughness and wear resistance of DNWs may be enhanced due to the hindering of dislocation movement by the impurities.

Attributed to the utilities of DNWs, numerous reports on their synthesis have been available so far. However, the synthesis of DNWs was claimed to be a low probability event in terms of reproducibility, which makes it as a thought-provoking task. Therefore, researchers tend to develop the suitable methods to grow the DNWs due to its potential benefits in ultraviolet (UV) light detectors and emitters [17, 18] radiation particle detectors [19], high-speed and high-power field effect transistors [20], field emission sources [21, 22], position-sensitive biochemical substrates [23] and room temperature-stabilized high-efficiency single-photon emitters [24]. So far, DNWs were grown from (1) Plasma-assisted reactive-ion etching process (RIE) with mask and maskless techniques; (2) chemical vapor deposition (CVD) techniques with diverse templates assistance, plasma enhancement, catalyst assistance, and so on; (3)  $sp^2$ -hybridized carbon and  $sp^3$ -hybridized diamondoids (post-treatment of multiwalled carbon nanotubes (MWCNTs) with hydrogen plasma and from fullerenes). Similar to the above reported techniques on DNW growth, the development of hybrid graphene-DNWs (G-DNWs) also attracted the modern scientific research because of their diverse conductivity or semiconductor applications [12]. However, such nanowire (DNWs and G-DNWs) growth is still a challenging task; hence, an overview on its synthesis, structures and applications is required.

In this chapter, we tend to present a brief report of the diamond nanowires, with discussions on DNW synthesis along with their structures, properties and applications. Wherein, the important synthetic pathways to grow the DNWs are pinpointed. Then, the comprehensive discussions on the structures and properties of the DNWs are derived from the available theoretical and experimental reports. Subsequently, the applications of those DNWs in diverse fields are summarized.

## 2. Synthetic strategies for DNW growth

### 2.1. Plasma-assisted reactive-ion etching

Reactive-ion etching (RIE) is an etching technology applied in micro- or nanofabrication, which may apply the dry etching than that of wet routes [25]. Wherein, plasma has been used to remove material deposited on substrates. A schematic diagram presented in **Figure 1** represents a typical RIE setup.

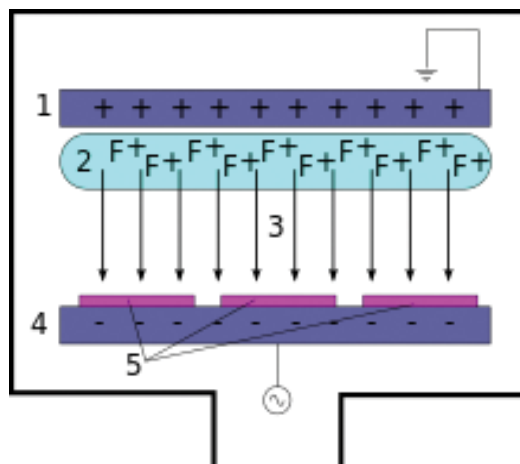
In general, the plasma is generated by an electromagnetic field under vacuum. Then, plasma produces the high-energy ions, which react with the surface of the sample to provide the desired nanostructures. However, its output also depends on the parameters such as power density, frequency, pressure, dc bias, gas composition, flow rate and so on. In 1997, Shiomi et al. informed the DNW growth by means of plasma-assisted RIE technique [26]. Thereafter, RIE technique has been widely applied for the growth of DNWs. However, later on, DNWs were grown up either with the support of mask or maskless processes.

### 2.1.1. RIE with masks for DNW growth

The planar diamond films can be etched to obtain the DNWs. This also was attained with the support of several masks such as (1) metal nanoparticles mask, (2) oxide nanoparticles mask, and (3) diamond nanoparticles mask. However, the size and density of the developed DNWs depends on the nanoparticles that are used in masks, in which the size of those nanoparticles may lie in few nanometers.

(A) RIE with metal mask: After Shiomi's report [26] on DNW growth by using Al mask and oxygen plasma RIE, various columnar diamond nanowires with 300-nm length and 10-nm width have been constructed through etching CVD polycrystalline diamond films in O<sub>2</sub> plasma [27, 28]. In this light, Liao et al. effectively developed the single-crystal diamond pillar like DNWs by Al-masked RIE technique [29]. However, Al-masked RIE method led to provide polycrystalline DNWs, hence having the disadvantages such as the presence of grain boundaries, impurities and large stresses in the films. Apart from Al-mask, other kinds of metals such as Mo, Ni, Fe and Au were also been utilized to develop doped or undoped DNWs [30–32]. Li and Hatta explored the effect of those metal masks, for the development of DNWs [33].

(B) RIE with oxide nanoparticles mask: Fujishima et al. successfully developed the DNWs through reactive-ion etching supported by oxygen plasma consisting of two-dimensional



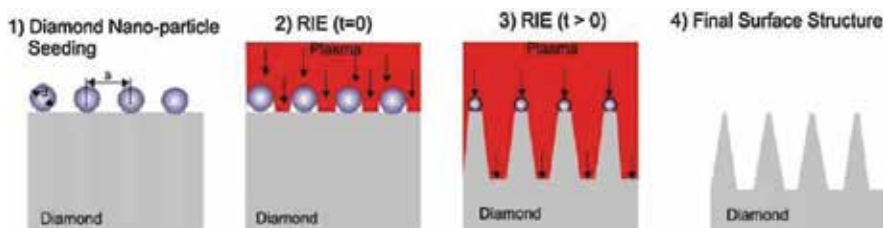
**Figure 1.** A diagram of a common RIE setup. An RIE consists of two electrodes (1 and 4) that create an electric field (3) meant to accelerate ions (2) toward the surface of the samples (5) ([https://en.wikipedia.org/wiki/Reactive-ion\\_etching](https://en.wikipedia.org/wiki/Reactive-ion_etching)).

(2D) arrays of monodisperse solid  $\text{SiO}_2$  particles as masks [34]. Wherein, on the planar diamond surface, fine  $\text{SiO}_2$  particles are packed at high density [35] and oriented layers over a wide surface area by water evaporation and lateral capillary forces [36]. Then, reactive-ion etching (RIE) was carried out with oxygen plasma through the  $\text{SiO}_2$  arrays for 5–120 min in a plasma-etching machine with a radio frequency (RF) generator. Lastly, the  $\text{SiO}_2$  particles were detached from the diamond by  $\text{HF-HNO}_3$  treatment, which afford the DNW arrays. After this report, Hausmann et al. also elaborate the DNW synthesis by  $\text{Al}_2\text{O}_3$  mask [37], which found to be the most etch resistant. Hence, these flowable oxide masks are demonstrated to be a suitable etching mask for the construction of ordered arrays of DNWs.

(C) RIE with diamond nanoparticles: As shown in **Figure 2**, by using diamond nanoparticles as a mask, Yang et al. described the vertically aligned DNW synthesis from boron-doped single-crystalline CVD diamond films [38]. Initially, a microwave-assisted CVD technique is used to grow the boron-doped (P-type) diamonds with smooth surfaces by homoepitaxy on Ib diamond substrates. Next, diamond nanoparticles etching mask with well-defined size and quality is deposited. The size of those diamond nanoparticles (dissolved in water by ultrasonication to form a pseudostable suspension) lies between 8 and 10 nm. Thereafter, to seed diamond nanoparticles on the surface of a diamond substrate, the planar diamond film is immersed into the suspension and sonicated. After deposition, RIE in an  $\text{O}_2$  (97%)/ $\text{CF}_4$  (3%) gas mixture is applied to afford the vertically aligned DNWs, which has been utilized in DNA sensing [39].

### 2.1.2. Maskless RIE for DNW growth

The above mask methods have certain limitations and hence become unfavorable for large-scale fabrication. Therefore, researchers tend to develop uncomplicated methods to remove some masks by additional chemical or physical processes that can grow the DNWs. In this way, Fujishima et al. described the synthesis of heavily B-doped DNWs (the boron doping level is  $2.1 \times 10^{21} \text{ B cm}^{-3}$ ) through oxygen plasma without any additional mask [40]. Here, boron atoms on the diamond act as the mask during plasma etching, hence avoiding the deposition or removal of mask by additional steps. In detail, during the etching step, those boron oxide species are removed collectively with carbon atoms, and then they appear to redeposit near the tops of the DNWs, which may serve as an etching mask. This straightforward maskless method has been widely used for the synthesis of DNWs in recent times.



**Figure 2.** Schematic illumination of the fabrication of vertically aligned diamond nanowires using a top-down technology and using diamond nanoparticles as the etching mask. Reproduced with permission from [38].



## 2.2. Chemical vapor deposition (CVD) for DNW synthesis

Among the available effective methods for DNW synthesis, CVD technique is one of the promising processes utilized extensively [41]. This simple evaporation technique has been used to grow the elemental or oxide nanowires in an appropriate atmosphere. However, CVD process can be applied by means of template assistance or template-free ones as follows.

### 2.2.1. Template-assisted CVD methods for DNW growth

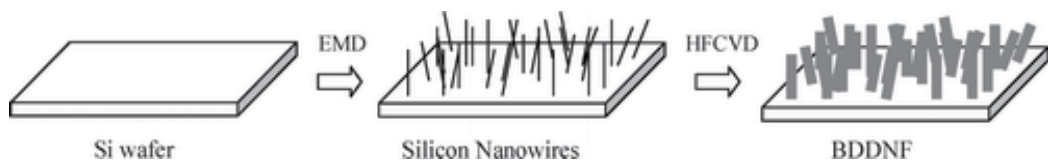
This is a convenient method to generate the 1D nanostructures and capable of producing nanostructures with exclusive structures, morphologies and properties [42–44]. Wherein, the template assists as a scaffold on which other materials with similar morphologies are produced. Moreover, they can be at nanoscale within mesoporous alumina or polycarbonate membranes. The following templates were applied so far to grow DNWs: (A) nanowires templated with CVD and (B) anodic aluminum oxide (AAO) templated CVD.

(A) Nanowires templated CVD for DNW growth: This method has two steps such as (1) synthesis of various nanowire templates and (2) conformal coating of nanowires templates with nanodiamond, which may lead to form the DNWs by CVD technique. Firstly, May et al. explored the microdiamond coatings into tungsten wires through CVD to construct the DNWs [45]. Afterward, several researchers applied this technique by using silicon, carbide, tungsten, titanium and copper nanowires as a template for DNW synthesis. **Figure 3** illustrates such B-doped DNW growth by Si nanowires template [46].

(B) Anodic aluminum oxide (AAO) templated CVD: Masuda and coworkers illustrated the growth of polycrystalline DNWs and diamond-like carbon (DLC) nanotubes by means of anodic aluminum oxide templates in microwave plasma-assisted CVD [47], in which those alumina templates [48] were prepared by electrochemical anodization of an aluminum sheet. Alumina templates possessing the holey nanoporous membranes and nucleated with 50- and 5-nm diamond particles led to the formation of DNWs. In this process, the deposition of diamond through the alumina pores yields a continuous film and supports the formation of nanostructures. Finally, by immersing in concentrated phosphoric acid at 250°C, those nanostructures can be released from the alumina.

### 2.2.2. Template-free CVD techniques for DNW growth

More recently, template-free CVD methods for DNW synthesis attracted the scientific community. Those template-free CVD techniques are (A) microwave plasma-enhanced CVD



**Figure 3.** Fabrication of B-doped DNWs by Si-nanowires templates with CVD. Reproduced with permission from [46].

(MPCVD), (B) hot cathode direct current plasma CVD (HCDC-PCVD), and (C) catalyst-assisted atmospheric pressure CVD. Detailed information of the above CVD methods is presented subsequently.

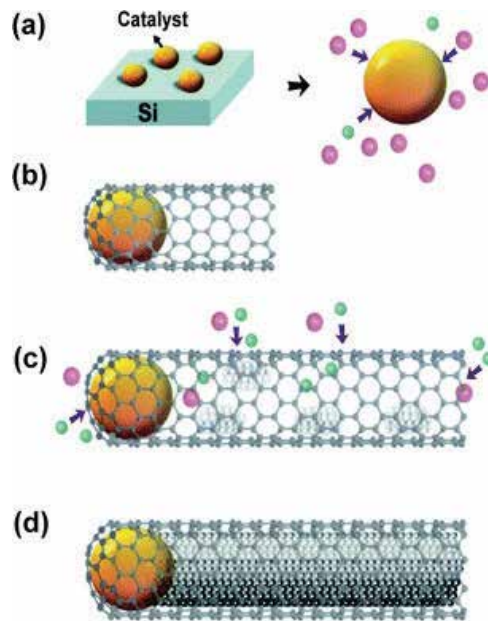
(A) Microwave plasma-enhanced CVD (MPCVD): At first, Valsov et al. presented the synthesis of hybrid graphite-diamond nanowires (G-DNWs) over an ultrananocrystalline diamond (UNCD) film by using MPCVD technique [49]. Afterward, Shang and coworkers described the development of ultrathin diamond nanorods (UDNRs) by this method [50]. However, the incorporation of  $N_2$  becomes essential as demonstrated by recent reports [51, 52], wherein the incorporation of  $N_2$  enhances the electrical conductivity through tuning the  $sp^2/sp^3$  carbon ratio. Hypothetically, the introduction of nitrogen into plasma may motivate the formation of molecular CN species, thereby generating favorable conditions for an increase in the grain size as well as the formation of ID diamond nanostructures. In general, DNWs synthesized through MPCVD technique show good electrochemical properties due to the rise of  $sp^2$  content, new C-N bonds at the grains and an escalation in the electrical conductivity at the grain boundaries [53].

(B) Hot cathode direct current plasma CVD (HCDC-PCVD): This is an innovative technique for the deposition of nano- and micro-crystalline diamond films with uniformity over a large area and with a high growth rate. Here, the cathode made up of a tantalum disc linked to a water-cooled cylindrical copper block, water-cooled copper block anode and a nonpulsed-type dc power source is used. From this technique, Zeng et al. explored the formation of DNRs along with (111) diamond microcrystals and (100) diamond microcrystals on Si substrates [54].

(C) Catalyst-assisted atmospheric pressure CVD: Apart from the previously mentioned high-temperature methods assisted by plasma or energy radiation, the production of long single-crystalline DNWs by conventional thermal CVD methods has become essential because of their potential benefits. In this light, Hsu et al. described the growth of DNWs by means of CVD without plasma or energy sources and at atmospheric pressure [55]. Here, methane and hydrogen were flowed into the Fe catalyst solution, which was dispersed on an Si substrate at  $900^\circ\text{C}$ . Then, pure hydrogen was run through the quartz tube chamber (at 200 sccm as rate) without pumping the residual methane. Subsequently, the temperature lowers down to an ambient condition at a rate of  $1.2^\circ\text{C min}^{-1}$  for 12 h. This method will produce the uniform long and thin DNWs with a diameter of 60–90 nm. Importantly, in this process, hydrogen plays a vital role in the formation of DNWs via  $sp$ - and  $sp^2$ -hybridized bonds transformation into  $sp^3$ -hybridized atoms [56]. **Figure 4** demonstrates the possible vapor-liquid-solid (VLS) mechanism for the growth of DNWs by this method [57]. This technique supports the exceptional utilization CVD with the support of transition metal catalyst such as Fe and also envisioned the applicability of CVD at atmospheric pressure.

### 2.3. DNW growth from $sp^2$ carbon and $sp^3$ diamondoids

Attributed to the importance, the conversion of  $sp^2$  graphite carbon to  $sp^3$  diamond crystals remains to be a challenging task over many years for which high pressures and high temperatures are required. Recently, researchers developed few methods for such transformation;

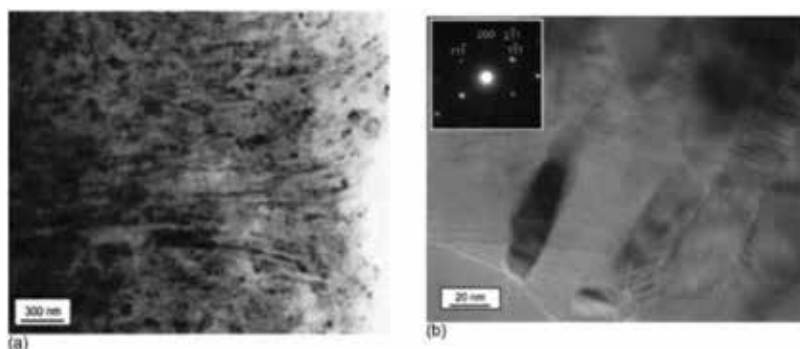


**Figure 4.** Schematic diagram showing a possible formation process of diamond nanowires: (a) catalytic particles are formed from the evaporated or deposited thin film on the substrate as the temperature rises; (b) carbon-containing radicals reach the surface of the catalysts, leading to the growth of either a diamond stud or a graphitic tube via the VLS mechanism. The size is determined by the catalyst. (c) Hydrogen assists in the growth process by either preferentially etching  $sp^2$  and  $sp^3$  bonds or transforming them into  $sp^3$  bonds. With the higher capillary pressure at smaller diameters, the diamond phase could be more stable but the capillary pressure rapidly decreases with diameter, leaving the shell more stable in the graphitic phase. (d) The grain growth, boundary healing and structural reorganization take place in the slow-cooling period in the presence of a pure hydrogen flow. Reproduced with permission from [57].

however, it is still a challenging task. The following are few examples of transformation of  $sp^2$  graphite carbon to  $sp^3$  carbon, which led to the formation of DNWs: (A) DNW synthesis by hydrogen plasma post-treatment of multiwalled carbon nanotubes, (B) DNW growth from fullerenes and (C) DNWs from diamondoids.

(A) DNW synthesis by hydrogen plasma post-treatment of multiwalled carbon nanotubes (MWCNTs): In 2005, Sun and coworkers presented this simple method for the growth of DNWs from carbon nanotubes (CNTs) via hydrogen plasma post treatment [58]. Impressively, upon extended hydrogen plasma treatment, the DNWs with the diameters of 4–8 nm and with the lengths up to several hundreds were obtained. This work also revealed the TEM of single-crystal DNWs from the MWCNTs after treatment in hydrogen plasma at 1000 K for 20 h. The author proposed the mechanism as clustering, crystallization, growth and faceting, which is similar to the report by Singh et al. [59]. It is also established that the presence of amorphous carbon sheath over diamond nanoparticles and DNWs is responsible for this kind of transformation.

(B) DNW growth from fullerenes: In 2005, Dubrovinskaia and coworkers synthesized a bulk sample of nanocrystalline cubic diamond from fullerene  $C_{60}$ , which have the crystallite sizes of 5–12 nm and a hardlike single-crystal diamond [60]. **Figure 5** represents the TEM of those ADNRs. These nanocrystalline diamonds seem to be highly stable at an elevated temperature



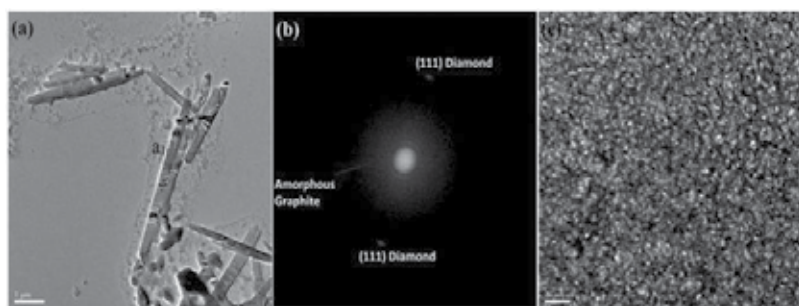
**Figure 5.** (a) Bright-field TEM image of a nanocrystalline aggregate with needle-shaped, elongated crystals diamond nanorods. The crystals can be longer than 1  $\mu\text{m}$ , whereby the needle width is only about 20 nm or less; (b) bright-field image shows a close-up of the elongated crystals. The long edges of the crystals are parallel to the (111) plane, and the needle axes are approximately parallel (211)\*. Reproduced with permission from [60].

and an ambient pressure. In the meantime, they developed the aggregated diamond nanorods (ADNRs) from  $\text{C}_{60}$  by multi-anvil apparatus [61]. Those ADNRs have the diameter of 5–20 nm and have the length of more than 1  $\mu\text{m}$ .

(C) DNWs from diamondoids: Similar to carbon nanotubes and fullerenes, diamondoids may also lead to the formation of DNWs. The 1D diamondoid aggregates confined in CNTs directed to form the DNWs via ‘face-fused’ reaction. However, these transformations of adamantane into DNWs seem to be energetically not feasible. Contrarily, Zhang et al. explored the theoretical and experimental proof for these fusion reactions by diamantane-4,9-dicarboxylic acid transformation to 1D diamond nanowires inside CNTs [62]. In which, the fusion of diamantane-4,9-dicarboxylic acid under the confinement of CNTs yields the DNWs.

#### 2.4. Wet chemical route to synthesis DNWs

Attributed to the applications of DNWs, numerous efforts have been made by the researchers to synthesize them. Among them, wet chemical route seems to be impressive with respect to cost-effectiveness than that of RIE and CVD techniques. But it is also essential to make them with reproducibility and uniformity. To this footpath, recently, our group report the pH-induced electrostatic self-assembly of novel cysteamine functionalized diamond nanoparticles (ND-Cys) to evidence hybrid G-DNW growth [63]. Those G-DNWs are highly stable in respective pH buffers, but if more amount of DI-water is added, the longer nanowires (initially at  $\sim 100 \mu\text{m}$ ) break into small wires/rods (few microns). At pH 6, the width of G-DNWs ranges between 20 and 800 nm and the length lies between 200 nm and hundreds of microns with respect to dispersion concentration. Wherein, the DNW formation was initiated through electrostatic forces within the partially graphitized ND-Cys particles. Next, those partially graphitized ND-Cys particles and defects/impurity channels were further promoted to form the graphene shells on the surface of DNPs and sandwiched between the diamond cores. These G-DNWs show exceptional conductivity due to the presence of defects and impurity channels. **Figure 6** illustrates the TEM image of those nanowires with defect or impurity channels.



**Figure 6.** (a) HR-TEM image of G-DNWs, (b) FT pattern of selected area  $a_1$  representing amorphous graphite along with diamond (111) diffraction pattern and (c) high magnification image of  $a_1$  region representing less perfect graphite layer along with defects or impurity channels. Reproduced with permission from [63].

In this way, with respect to Berman et al. report on metal-induced graphitization of diamond particles [64], metal ions induced G-DNWs formation is also seem to be highly feasible. However, the reproducibility and percentage formation of G-DNWs by this path is still a challenging task. Currently, our group is working on this research to grow the G-DNWs with good reproducibility.

### 3. Structures and properties of DNWs

In order to establish the diverse applications of DNWs, the structure and properties should be elucidated. The properties such as structural stability, mechanical properties, density and compressibility, photon optical mode and electronic structure, thermal conductivity and electrochemical properties play vital role in their applications. Hence, researchers described the experimental and theoretical investigations on the structure and properties of DNWs as follows.

#### 3.1. Structural stability of DNWs

From theoretical investigations, it has been found that dehydrogenated C(111) octahedral nanodiamond surfaces are structurally unstable. However, cuboctahedral structures of nanodiamond may increase the C(100) surface area and become more stable, which also reduce the surface graphitization. In this light, Barnard et al. investigated three kinds of DNWs including dodecahedral, cubic and cylindrical nanowires and found that nanocrystalline diamonds are structurally stable at one dimension [65]. Moreover, they also demonstrate that stability depends on the surface morphology and crystallographic direction of the principal axis of DNWs. In a similar fashion, Tanskanen and coworkers established the structures of polyicosahedral DNWs derived from diamondoids,  $C_{20}H_{20}$ ,  $C_{20}@C_{80}H_{60}$ , and  $C_{20}@C_{80}@C_{180}H_{120}$ . For which they have summarized the HOMO-LUMO gaps, and band gaps via B3LYP calculations [66]. Wherein, the  $C_{20}@C_{80}@C_{180}H_{120}$  structures are energetically favored and the DNWs at 110 direction have the lowest strain energies leading to more stability. This has been experimentally

proved by the stability of DNRs (at 110 direction) synthesized through hydrogen plasma post-treatment of multiwalled CNTs [58], whereas the DNWs at 100 direction seem to be unstable as reported earlier [67].

### 3.2. Mechanical properties of DNWs

Tanskanen et al. described the mechanical properties of DNWs through Poisson's ratios, Young's moduli and shear moduli interrogations [66], which proved that (111) DNWs have the highest Young's moduli than the (110) and (111) DNWs. In this report, they suggest that polyicosahedral DNWs have more strain than that of conventional DNWs. In this way, Guo and coworkers presented the mechanical properties of (001) DNWs by means of molecular dynamics simulations [68] and specified that Young's modulus of those DNWs is lower than those of bulk diamond. Similarly, Jiang et al. explored Young's modulus of DNWs in different crystallographic orientations as a function of cross-sectional area [69]. Wherein, Young's modulus has the sequence of (100), (110), (111) and (112) directions and indicated that those values are lower than the bulk value and increase with its cross-sectional area.

### 3.3. Density and compressibility of DNWs

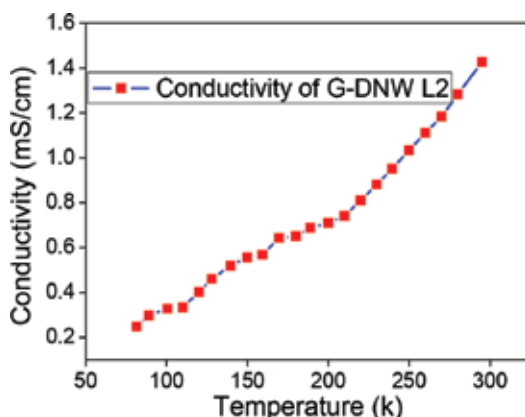
Initially, Dubrovinskaia and Dubrovinsky reported the density of the aggregated diamond nanorods (ADNRs), which were developed from fullerene  $C_{60}$  by multi-anvil apparatus [61]. The X-ray density of ADNRs is about 0.2–0.4% greater than the bulk diamond, which also corresponds to the measured density of  $3.532(5) \text{ g cm}^{-3}$ . The higher density of ADNRs may arise from the outerlayer contraction leading to shortening of the C-C bonds inside the diamond. In this work, they have also evaluated the compressibility of ADNRs by using the third-order Birch-Murnaghan equation of state, wherein they established the >11% lesser compressibility of ADNRs than that of usual diamond.

### 3.4. Phonon optical modes and electronic properties of DNWs

Trejo and coworkers reported the optical phonons and Raman-scattering properties of DNWs by using a local bond polarization model based on the displacement–displacement Green's function and the Born potential [70]. Further, they have also studied the electronic band structure of DNWs through a semiempirical tight-binding approach and compared with density functional theory (DFT) studies. From the calculations, they have concluded that phonons and electrons tend to show a clear quantum confinement signature. Moreover, this study also establishes that during the DNWs width increase, the Raman peak shifts to lower frequencies due to the phonon confinement, as reported by our group [71]. Subsequently, the band gap also decreases as the width of the DNWs increases.

### 3.5. Thermal conductivity and electrochemical properties of DNWs

In general, it is recognized that the thermal conductivity of DNWs may not be incredibly affected by surface functionalization. However, at nanometer scale, dimensions of DNWs may reduce the thermal conductivity than that of bulk diamond as demonstrated by Novikov et al. [72]. In this way, Moreland and coworkers explored that the conductivity of DNW is lower



**Figure 7.** Temperature-dependent conductivity of DNW L2. Reproduced with permission from [63].

than the CNT and depends on the choice of thermostat [73]. Similarly, Guo et al. described that the thermal conductivity of DNWs may rise with respect to the increase in length and cross-sectional areas [74]. Recently, as seen in **Figure 7**, our group also proved the downfall in the conductivity of a single G-DNW with respect to a decrease in temperature [63]. Overall, it has been concluded that between 0 and 1000 K, DNW's thermal conductivities firstly upsurge with an increasing temperature and then dropdown.

Next, coming to the electrochemical properties, it is well recognized that the planar boron-doped diamond (BDD) materials have the unique physical properties and were already been effectively applied as electrodes in many sensing studies. Wherein, compared to glassy carbon electrode, the diamond electrode acts as a potential candidate due to its chemical stability and biocompatibility [75]. Moreover, BDD electrode is not fouled easily and has a low background current with a wide potential window. By altering the surface end of BDD, the electronic and chemical properties can be tuned according to the requirement. Currently, the BDD nanoglass array is also involved in electron transport and electrocatalytic utilities [76, 77]. Conclusively, it is well established that the nanotextured DNW surfaces become the suitable platform for novel biosensor investigations.

## 4. Applications of DNWs

Among the applications of DNWs, the following five utilities have been demonstrated strongly. Those applications are (1) field emission applications of DNWs, (2) DNWs in mass analysis of small molecules, (3) DNWs as nanoelectromechanical switches, (4) DNWs as electrochemical sensors and (5) DNWs in ultrasensitive force microscopy.

### 4.1. Field emission applications of DNWs

The negative electron affinity of DNWs has been used in field emission studies. Recently, reports on the electron field emission (EFE) properties of CVD-developed ultracrystalline

diamond and hybrid diamond-graphite films were reported [78, 79]. In this way, the EFEs of DNWs were also been described by (A) planar DNWs array and (B) single DNW.

(A) Electron field emission of planar DNW array: Lee et al. demonstrated the EFE characteristics of planar diamond film array, which has been developed by CVD techniques [80]. Recently, Sankaran et al. presented the improved EFE applications of graphite-wrapped DNWs [81]. In this path, the above group determined the enhanced electron field emission of vertically aligned ultrananocrystalline diamond needles via ZnO coating to form the heterostructured nanorods [82]. Wherein, it shows a high emission current density of  $5.5 \text{ mA cm}^{-2}$  at  $4.25 \text{ V } \mu\text{m}^{-1}$  and has a low turn-on field of  $2.08 \text{ V } \mu\text{m}^{-1}$  than that of bare Zn-nanorods. This outstanding emission property of planar diamond film arrays seems to be impressive to apply as the electron emitters in flat display panels.

(B) Electron field emission of a single DNW: Recently, Hsu and coworkers presented the electron field emission of a single DNW [57]. Wherein, the threshold field of DNW ( $1.25 \text{ V } \mu\text{m}^{-1}$ ) is four times lower than that of carbon nanotube ( $5 \text{ V } \mu\text{m}^{-1}$ ). This might be due to the electron affinity of DNW and defects existence. In addition, the EFE property of DNW may be attributed to its chemical inertness, high mechanical strength and high thermal conductivity.

#### 4.2. DNWs in mass analysis of small molecules

Firstly, Coffinier et al. described the matrix-free laser desorption/ionization (D/I) mass spectrometric utilization of boron-doped DNWs (BDD NWs) toward small molecular analysis [83], in which the S/N ratios of UDD NWs are very low than that of BDD NWs. Therefore, the potentiality of BDD NWs in mass analysis of small molecules has been proved.

#### 4.3. DNWs as nanoelectromechanical switches

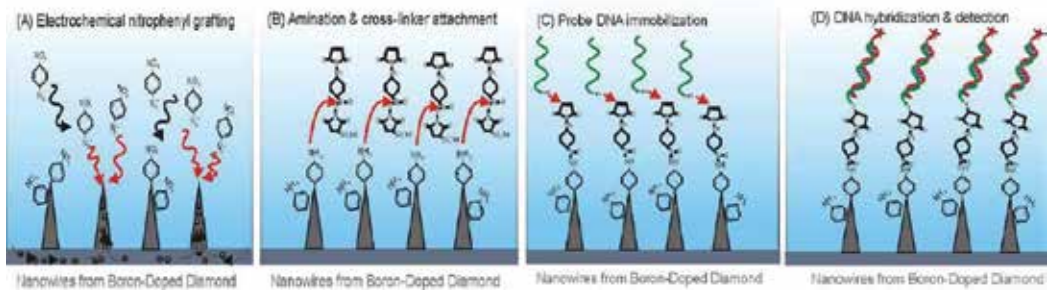
Recently, researchers tend to develop the diamond-based nanoelectromechanical (NEM) switches as an alternative silicon-based ones due to its exceptional properties such as high Young's modulus, maximum hardness, hydrophobicity, low mass density, greater thermal conductivity, extraordinary corrosion resistance and low toxicity. However, because of existed grain boundaries, impurities, large stress, low electrical conductivity and poor reproducibility, the polycrystalline or nanocrystalline film-based switches seem not to be as impressive candidates [84, 85]. In contrast, the utilization of single-crystalline DNWs as NEM is appraised by Liao and coworkers [29], in which those switches show low leakage current ( $<0.1 \text{ pA}$ ) with a high ON/OFF ratio, hence can compete Si-NEMS structures.

#### 4.4. DNWs as electrochemical sensors

DNWs were effectively applied in many electrochemical sensory studies. For example, Yang and Nebel utilized the vertically aligned diamond nanowires toward DNA detection *via* electrochemical approach [86]. **Figure 8** represents the schematic of biofunctionalized vertically aligned diamond nanowires for the determination of DNA in the abovementioned report.

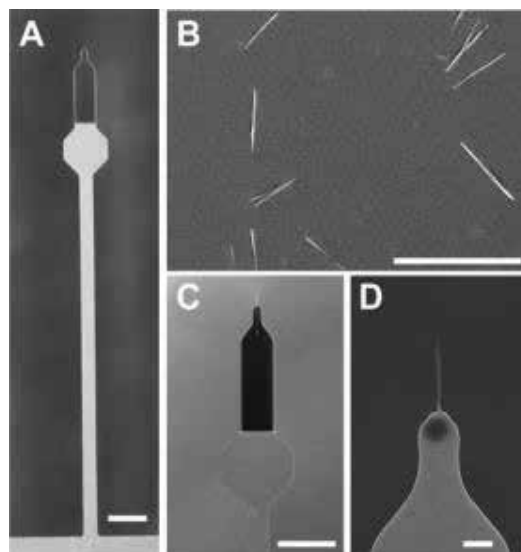
Further, they have protracted those diamond nanowires in electrochemical gene sensors [87]. Akin to vertically aligned nanowires, BDD NWs were applied in nonenzymatic amperometric





**Figure 8.** Schematic illumination of the biofunctionalization of vertically aligned diamond nanowires to realize a nanoscaled spacing between DNA molecules. Reproduced with permission from [86].

glucose biosensing by Zhi et al. [46]. Wherein, the selective determination of glucose has been demonstrated in the presence of ascorbic acid (AA) and uric acid (UA). Meanwhile, BDD NWs were also been efficiently used in the electrochemical identification of tryptophan by Szunerits and coworkers [88]. Alternatively, Lee and Lin collaborators developed a nitrogen incorporated DNW electrode for the amperometric detection of urea and *in situ* detection of dopamine [89]. Here, dopamine determination was well illustrated in the presence of AA and UA. More recently, Peng et al. reported the detection of CO gas by BDD NWs through electrochemical studies [90]. Wherein, the boron-doped ultrananocrystalline diamond (B-UNCD) nanowires (NWs) evidenced greater selectivity to CO gas than that of competitive species.



**Figure 9.** Integration of diamond nanowire tips on ultrasensitive silicon cantilevers. (A) Bare silicon cantilever with a nominal length of 90  $\mu\text{m}$ , a shaft width of 4  $\mu\text{m}$  and a thickness of 135 nm. The scale bar is 10  $\mu\text{m}$ . (B) Batch of DNWs transferred onto an Si substrate for manual pickup. The scale bar is 10  $\mu\text{m}$ . (C, D) zoom-in onto the end region of two different cantilevers where DNW tips had been attached. Scale bars are 10  $\mu\text{m}$  in C and 1  $\mu\text{m}$  in D. Reproduced with permission from [91].

#### 4.5. DNWs in ultrasensitive force microscopy

Recently, Tao et al. described the utility of DNWs as tips for ultrasensitive force microscopy experiments [91]. Wherein, they have fabricated two types of tips using the upper and lower halves of a DNW by means of a top-down plasma etching technique and from a single-crystalline substrate. **Figure 9** demonstrates the integration of diamond nanowire tips on ultrasensitive silicon cantilevers. The typical lengths of those DNWs lie in few micrometers with diameters around 100 nm. Moreover, the tip radii were at the order of 10 nm, hence becoming suitable for scanning probe applications [32].

### 5. Conclusions and perspectives

Attributed to the importance of DNWs, several efforts have been driven by experts to apply in diverse semiconductor and biological applications. In this way, those DNWs were effectively developed through different methods such as reactive-ion etching, chemical vapor deposition, from  $sp^2$  carbon and  $sp^3$  diamondoids and wet chemical route. Among them, the template-assisted synthesis of DNWs seems to be impressive to produce highly precise nanostructures. On the other hand, the cost-effective wet chemical route still remains a challenging task in terms of reproducibility and obtaining the unique structures. From experimental and theoretical studies, it has been found that DNWs have the exceptional structural, mechanical, thermal conductivity, electronic and electrochemical properties. However, structural studies on hybrid G-DNWs require exclusive focus for future applications. Subsequently, those DNWs also possess the unique applications such as EFE device, high-performance NEM switches, conductivity and electrochemical biosensor and so on. However, with respect to practicality, those applications remain unsatisfied. For instance, the reported DNW-based electrochemical biomolecules monitoring was affected by its stability; hence, still it is a challenging task to fabricate the DNW-based device for real-time continuous determination.

So far, except the wet chemical route, the reported synthetic techniques for DNWs are costly, and hence their development is still a challenging task. Therefore, much effort needed to develop the DNWs at large scale, which can be attained by the collaboration of diverse technical fields such as electro-biochemistry, nanoelectronics and analytical techniques, etc. For example, attempts are needed to develop hybrid G-DNWs by the association of CVD and wet chemical pathways. Such investigations may direct the DNWs toward diverse opto-electronic applications.

#### Conflict of interest

The author declares that there is no conflict of interest.

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# Analysis of Electrochemical and Structurally Enhanced $\text{LiMn}_2\text{O}_4$ Nanowire Cathode System

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Additional information is available at the end of the chapter

<http://dx.doi.org/10.5772/0>

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## Abstract

The performance of the battery cathode depends on the electrode microstructure and morphology, as well as the inherent electrochemical properties of the cathode materials. The spinel  $\text{LiMn}_2\text{O}_4$  is the most promising candidate as a cathode material because of its low cost and nontoxicity compared with commercial  $\text{LiCoO}_2$ . However, there is still a challenge to synthesize high-quality single-crystal nanostructured cathode materials. Nanowires offer advantages of a large surface to volume ratio, efficient electron conducting pathways and facile strain relaxation. To enhance the activity and stability, flexible spinel nanowires are synthesized, via  $\alpha\text{-MnO}_2$  nanowire precursor method. Ultrathin  $\text{LiMn}_2\text{O}_4$  nanowires with cubic spinel structure were synthesized by using a solvothermal reaction to produce  $\alpha\text{-MnO}_2$  nanowire followed by solid-state lithiation.  $\text{LiMn}_2\text{O}_4$  nanowires have diameters less than 10 nm and lengths of several micrometers. The  $\text{LiMn}_2\text{O}_4$  nanowires are used as stabilizing support during the electrochemical redox processes. The unique nanoporous material effectively accommodates structural transformation during  $\text{Li}^+$  ion insertion and effectively reduces  $\text{Li}^+$  diffusion distances, reducing the volumetric changes and lattice stresses during charge and discharge. Galvanostatic battery testing showed that  $\text{LiMn}_2\text{O}_4$  nanowires delivered 146 mAh/g in a large potential window. The electrochemical and spectrochemical interrogation techniques demonstrated that  $\text{LiMn}_2\text{O}_4$  nanowires are promising cathode materials for lithium ion batteries as apposed to  $\text{LiMn}_2\text{O}_4$  powders.

**Keywords:** electrochemistry, cathode, energy, nanowire, diffusion

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## 1. Introduction

The energy storage field faces a critical challenge: namely, the development of rechargeable systems for load leveling applications (e.g. storing solar and wind energy). Among the available battery technologies to date, only Li-ion batteries may possess the power and energy densities necessary for high power applications. The Li ion battery interface materials can store a lot of Li ions but have large structure change and volume expansion, which can cause mechanical failure. In this work we exploited the use of nanowire (NW) cathode morphology to alleviate these issues. Nanowires offer advantages of a large surface to volume ratio, efficient electron conducting pathways and facile strain relaxation [1]. In lithium-ion batteries, the cathode plays a critical role in determining energy density. Here the main requirements are a prolonged cycle life, components (i.e., relevant elements) abundant in high quantities in the earth's crust, and environmentally friendly systems [2, 3]. Among the commonly used layered Ni or Co oxide materials, the spinel  $\text{LiMn}_2\text{O}_4$  appears to be a more favorable cathode in lithium ion batteries [4]. Spinel  $\text{LiMn}_2\text{O}_4$  is economical, nontoxic and a highly abundant material with superior safety [5]. Conversely, its common drawback is a kinetic limitation, which is observed under fast scan rate or high current density, when the characteristic two peaks/plateaus associated with the charge and discharge mechanism of the spinel structure diminishes. To overcome this obstacle and permit the use of  $\text{LiMn}_2\text{O}_4$  in energy-demanding applications, the use of nanostructured morphologies for the development of fast kinetic electrodes is an ideal approach [6]. Literature studies have shown that the one-dimensional nanosized materials have faster kinetics and higher rate capability than micrometer-sized materials due to the large surface-to-volume ratio that enhances the contact between active material grains and electrolyte. However, the high-temperature sintering process, which is necessary for high-performance cathodes based on high-quality crystallinity, such as  $\text{LiMn}_2\text{O}_4$  leads to large grain size and aggregation which alters the battery performance due to increased lithium ion diffusion length and decreased effective surface area contact with electrolyte. Here, the objective was to produce a highly crystalline nanostructured cathode electrode material. Single crystalline nanowire morphology has proven most appealing because the untwined material fabricated by the single crystalline nanowire reduces aggregation, electronic resistance and grain growth at elevated temperature [7]. Generally, the electrochemical performances of electrode materials are strongly influenced by the phase crystallinity, purity, particle size, and distribution. The internal channels in these nano-crystalline cathode material spheres serve two purposes. They admit liquid electrolyte to allow rapid entry of  $\text{Li}^+$  ions for quick battery charging, and they provide space to accommodate expansion and contraction during  $\text{Li}^+$  intercalation and deintercalation, boosting battery power characteristics critical to improve the  $\text{LiMn}_2\text{O}_4$  performance. This research work produced highly crystalline  $\text{LiMn}_2\text{O}_4$  nanowires, synthesized using a facile, easy to scale up process, starting with the preparation of R- $\text{MnO}_2$  nanowires followed by solid state reaction with LiOH. Concomitantly,  $\text{LiMn}_2\text{O}_4$  powders were also prepared and studied as comparison [8]. The high rate capability as well as phase stability of the nanowires architecture and electrochemistry was demonstrated as probed by electrochemical and spectrochemical characterization techniques. Because determination of

the local structure seems one of the key issue for understanding electrochemical properties, vibrational spectroscopy was also applied to provide information on structural features of the nanowires.

## 2. Experimental procedure

### 2.1. Modified $\text{LiMn}_2\text{O}_4$ nanowire syntheses

In this work, the spinel  $\text{LiMn}_2\text{O}_4$  powders were prepared following a procedure found in the literature with minor adjustments [9]. Typical synthesis includes the reaction of lithium hydroxide and manganese acetate ( $\text{LiOH}$  and  $\text{Mn}(\text{CH}_3\text{COO})_2$ ) via a co-precipitation method. A stoichiometric amount of  $\text{LiOH}$  and  $\text{Mn}(\text{CH}_3\text{COO})_2$  with the cationic ratio of  $\text{Li}/\text{Mn} = 1:2$  were dissolved in deionized water and mixed by stirring. The solution is then evaporated at  $100^\circ\text{C}$  for 10 h to obtain the precursor powder [10]. Concomitantly we effectively produced ultrathin spinel  $\text{LiMn}_2\text{O}_4$  nanowires using a facile, two-step process. First, single crystals were produced from a nonaqueous solution in an autoclave reaction to prepare  $\text{R-MnO}_2$  nanowires, followed by solid state reaction with  $\text{LiOH}$ . In a typical process, a hydroalcoholic solution was formed in distilled water and adding first  $(\text{NH}_4)_2\text{SO}_4$  with  $(\text{NH}_4)_2\text{S}_2\text{O}_8$  and then 1-octanol. The solvothermal reaction was then performed at  $140^\circ\text{C}$  for 12 h in an autoclave to obtain  $\alpha\text{-MnO}_2$ . This was followed by a solid state reaction between  $\text{R-MnO}_2$  nanowires and  $\text{LiOH}$  at low pressure and oxygen atmosphere to achieve the pure  $\text{LiMn}_2\text{O}_4$  nanowire phase. The chemistry of the nanostructures, the crystallinity and phase purity of  $\text{LiMn}_2\text{O}_4$  powders,  $\text{R-MnO}_2$  and  $\text{LiMn}_2\text{O}_4$  nanowires were all characterized by X-ray diffraction (XRD), scanning electron microscopy (FE-SEM), high resolution transmission electron microscopy (HR-TEM), electrochemical impedance spectroscopy and Nuclear magnetic resonance spectroscopy ( $^7\text{Li}$  NMR) technique to observe the local magnetic fields around atomic nuclei.

## 3. Characterization and analysis

The morphology and particle size distribution of  $\text{LiMn}_2\text{O}_4$  nanowire material was analyzed with a Transmission electron microscopy (TEM), taken with a JSM-6700F (JEOL). Raman spectra of all samples were recorded at wavelength of 514.5 nm using a laser-spectrometer (model Jobin-Yvon U1000) equipped with dual monochromators coupled with special filter and emission of an argon-ion laser (model Spectra-Physics 2020). To avoid sample photo-decomposition or denaturation a power density as low as  $10 \text{ W cm}^{-2}$  was used. NMR was carried out at room temperature on Varian spectrometer (VNMRS WB 500 solids) with an 11.7 T magnet. Magic angle spinning (MAS) NMR experiments were performed using a 6 mm MAS probe. The  $^7\text{Li}$  resonance frequency was 194.29 MHz with a sample spinning speed of 16 kHz. Single-pulse and echo-pulse sequences were used to acquire all spectra. The amount of lithium detectable in the materials was quantified using 1 M  $\text{LiCl}$  (116.571 MHz) as an

external reference for chemical. XRD Measurements were carried out with a D8 ADVANCE diffractometer from BRUKER axs using an X-ray tube with copper K-alpha radiation operated at 40 kV and 40 mA and a position sensitive detector, Vantec\_1, which enables fast data acquisition. Measurement range: [12–90° in 2 theta], Step size: 0.027° in 2 theta, Measurement time: 1 s/step.

### 3.1. Electrochemical tests

The Electrochemistry of the  $\text{LiMn}_2\text{O}_4$  nanowire cathode, were carried out with electrodes mounted on coin-cells operating at 30°C. Electrode mixtures were prepared by mixing the oxide powder (70 wt%) with acetylene black (current collector) (20 wt%) and polyvinylidene fluoride (PVdF) binder (10 wt%) in N-methylpyrrolidone (NMP) solvent to form a mixed slurry. The slurry was coated on an aluminum foil, followed by drying in a vacuum oven at 120°C for 48 h and a cathode electrode was formed.

## 4. Results and discussions

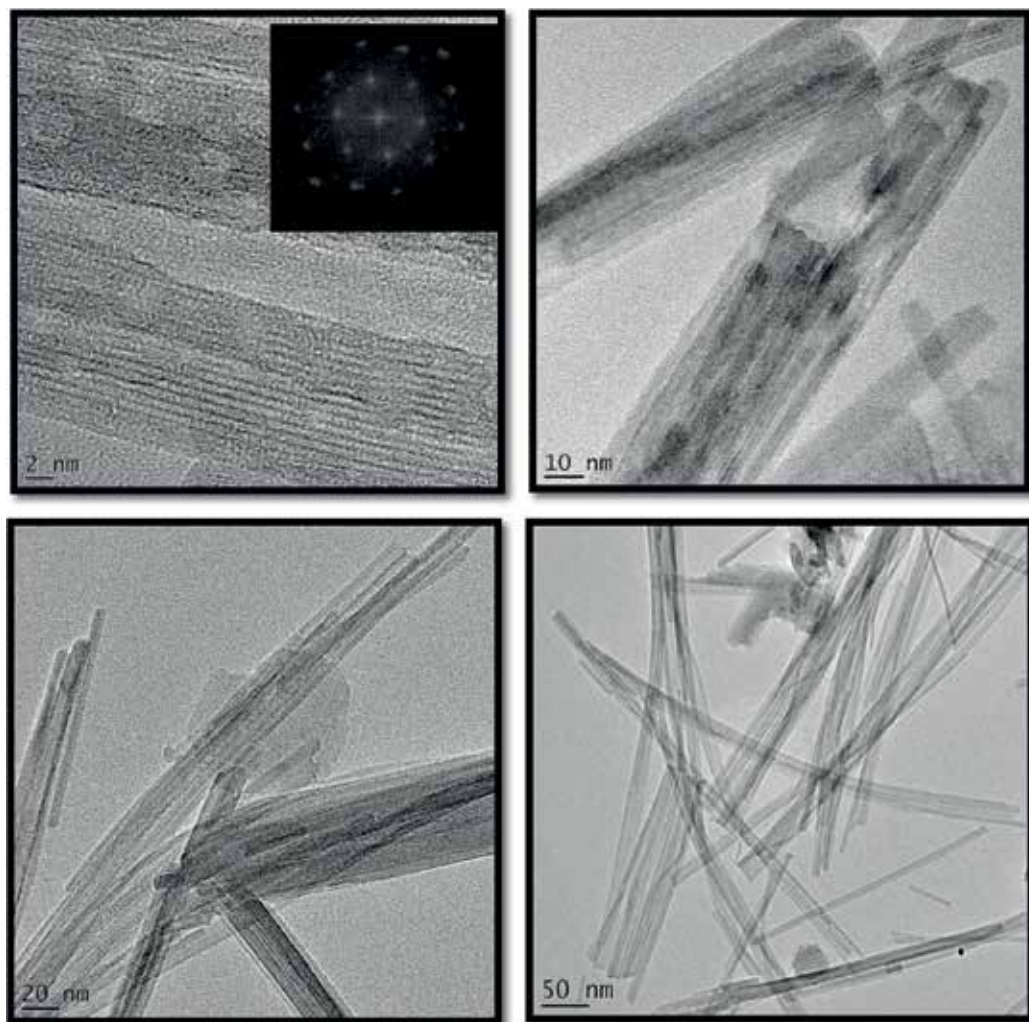
### 4.1. Surface morphology

#### 4.1.1. Transmission electron microscopy (TEM)

**Figure 1a–d** shows the TEM micrographs of the crystalline  $\text{LiMn}_2\text{O}_4$  spinel nanowires in the range of 5–20 nm without agglomeration. The wires appear to be highly crystalline and moderately dispersed, which causes the material to possess a larger surface area. These nanowires have diameters of tens of nanometers and lengths up to several micrometers and basically retains the morphology of the precursor  $\text{MnO}_2$  nanowires. At higher magnification, **Figure 1c**, nanowires adhering together are observed, which leads to the notion that these nanowires have a fiber-brush aspect. **Figure 1b** is a typical high-resolution electron microscopy (HREM) image clearly displaying the lattice fringes of the material. The SAED patterns along [100] direction of the single crystals are showed in the inset. Bright diffraction spots including (022), (222) and (004) in the [100] zone are generated from the spinel structure with  $Fd3m$  space group. The images also show single  $\text{LiMn}_2\text{O}_4$  nanowires with distinctive lattice fringes. The discriminable lattice fringes illustrate that the prepared nanowires are single crystals in the area shown. **Figure 2** shows a typical TEM image at a magnification of 2 and 20 nm of  $\text{LiMn}_2\text{O}_4$ . The primary particle size of the as-synthesized powders is around 10 nm with visible agglomerations. The as-synthesized and calcined powders typically have surface areas of  $18.0 \text{ m}^2 \text{ g}^{-1}$  measured by BET method [11].

#### 4.1.2. Atomic force microscopy (AFM)

The surfaces of the cathode were observed by high-resolution atomic force microscopy in dried state. **Figure 3** shows  $2 \times 2 \mu\text{m}^2$  dimensional AFM images of  $\text{LiMn}_2\text{O}_4$  nanowires and the



**Figure 1.** High resolution TEM image of crystalline  $\text{LiMn}_2\text{O}_4$  nanowires.

$\text{LiMn}_2\text{O}_4$  nanopowders in Ar atmosphere. The images reveal apparent changes in roughness between the films. It is known that the distribution of particles influences the cyclability and discharge capacity [12]. Tapping mode AFM images displayed the surface morphology of nano- $\text{LiMn}_2\text{O}_4$  particles (a–b). Evidently, a rough structure with closely distributed micropores of less than 5 nm in diameter was observed. From the two-dimensional image, it was evident that this surface yields a large degree of surface roughness (a). A more detailed analysis of the particle size is shown in the histogram of (a, b-i). Particle sizing and metrics play a critical role in determining battery capacity and performance. Therefore, the typical size of materials used for battery construction is  $>1 \mu\text{m}$  [13]. Here, the size distribution moved toward an average diameter of 60 nm aiding the high-rate capabilities of the cathode. For  $\text{LiMn}_2\text{O}_4$  several “cauliflower-like” areas are observed with a rms (root mean square) roughness of

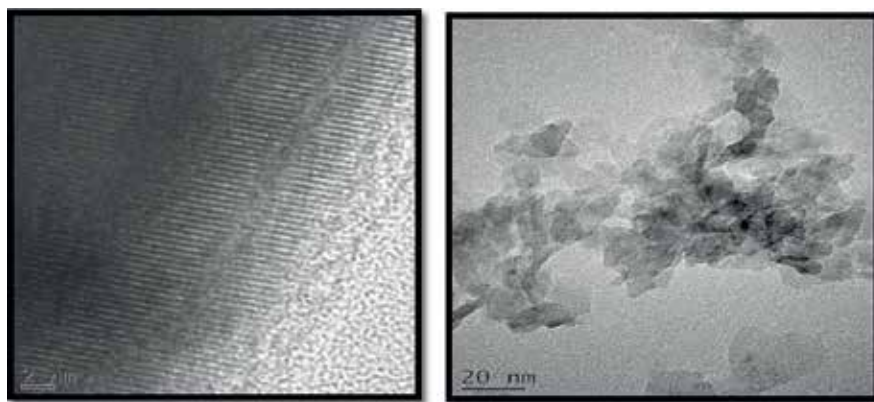


Figure 2. TEM images of  $\text{LiMn}_2\text{O}_4$  nanoparticles.

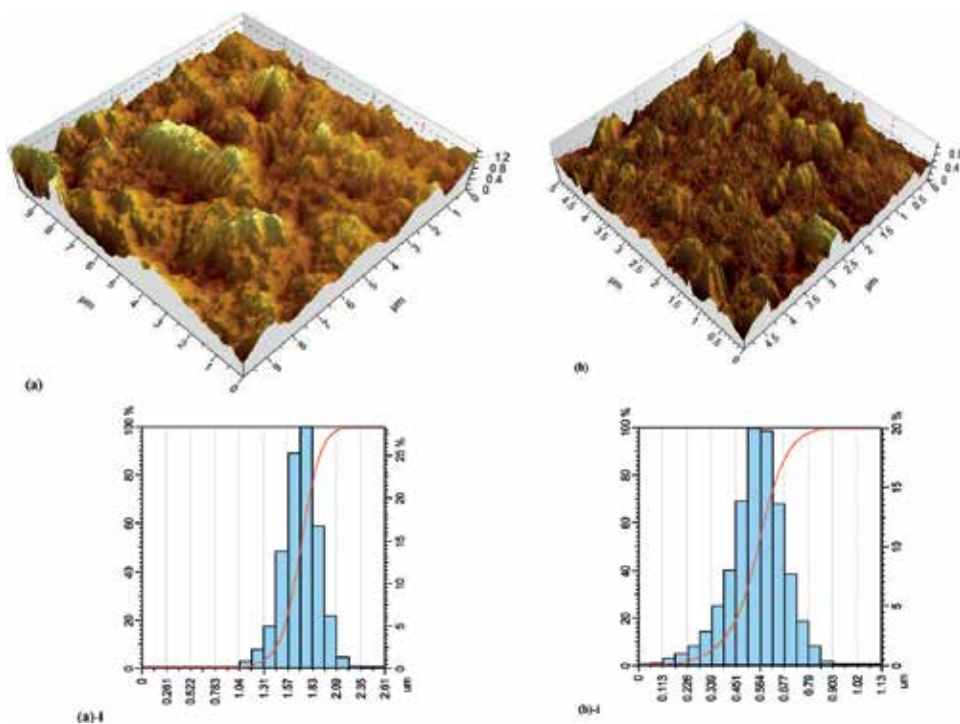


Figure 3. AFM images of  $\text{LiMn}_2\text{O}_4$  shown in tapping mode in three-dimensional views at 600 nm and particle size distribution histogram (i) of  $\text{LiMn}_2\text{O}_4$  (a) and  $\text{LiMn}_2\text{O}_4$  nanowires (b), respectively.

$73.5 \text{ \AA}$ , whilst  $\text{LiMn}_2\text{O}_4$  nanowires topography appears smoother and homogeneous with rms roughness of  $26.3 \text{ \AA}$  which can be calculated using Eq. (1). These features constitute the stability of the spinel structure, which enhances the electrochemical properties.

$$R_q = \sqrt{\frac{1}{n} \sum_{i=1}^n y_i^2} \quad (1)$$

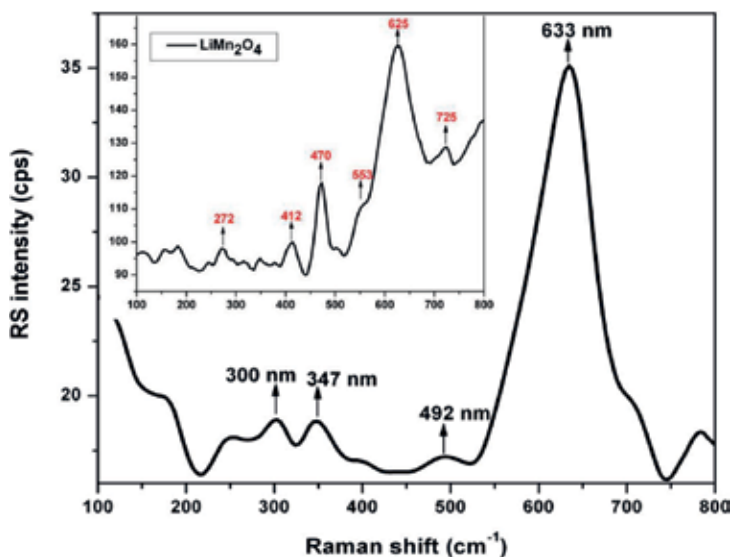


#### 4.1.3. Vibrational structure analysis (Raman/SS-NMR)

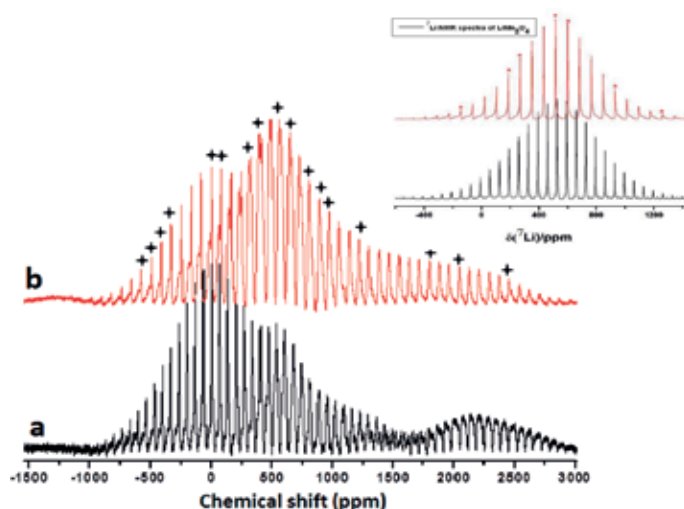
**Figure 4** shows the Raman spectra (RS) of  $\text{LiMn}_2\text{O}_4$  and  $\text{LiMn}_2\text{O}_4$  nanowires. A Blue shift of photons and a noticeable decrease in the peak width is observed with modification. The bands at  $\sim 560$  and  $\sim 660 \text{ cm}^{-1}$  are attributed to the O-Mn-O bending and stretching modes, respectively. Some vibrational or rotational transitions, which exhibit low polarizability, becomes Raman inactive therefore sharper peaks signifies better crystallinity and less cation mixing [14]. The observation of narrower bands and less modes may be the result of no translation invariance and lattice distortion around the  $\text{Mn}^{3+}$  and  $\text{Mn}^{4+}$  cations. The catalytically active tetragonal hausmannite ( $\text{Mn}_3\text{O}_4$ ) spinel is acknowledged by the solid peak erected at  $633 \text{ cm}^{-1}$ , conforming to the Mn-O breathing vibration of  $\text{Mn}^{2+}$  ions in tetrahedral coordination ( $A_{1g}$  mode) [15]. The  $T_2 g$  (1),  $E_g$ , and  $T_2 g$  (2) modes of  $\text{Mn}_3\text{O}_4$  is shown by peaks 300, 347, and  $492 \text{ cm}^{-1}$  respectively. As discussed by Julien et al. [16], the  $A_{1g}$  mode correlated with Mn-O vibration of  $\text{MnO}_6$  groups will shift to lower energies as the average Mn oxidation state increases. The Raman data are in agreement with diffraction analysis.

**Figure 5** shows the  $^7\text{Li}$ -NMR spectrums of both spinel  $\text{LiMn}_2\text{O}_4$  and  $\text{LiMn}_2\text{O}_4$  nanowires.

Nuclear magnetic resonance (NMR) spectroscopy has been employed as a significant tool to probe the local structure and dynamics of these nano materials. Here we show how this technique help understand the origins of the performance of the given nanomaterial [17].  $\text{LiMn}_2\text{O}_4$  is a hopping semiconductor containing both  $\text{Mn}^{3+}$  and  $\text{Mn}^{4+}$  ions. The hopping time scale is fast between these ions in comparison to the NMR time scale (ca.  $10^{-5} \text{ s}$ ) and therefore the lithium spins detect a manganese oxidation state corresponding to 3.5 (i.e., " $\text{Mn}^{3.5+}$ " ions), pertaining to only one magnetically inequivalent lithium site (the 8a site) [12]. This permits the NMR spectroscopy to be used as a means to follow the partial charge-ordering process. The hyperfine shift of  $>500 \text{ ppm}$  from the chemical shift range of diamagnetic compounds containing lithium is characteristic of the  $^7\text{Li}$  MAS NMR spectrum of  $\text{LiMn}_2\text{O}_4$ .



**Figure 4.** Vibrational spectra of  $\text{LiMn}_2\text{O}_4$  nanowires and  $\text{LiMn}_2\text{O}_4$  precursor (inset).

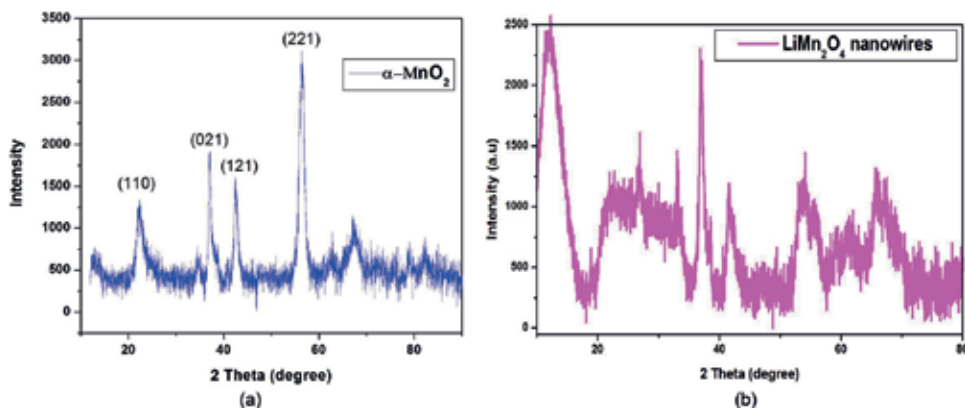


**Figure 5.**  $\text{LiMn}_2\text{O}_4$  nanowire NMR spectrum at 13 kHz (a) and 16 kHz (b) spinning speed, magnetic field strength of 11.7 T and resonance frequency of 194.29 MHz.  $\text{LiMn}_2\text{O}_4$  nanopowder (inset).

The spinel  $\text{LiMn}_2\text{O}_4$  nanowires have a distinctly different spectral line shape as compared to their powder form [18]. The isotropic resonance at 511 ppm for  $\text{LiMnO}_4$  is assigned to lithium ions in the tetrahedral 8a site, whilst the isotropic resonances at ~680 and 835 ppm for  $\text{LiMn}_2\text{O}_4$  nanowires is assigned to lithium present in the proximity of higher oxidation state manganese ions ( $\text{Mn}^{4+}$ ). The latter is generally originated from vacancies on both the lithium and manganese sites (i.e., Mn 16d sites and the interstitial 16c sites or Li-for-Mn substitutions) [19]. The resonance at 445 ppm is ascribed to the presence of  $\text{Mn}^{3+}$  ions in the Li local coordination sphere.  $\text{Mn}^{3+}$  is a *Jahn–Teller (distortion)* active ion; therefore there will be a distortion of the octahedron in this case [20]. The enhancement of spinning sideband manifold for  $\text{LiMn}_2\text{O}_4$  nanowires is caused by the increased portion of paramagnetic manganese around lithium [21]. Hence, it can be suggested that the lithium atoms are interacted with the manganese and cause for better electrochemical performance.

#### 4.1.4. X-ray diffraction microscopic analysis (XRD)

The lattice constant was calculated from the corresponding diffraction pattern using XRD spectra, in relation to the crystal structure and is reported in **Figure 6**. All diffraction peaks can be assigned to the diffraction indices of  $\text{LiMn}_2\text{O}_4$  spinel (JCPDS file no. 35-782), indicating that the structure of the spinel was maintained. The majority diffraction peaks of  $\text{LiMnO}_2$  are observed, and closely correspond to layered  $\text{LiMnO}_2$  (011), (202) and (111) planes. The intensive diffraction peaks appeared at 21.92, 36.92, 42.42 and 55.96°, respectively, should be assigned to the characteristic peaks for  $\gamma\text{-MnO}_2$ , and the peaks occurred at 17.94, 28.78, 66.0°, respectively, should be ascribed to the characteristic peaks for  $\alpha\text{-MnO}_2$ . Hence, the sample appears to be composed of both  $\gamma$  and  $\alpha\text{-MnO}_2$ . From the broadening of XRD peaks, it can be said that nanowires are being formed. This suggest that the nanowires would render



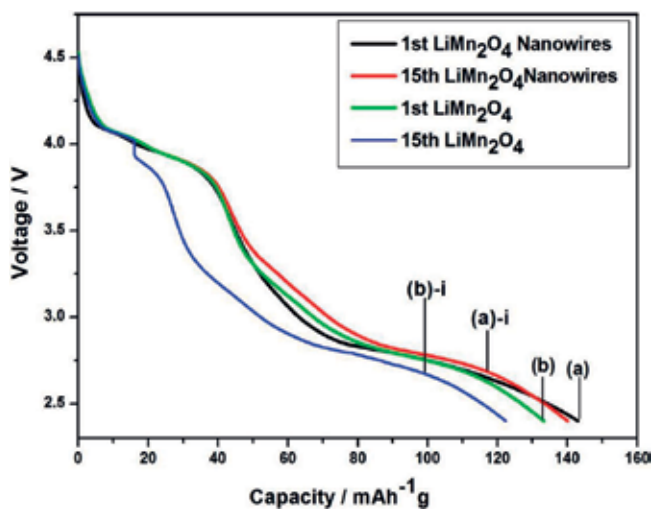
**Figure 6.** XRD spectra of  $\text{LiMn}_2\text{O}_4$  nanowires (a) and  $\alpha\text{-MnO}_2$  (b).

unchallenging  $\text{Li}^+$  pass through the coating layer more during charging and discharging process as the diffusion path is unhindered [22, 23]. The strong peak at  $2\theta = 18.76^\circ$ , corresponds to a (111) peak with an interplanar distance of  $d = 0.476$  nm. The full width at half-maximum (FWHM) becoming narrower is also due to higher synthesis temperatures which help to enhance the mobility of atoms. Sharp and relatively high intensity peaks (as compared to  $\text{LiMn}_2\text{O}_4$ ) depicts high crystallinity [24].

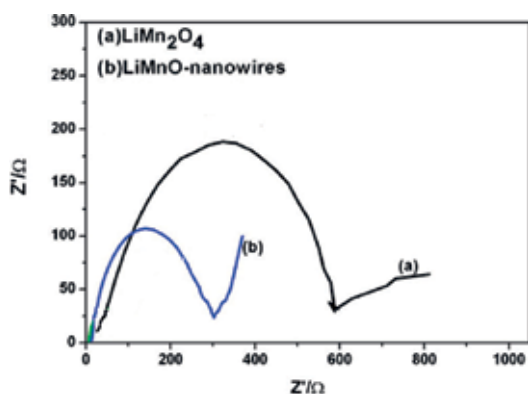
## 4.2. Electrochemical analysis

### 4.2.1. Redox reaction analysis

To clarify the kinetic behavior of lithium-ion transfer, the discharge tests were carried out at. **Figure 7** shows the effect of discharge current densities on the capacities of the two types of electrodes. It is well known that the nanowire electrodes have good electronic conductivity; therefore, they can greatly increase the electrical conductivity among the transition metal oxide particles and reduce the polarization of the  $\text{LiMn}_2\text{O}_4$  electrode. The nanowire cathode shows an increased discharge current densities due to improvement of  $\text{Li}^+$  diffusion pathway [25–27]. This coincides with prior research that has recognized the ability of nanowire architecture to enhance the stability or cyclability of electrode materials [28]. The initial discharge curve of  $\text{LiMn}_2\text{O}_4$  nanowire electrode was similar to that of  $\text{LiMn}_2\text{O}_4$  spinel, due to the lower ratio of  $\text{Mn}^{3+}/\text{Mn}^{4+}$  ions [29]. The initial discharge capacities and corresponding coulombic efficiencies after cycling for the nanowires and  $\text{LiMn}_2\text{O}_4$  were  $146 \text{ mAh g}^{-1} / 99\%$  and  $122 \text{ mAh g}^{-1} / 70\%$ , respectively. The decrease in capacity over subsequent cycles is explained by the change in surface area [30]. Three-dimensional porous nanostructures with large surface area could exhibit higher durability in the lithium insertion/ extraction process at a high current density, owing to the short lithium ion diffusion lengths in the 3-D channels of the electrode [31]. The  $\text{LiMn}_2\text{O}_4$  nanowire electrode exhibited excellent rate capability as shown in **Figure 7**. The large capacities are due to the nanowire morphology, stability and the high quality of the single crystal, which can shorten the diffusion lengths of both the lithium and electrons [32].



**Figure 7.** Discharge current densities at  $0.1 \text{ mV s}^{-1}$  for  $\text{LiMn}_2\text{O}_4$  nanowires (a) and  $\text{LiMn}_2\text{O}_4$  (b) in EC: DMC, 1 M  $\text{LiPF}_6$  for the (1st (a, b) and 50th (a-i, b-i) cycle).



**Figure 8.** Nyquist plot of  $\text{LiMn}_2\text{O}_4$  (a) and  $\text{LiMn}_2\text{O}_4$  nanowires (b).

This was further corroborated by electrochemical impedance spectroscopy results shown in **Figure 8**. The nanowires show a significant decrease in impedance due to their enhanced electrochemical diffusion processes.

## 5. Conclusion

In summary, the  $\text{LiMn}_2\text{O}_4$  nanowires have proven excellent thermal stability for a high-temperature sintering process as well as a charge-discharge reversible stability and improved conductivity attained by their architecture, excellent crystallinity and decreased impedance.

All the as-prepared powders were identified as a single phase of cubic spinel structure indicative of an unobstructed Li ion diffusion pathway. The results have shown that thin-nanowire precursor morphology is preserved after the solid-state reaction. Such morphology improves the kinetic properties at very high current rate and was capable of the facile structural transformation of the cubic and tetragonal phase in the large compositional range. The  $\text{LiMn}_2\text{O}_4$  nanowires showed a decrease in potential difference, indicating an improved charge transportation process. The nanostructures aid structural stability, reduction of side reactions and Mn dissolution between the interface of the cathode and electrolyte, which contributes to the recovering performance. Moreover, the nanowire cathode system has great potential for improving the electrode-filled ratio and safety in lithium ion battery-operating electronic devices, in transportation applications, and in applications on the electric grid.

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## Conflict of interest

The author declares that there is no conflict of interest.

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# Applications

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# Nanowires for Room-Temperature Mid-Infrared Emission

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Additional information is available at the end of the chapter

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## Abstract

InAs-based nanowires hold a promise to offer transformational technologies for infrared photonic applications. Site-controlled InAs nanowire growth on low-cost Si substrates offers the practical integration advantages that silicon photonics benefits from. This includes the realisation of cheap photonic circuitries, light emitters and detectors that are otherwise expensive to realise with III/V material-based substrates. This chapter details the growth development of advanced faceted multi-quantum well structures within InAs nanowires using molecular beam epitaxy. We review the crystal structure for the faceted quantum wells along with an analysis of their optical emission characteristics which shows quantum confinement and localisation of the carriers on the quantum well nanostructure. This enables tuning of the emission wavelength and enhanced emission intensity up to the technologically important room-temperature operation point.

**Keywords:** indium arsenide, nanowires, multi-quantum wells, molecular beam epitaxy, photoluminescence, infrared photonics, silicon photonics

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## 1. Introduction

Developments that took place in the past few decades in the semiconductor industry have allowed the realisation of III–V one-dimensional (1D) structures [1], such as nanowires (NWs), that have attracted increasing attention as promising materials for the fabrication of mid-infrared nanoscale devices [2]. III–V semiconductor NWs have many interesting physical and optical properties due to their narrow band gap [3], small electron effective mass [4], very high electron mobility [5], along with a great potential for realising nanoscale devices [1, 6, 7].

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In recent years, silicon photonics attracted a significant research effort because of the potential benefits of integrating optoelectronics functions within silicon (Si) CMOS electronic devices [8]. The growth of InAs nanowires on low-cost Si substrates paves the way towards low-cost infrared detection technologies [7]. The unique geometry of NWs structures offers new silicon photonics architectures for sensing to be operating in the mid-infrared spectral range [9], hence improving the control over the size [10], higher flexibility in sample processes [11, 12] and more freedom for band-gap engineering [13]. The field of InAs-based NWs growth in particular has attracted many researchers interest and have been extensively investigated in order to fabricate nanoscale devices including field-effect transistors [14], solar cells [15], sensor applications [16], lasers [12] and photodetectors [17].

Semiconductor NWs in nanotechnology can be synthesised through two main approaches, one is called top-down and the other bottom-up approaches [18]. The idea behind the top-down approach is to etch out and remove the crystal planes of the material from larger pieces which is usually already present on the substrate to form the nanowires. This approach mostly dominates in industry for large-scale fabrication. Few researches demonstrated successful production of NWs using this approach, for example, using InP and InGaAsP/InP materials [11, 19]. To the best of our knowledge, until today, there has not been any report of InAsSb fabricated using top-down approach; this approach shows to some extent the ability to be producing NWs for some III-V materials. However, this method suffers from drawbacks such as wire surface contamination or damages after etching treatment which hinders achieving an optimum optical performance. In the bottom-up approach, the nanostructures are built up on the substrate by adding atoms layer by layer in an ordered manner, offering the growth of a very high uniformity to the crystal structures, with a higher relative controllability in the NWs growth rate. There are two methods within the bottom-up fabrication of nanowire growth techniques: the template directed and the free-standing methods. Most of the reported NWs have been fabricated using the second approach by random or site-controlled growth.

The NWs growth mechanisms are usually governed by the famous vapour-liquid-solid (VLS) [20] or vapour solid (VS) mechanisms [21]. In the VLS approach, metal droplets are deposited on the growth substrate either through self-induced (e.g. In for InAs) or foreign metal catalyst (such as Au droplets) followed by subsequent nanowire synthesis. In the VS growth mechanism, the material starts in a vapour form inside the growth chamber and then the layers are deposited on top of the substrate epitaxially layer by layer in the solid phase, and such processes can be lithographically patterned or self-assembled. In the case of self-assembled, the NWs are grown randomly on the surface and have a variation of diameter and length. However, this kind of growth may lead to an unintentional kinking in the grown NWs [14]. Thus, controlling over position and size (diameter and length) of the NWs are required to fabricate efficient nanowire-based devices, hence allowing their applications to be realised at a large scale.

As more progress is made towards realising efficient optoelectronic devices, many methods are adapted for NWs growth, such as pulsed laser deposition [22], chemical beam epitaxy [23], metal organic chemical vapour deposition MOCVD [24] and molecular beam epitaxy (MBE) [25]. The most popular methods to grow NWs are (MOCVD) [26] and (MBE) [27]. MOCVD system and related gas phase techniques are used in the production of commercial

large-scale product, due to their high surface diffusion, fast growth rates and high throughput [28]. However, in comparison to MOCVD, solid-source MBE offers several advantages in low impurity incorporation due to the ultrahigh vacuum growth environment and the highly pure elemental growth species [26], the very accurate control over the composition of the deposited monolayer and their doping, and finally the ability to grow advanced radial and axial core-shell heterostructures via sophisticated in situ growth-monitoring methods [29].

The focus in earliest studies was on optimising the growth conditions, analyse or control the crystal structure for such wires. However, the crystal phase is particularly relevant to PL studies because the band gap and hence emission wavelength are phase dependent. In general, bulk InAs grown by conventional epitaxial techniques have a zinc-blende phase, hence its band gap ( $E_{g_{\text{InAs (ZB)}}}$ ) is well characterised at 0.415 eV. However, by contrast, a consensus has not yet been reached around a single band gap for Wurtzite InAs ( $E_{g_{\text{InAs (Wz)}}$ ). While low-temperature studies build initial understanding, only a few optical emission studies have been reported, due to the very poor optical efficiency of these materials, very strong non-radiative surface and difficulties of performing spectroscopy in the IR spectral region [30]. For instance, the first low-temperature PL of InAs NWs was reported by Sun et al. [30] for InAs NWs on Si having both pure WZ and ZB crystal phases, with band energy (0.41–0.425 eV) corresponding to the above band edge surface state-related recombination, with a slight shift at increasing the temperature. They also noticed a blue shift due to quantum confinement depending on the wire diameter and not on the structure changes. Furthermore, Trägårdh et al. [31] predicted a WZ band gap of 0.54 eV from extrapolation-fitted photocurrent measurements at 5 K on single InAs NWs with a centrally placed  $\text{InAs}_{1-x}\text{P}_x$  segment of the composition  $0.14 < x < 0.48$ . Möller et al. [32] reported temperature-dependent PL studies that enable them to estimate the wurtzite band gap to be 0.458 eV at low temperatures. Koblmüller et al. [11] have gone further to report PL temperature dependence of InAs NWs having a WZ phase structure and a peak position 0.411 eV at 15 K. Despite the strong PL emission at low temperatures, the emission was only reached 130 K before the signal was quenched [31]. The wires show a 25-meV blue shift due to confinement by reducing the wire diameters from 100 to 40 nm with respect to bulk InAs due to quantum confinement and dominant surface effect which limits PL efficiencies. The non-radiative recombination causing this quenching could originate from lattice defects, surface states or Auger recombination [31]. Recently, Rota et al. [33] estimated the energy gap for InAs WZ nanowires to be 0.477 eV, higher than the ZB band gap by 59 meV which does not depend on the nanowires size and carrier confinement. The spread of results that ranges from 0.41 to 0.54 eV may originate from polytypism, with a further complication being atmospheric absorption in the commensurate spectral range. However, while low-temperature PL measurements have supported initial studies confirming the crystal structure, emission at room temperature will be required for most practical applications. A common route to suppressing non-radiative recombination at the surfaces is the in situ growth of a wider band-gap shell which has been employed to InAs wires by Treu et al. [34] using an InAsP shell where they demonstrated  $10^2$  times enhancement of the PL emission up to room temperature [35]. Also, GaAs/AlGaAs core-shell NWs showed improved PL intensity compared to bulk GaAs NWs caused by the reduction in the surface states, which was found to be effective in enhancing the PL emission intensity, allowing it to persist up to room temperature. In addition to suppressing

loss through non-radiative recombination, PL intensities and quenching temperatures can be increased by acting to raise the radiative recombination rate. In a very recent study, Jurczak et al. demonstrated a 10-fold enhancement of InAs NWs PL emission using an InP core-shell layer that passivates the surface states to reduce the rate of non-radiative recombination [35]. This research direction is attracting many researchers today in order to develop advanced optoelectronic devices and nanoscale photonic applications [13]. Progress in this direction will provide further insight into the optical emission and energy band-gap properties, hence improving the use of these materials, especially for infrared detectors and emitters. This chapter discusses the concept of developing novel InAsSb/InAs multi-quantum wells (MQWs) NWs on Si (111) substrate structures within InAs nanowires, as significant step towards viable nano- and quantum emitters in the extended IR wavelength range. We review the growth process for these structures, the crystal structural characterisation. Finally, we discuss their optical properties along with developing a band structure for the NWs of this material.

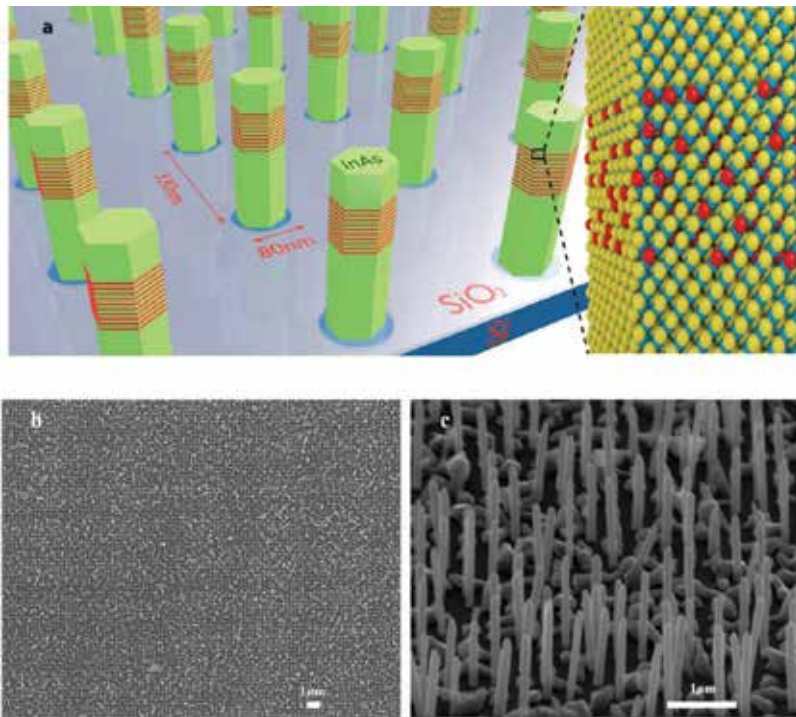
## 2. The growth

The growth of high-quality nanowires should be achieved by avoiding the common growth process that employs a foreign catalyst such as gold to nucleate the wires. Au is well known to introduce deep level traps in the material band gap as contaminations [36, 37], which then limits the performance of the devices functionality on Si. Therefore, using lithographically predefined  $\text{SiO}_2$  template is an additional benefit besides avoiding catalysts which enables an accurate control over positions and diameters of the grown NWs that determines where the growth occurs and allows homogeneous arrays by controlling the nucleation position without any catalysts by the selective area epitaxy technique.

In this work, the selective area molecular beam epitaxy (SA-MBE) technique has been used as a first step to grow the InAsSb/InAs MQWs NWs on Si (111) wafers and was achieved with nano-hole patterns produced by EBL [38]. The substrate is masked with a patterned dielectric layer, normally a  $\text{SiO}_2$  layer with a thickness below 100 nm. The NW growth starts when the As and In adatoms start to form critical nuclei of certain sizes on the surface inside the patterned holes.

The MQW nanowire growth was initiated by impinging As flux followed by exposure of the sample to the In flux 20 s later. After the growth of an initial pure InAs section for 1 h, the MQW active region was grown as 10 repeats of InAs/InAs<sub>1-x</sub>Sb<sub>x</sub> MQWs with growth durations of 180 and 27 s, respectively. This was expected to form 10 repeats of 25 nm InAs and 8 nm InAs<sub>1-x</sub>Sb<sub>x</sub> giving a total active region thickness of 330 nm as shown in **Figure 1(a)**.

Finally, the wires were finished with an InAs cap, grown for 10 min. All growths began with a pure InAs section and were finished with an InAs cap. For further comparison, a planar bulk InAs sample was grown as a reference under the same growth conditions. The MQW NWs were grown in arrays of 50-nm diameter holes patterned using e-beam lithography in a 330-nm pitch square array defined within a number of  $200 \times 200 \mu\text{m}$  areas, on each silicon



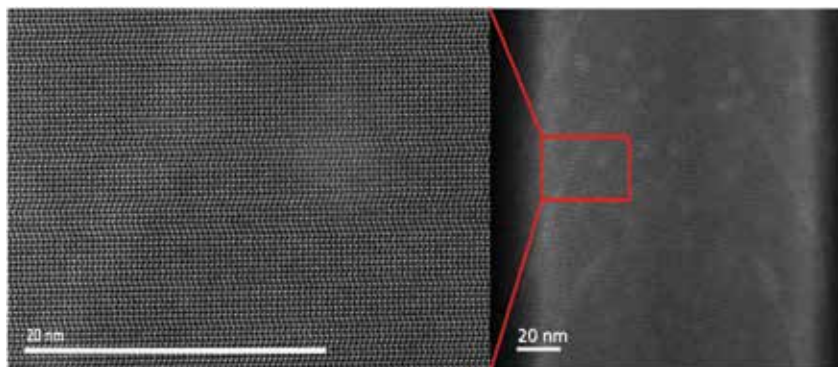
**Figure 1.** Nanowire structural design and analysis. (a) Schematic drawing of the InAsSb/InAs MQW within InAs NWs, grown in a 330-nm pitch square array patterned on a silicon substrate using a SiO<sub>2</sub> template with 80-nm diameter holes. Blue and yellow spheres represent In and As atoms, respectively, while red spheres indicate the random incorporation of Sb within the quantum well region. (b and c) Top view and tilted view SEM images, respectively. The nanowires are viewed at an angle of 70°. The scale bars in (b) and (c) are 1 μm. Figure obtained with permission from authors [38].

substrate, and the final hole diameter was ~80 nm due to lateral etching. From the SEM images, the MQW wires were on average ~1.5 μm in length and ~100 nm in diameter, see **Figure 1(b)** and (c).

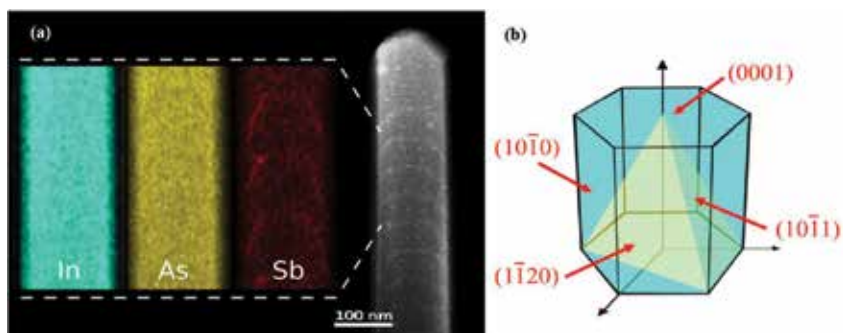
### 3. Structural analysis

The lattice structure of the InAs/InAsSb MQW NWs has been investigated using Scanning Transmission Electron Microscopy (STEM). A close inspection of high-resolution STEM images shows both the angled nature of the well and a continued WZ structure within the InAsSb well. **Figure 2** shows a full diameter image of the wire together with a zoomed-in view, within which the lattice structure can be discerned. From the zoomed-in view, it is clear that the structure of the InAsSb well does not change to ZB, as it would do in a pure InAsSb NW. However, in the case of the QWs, the short growth durations are insufficient to allow a flat top to form and so the WZ phase is maintained. Hence, the Sb fraction in the wells is currently best estimated at 6–7%, based on comparable bulk wires.

The nanowires had a regular hexagonal cross section with  $\{10\bar{1}0\}$  sidewalls, faceted tips and a twinned WZ crystal structure with stacking faults, which is a characteristic of InAs NWs [39, 40]. In common with other researchers, it has been found that the addition of Sb to form bulk InAsSb NWs forces a rotation in the layer stacking, leading to a predominantly ZB structure with a flat top. Bulk InAsSb NWs grown under these conditions contained 6–7% Sb, in good agreement with the earlier work that reports saturation at this concentration [33]. However, when the InAsSb growth is limited to nanoscale quantum wells, energy-dispersive X-ray (EDXS) mapping revealed preferential incorporation of the Sb on specific crystal planes. This results in the formation of novel quantum wells having faceted, flat-topped, conical shape, with open or partially closed flat tops, as shown in **Figure 3(a)**. It is noted that in other NW studies, the authors have considered nanoscale axial heterostructures as both quantum dots (QDs) [41, 42] and QWs [43–45]. The QD model is understandably favoured for lower-wire diameters; however, in light of the larger 100-nm diameter and the weak lateral confinement of the higher effective mass holes, the heterostructures described here are considered to be QWs.



**Figure 2.** High-resolution TEM analysis of the QW in NW. Cross-sectional TEM images, looking across a section of wire including a QW. The lattice structure is visible, and the crystalline phase (WZ) is evidently continuous across the quantum well. Figure obtained with permission from authors [38].

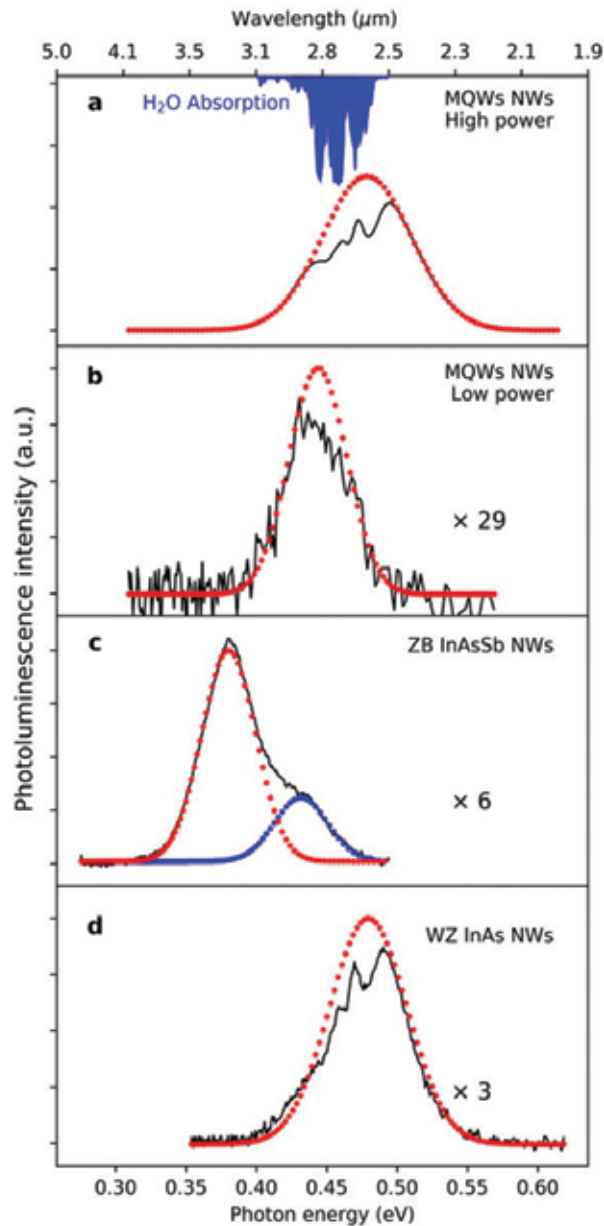


**Figure 3.** (a) STEM image showing the distribution of In, As and Sb obtained from 2D EDXS mapping and the resulting unusual faceted conical shape of the InAsSb MQW and (b) the unit cell of the WZ crystal structure showing the possible growth planes for the InAsSb MQW facets. Figure obtained with permission from authors [38].



#### 4. 4-K micro-PL measurements

The 4-K PL emission from the MQW was studied using temperature-dependent micro-photoluminescence spectroscopy. Introducing the InAsSb MQW significantly changes the

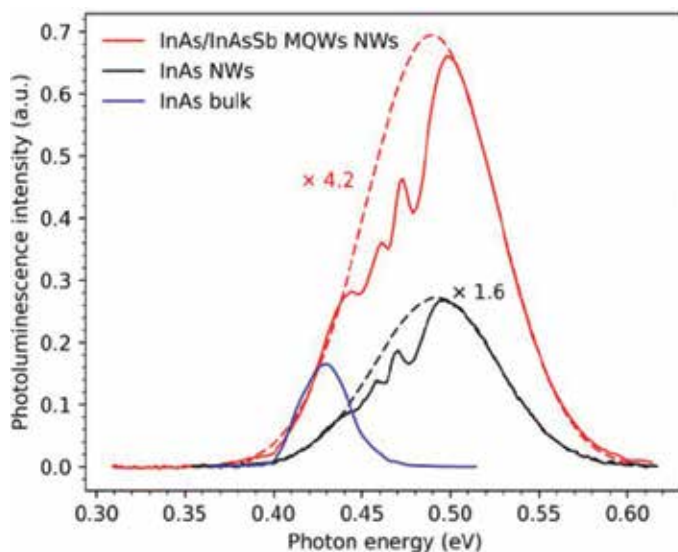


**Figure 4.** Micro-PL spectra obtained at 4 K. The emission spectra measured from (a) InAsSb/InAs MQW NWs at high power ( $1.2 \times 10^4 \text{ W cm}^{-2}$ ), (b) InAsSb/InAs MQW NWs at low power ( $80 \text{ W cm}^{-2}$ ), (c) InAsSb NWs at ( $1.2 \times 10^4 \text{ W cm}^{-2}$ ) and (d) InAs NWs at ( $1.2 \times 10^4 \text{ W cm}^{-2}$ ). The dotted lines represent Gaussian fits to the spectra, which also reveal the atmospheric absorption from water vapour [49] in this spectral region, as shown in (a). Figure obtained with permission from authors [38].

PL emission characteristics of the NWs in a number of ways. A comparison of the spectra measured at 4 K from the InAsSb MQW NWs, bulk alloy InAsSb NWs and InAs NW samples is shown in **Figure 4**.

**Figure 4(a)** and **(b)** shows the PL emission from the InAsSb MQW NWs at high and low excitations, respectively. The emission from the bulk InAsSb NWs is shown in **Figure 4(c)** and are deconvoluted into peaks at 0.380 eV corresponding to 6% Sb with a dominant ZB phase in agreement with previous work [46], and a shoulder on the main peak originating from ZB InAs which appears as the dominant phase in the early stage of all the NW growths [47].

The emission from the InAs NWs peaks at 0.482 eV is shown in **Figure 4(d)**, demonstrating a dominant WZ phase [3, 33]. The PL emission of the InAsSb MQW NWs collected at high and low excitations exhibits a clear increase in peak emission energy with respect to the bulk InAsSb NWs. This is due to the strong carrier confinement within the quantum wells. In addition, the InAsSb MQW NWs also exhibit an increased emission intensity and a superior temperature-quenching behaviour compared with the bulk InAsSb NWs as expected. Most notably, the emission intensity is enhanced at all temperatures, due to the quantum confinement of electrons and holes. PL originates from type II spatially indirect recombination of electrons in the InAs layers with confined hole states in the InAsSb QWs, where the spatial separation helps in reducing non-radiative Auger recombination with a corresponding increase in radiative emission [48]. Single Gaussian fitting to the spectra reveals interference from characteristic atmospheric absorption by water vapour between 0.445 and 0.485 eV [10]. The spectra can be scaled to account for the reduced cross-sectional area of the nanowire samples, where only 7% of the surface area is covered by the NWs, assuming a 100% nucleation yield in the mask sites. Accounting for this reduced active area allows the most direct comparison of emission intensity.

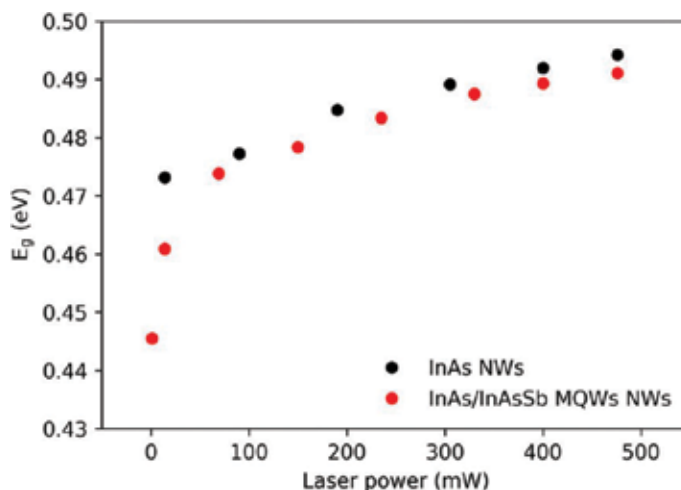


**Figure 5.** Comparison of emission intensities. PL spectra, under  $3.2 \times 10^4 \text{ W cm}^{-2}$  excitation, showing the relative emission intensities for MQW NWs, InAs NWs and an InAs bulk sample, scaled by active area. Figure obtained with permission from authors [38].

We observed that at the low excitation ( $0.011 \text{ W cm}^{-2}$ ), the MQW NW emission intensity is 4.2-fold enhanced with respect to a bulk InAs reference sample, compared to a 1.6-fold enhancement from the InAs NWs (see **Figure 5**). It must be noted that this comparison assumes a direct area proportionality for the optical pumping efficiency. However, it has been shown that the efficiency of optical absorption in nanowire arrays exhibits a spectral dependence arising from mode guiding, due to the geometry of the wire and the array, such that the peak field can occur either inside or outside the wires [50]. To a first order, this effect is defined by the wire diameter, and in prior work, a very similar effect has been reported from an array of InAsSb NWs in a photodetector [11], where the peak response was obtained at  $1.5 \mu\text{m}$  with an FWHM of 320 nm. Consequently, there is a non-optimal coupling with the 808-nm pump laser used in the present PL studies, and hence, further enhancement of PL emission intensities is to be expected if the pump laser wavelength is correctly matched to the NW geometry.

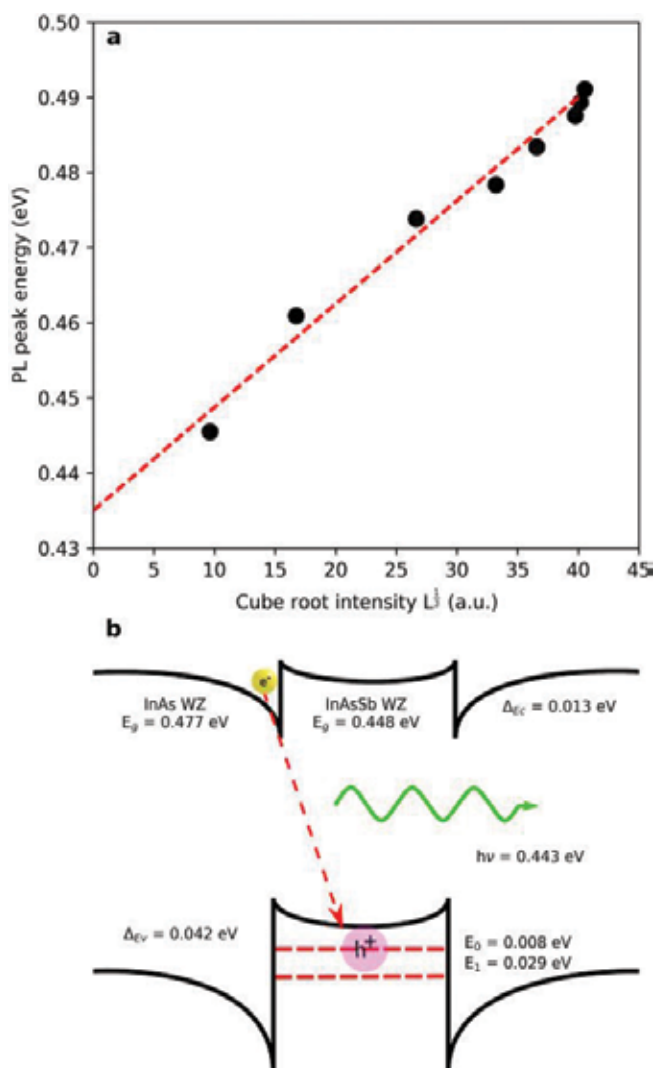
## 5. PL power dependence

Under low excitation conditions, bulk ZB InAs at 4 K normally exhibit characteristic emission from bound exciton and donor-acceptor transitions around 0.417 and 0.374 eV, respectively [51]. The high excitation intensity ( $\sim 0^4 \text{ W cm}^{-2}$ ) in our micro-PL experiments results in state filling such that a single InAs peak is observed at 0.425 eV. In the present case, the InAs NW emission is further blue-shifted with respect to the bulk ZB reference sample, due to the WZ crystal structure of the NW, with a peak emission energy ranging from 0.469 eV under low excitation, to 0.485 eV under high excitation, see **Figure 6**. The band gap for WZ InAs is known to be higher than that of ZB InAs, and our result is consistent with earlier studies of WZ InAs NWs which reported band gaps in the range of 0.477–0.540 eV [12, 32].



**Figure 6.** Power dependence of PL emission. The dependence of the peak emission energy on the power of the pump laser incident on the sample, for InAs and InAsSb/InAs MQW NWs, showing the difference in the blue shift between the pure InAs NW, with minimal quantum confinement effects and the MQW NW, with a strong quantum confinement and charging effects. Figure obtained with permission from authors [38].

Quantum confinement-induced blue shifts have also been observed as the diameter of InAs NWs is reduced [14]; however, in the case of 100-nm diameter wires, the shift is rather small  $\sim 5$  meV. Hence, the emission observed from the InAs NWs grown in this work is in good agreement with earlier reports. We envisage modest size-related confinement effects in the InAsSb MQW NWs. The PL emission energy from the InAs NWs is blue-shifted by 21 meV over the range of pump powers used in our experiments, which is similar to that obtained by others [14] and is associated with band filling. The commensurate shift for the MQW NWs is much larger, at 45 meV (see **Figure 6**). The majority of this blue shift occurs at low pump powers before the dependence becomes similar to that for pure InAs wires at higher powers. The blue shift in the MQW nanowires arises due to band-bending effects characteristic of type II QWs



**Figure 7.** Determination of the flat-band transition energy at the QW. (a) PL peak energy versus cube root of integrated PL intensity, elucidating the charging of the type II QW and allowing the flat-band transition energy to be identified and (b) the calculated band diagram for the InAsSb/InAs QW and photon energy, showing the band bending and triangular well formation. Figure obtained with permission from authors [38].

and originates from Coulombic attraction between localised holes in the InAsSb quantum well attracting electrons from the adjacent InAs barrier forming triangular quantum wells.

An increase in excitation intensity will raise the steepness of the confining potential and consequently the electron quantisation energy  $E$ , with a typical  $\Delta E \sim L^{1/3}$  behaviour. Accordingly, the flat-band transition energy of the InAsSb/InAs MQW can be extracted from the intercept of **Figure 7(a)**—the PL peak position versus  $L^{1/3}$ . Consequently, the flat-band transition energy for the InAs/InAsSb MQW NWs is obtained as 0.438 eV which is in good agreement with the calculated transition energy of 0.443 eV, as shown in the schematic band diagram in **Figure 7(b)**.

## 6. Development of a band structure for the MQWs

Developing a band diagram for the InAs/InAsSb MQW NWs is not straightforward, due to limited data describing band gaps and alignments for the Wurtzite phases. In particular, pure  $\text{InAs}_{1-x}\text{Sb}_x$  nanowires take on a predominately zinc-blende structure for  $x > 4\%$ , precluding the measurement of Wurtzite band gaps at higher antimony fractions. By contrast, the  $\text{InAs}_{1-x}\text{Sb}_x$  growth in this work maintains its Wurtzite structure due to the growth being on the (10 $\bar{1}$ 1) facets of the Wurtzite InAs wire. To approximate the band diagram in this absence of reported parameters, we start with the band alignment for a comparable zinc-blende structure, calculated taking account of the Sb fraction and the strain within a (111) orientated nanowire. The band gap of Wurtzite InAs is taken to be 60 meV greater than that for InAs zinc-blende ( $E_{\text{Gwz}}$ ), with the noted caveat that there is a lack of consensus in the study. The value of the band gap for the wurtzite  $\text{InAs}_{1-x}\text{Sb}_x$  can be calculated using the quadratic approximation:

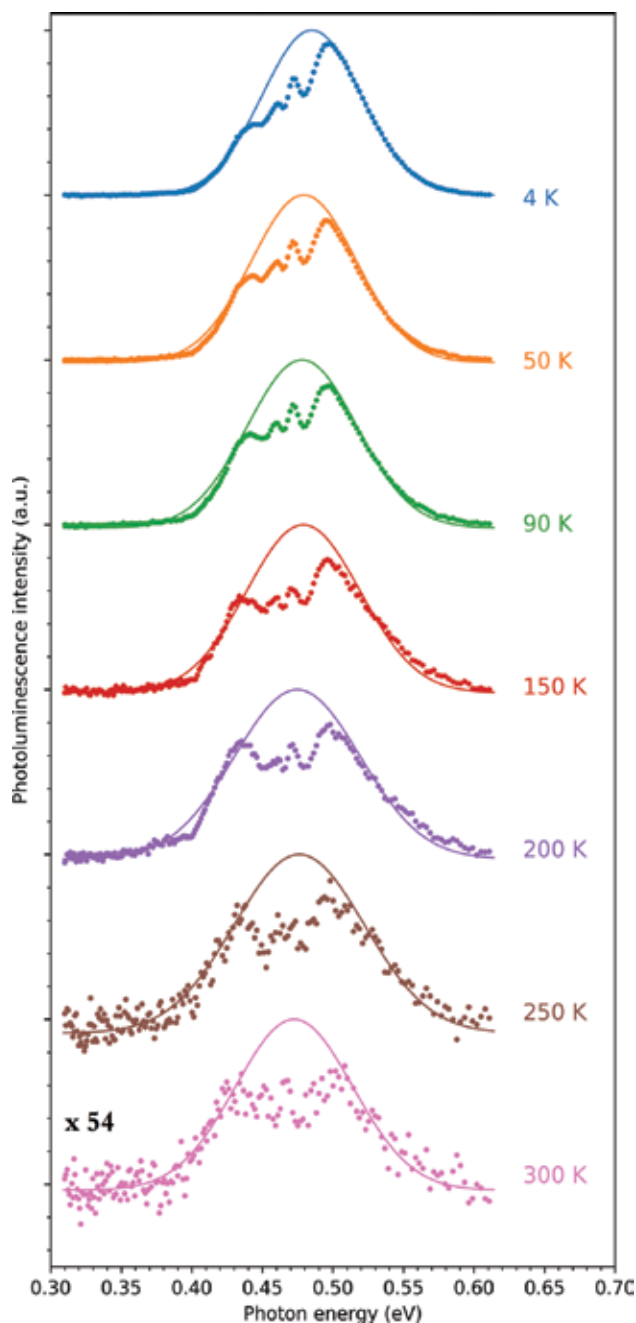
$$E_g(\text{InAs}_{1-x}\text{Sb}_x) = xE_{\text{gWZ}}(\text{InAs}) + (1-x)E_{\text{gWZ}}(\text{InSb}) + x(1-x)0.67 \quad (1)$$

where 0.67 is the bowing parameter for zinc-blende InAsSb and  $E_{\text{gWZ}}(\text{InSb})$  is the band gap of wurtzite InSb taken as 0.287 eV [52]. This gives  $E_g(\text{InAs}_{0.93}\text{Sb}_{0.07}) = 0.424$  eV which is 63 meV greater than the known value for the zinc-blende phase. This also agrees with results by Farrell et al. who showed that for  $x = 3.9\%$ , the band gap of Wurtzite  $\text{InAs}_{1-x}\text{Sb}_x$  was also ~60 meV greater than the known value for the zinc-blende phase.

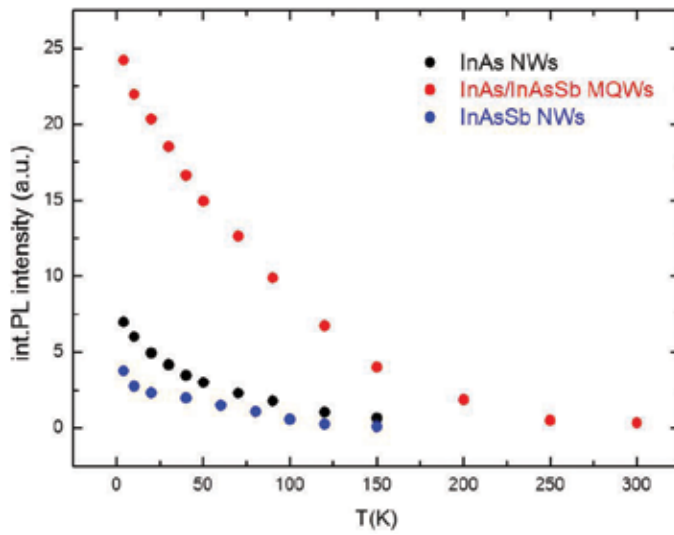
The confined hole states were calculated using a six-band k.p. solver in Nextnano. The first confined heavy hole state is calculated to be 8 meV above the band edge, as shown in the schematic energy band diagram in **Figure 7(b)** corresponding to a flat-band recombination transition energy,  $E_r$ , of 0.443 eV.

## 7. PL temperature dependence

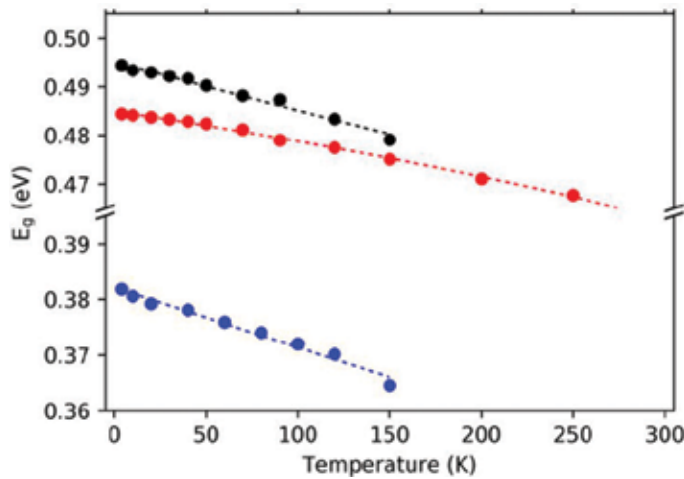
PL spectra obtained at different temperatures for the InAsSb MQW NWs are shown in **Figure 8**. Although the wires are not capped or passivated, they exhibit strong PL emission which persists up to room temperature. This indicates that radiative recombination occurs primarily in the MQW away from the near surface regions, which in InAs NWs are known



**Figure 8.** Temperature dependence of the PL emission spectra obtained from the InAsSb MQW NWs. Emission spectra measured over the range of 4–300 K using high excitation ( $2.6 \times 10^4 \text{ W cm}^{-2}$ ), showing the room-temperature emission required for future practical NW infrared emitters. The dotted lines indicate Gaussian fits used to extract the peak emission wavelength. Atmospheric water vapour absorption is again evident in all the spectra. Figure obtained with permission from authors [38].



**Figure 9.** Temperature-dependent analysis of PL data from the InAsSb/InAs MQWs, InAsSb and InAs NW samples; the graph demonstrates quenching behaviour of the three NW sample-integrated PL intensities with increasing temperature. Figure obtained with permission from authors [38].



**Figure 10.** Temperature-dependent analysis of PL peak from the InAsSb/InAs MQWs, InAsSb and InAs NW samples. The temperature dependence of the PL peak emissions (points) was used to fit the Varshni relationships (solid lines) and extract the associated coefficients, for the three NW samples. Figure obtained with permission from authors [38].

to be accumulated due to Fermi level pinning, resulting in a low efficiency for radiative emission [53]. In our case, the quantum confinement of the MQWs allows room-temperature emission to be observed without passivation. The PL spectra are inhomogeneously broadened due to length variations in the NWs which also result in thickness variations in

the QWs. The atmospheric water vapour absorption is again evident near  $2.7 \mu\text{m}$  (0.459 eV) in all the spectra.

**Figure 9** presents a comparison of the temperature quenching of the NW samples, where the superior performance of the InAsSb MQW NWs is clearly evident due to the increase in the radiative emission rate and suppression of Auger recombination.

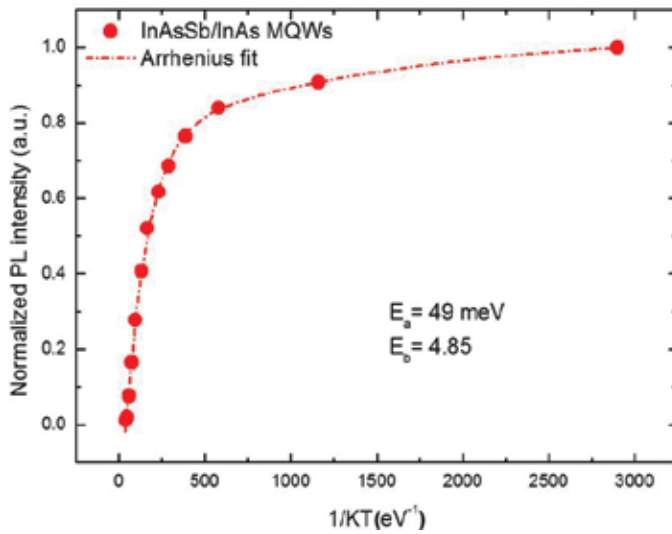
The temperature dependence of the peak energies for the NWs is shown in **Figure 10**. The dotted lines represent fitting of the results using the empirical Varshni equation. The values obtained for the fitting parameters ( $E_g(0)$ ,  $\alpha$  and  $\beta$ ) are given in **Table 1** along with reference values for bulk InAs; both the InAs and the InAsSb MQW NWs have a WZ crystal structure and consequently have a weaker dependence of band gap on temperature (lower value of  $\alpha$ ) than the corresponding bulk ZB materials.

An Arrhenius plot is shown in **Figure 11** for the InAsSb MQW NWs from which an activation energy of 49 meV was obtained from the high-temperature region and  $\sim 5$  meV for the low-temperature region. This is in approximate agreement with the confinement energy for thermal excitation of holes out of the QW (hole localisation energy of 34 meV) and electrons from the interface triangular QWs ( $<13$  meV localisation energy), respectively. The activation energy of  $\sim 49$  meV obtained from the Arrhenius plot is consistent with quenching due to carriers escaping confinement rather than Auger recombination and so provides indirect evidence for Auger suppression.

	$E_0$	$A$	$\beta$
	(eV)	(meV/K)	(K)
InAs NWs	0.494	0.126	47
InAsSb NWs	0.380	0.133	42
InAsSb/InAs MQWs	0.484	0.099	97
InAs bulk	0.417	0.276	93
InSb bulk	0.235	0.320	170

**Table 1.** Comparison of Varshni parameters. The fitted Varshni parameters  $E_g$ ,  $\alpha$  and  $\beta$  for the NW samples, compared with published parameters for bulk InAs and InSb [54].





**Figure 11.** Temperature-dependent analysis of PL emission intensity from the InAsSb/InAs MQWs sample shows an Arrhenius plot of integrated PL intensity as a function of inverse temperature for the InAsSb/InAs MQW NWs. The curve was used to extract activation energies for the mechanisms that drive thermal quenching. Figure obtained with permission from authors [38].

In type II QWs, the Auger rate is determined by the overlap integral (between initial and final electron states at small transferred momentum) which is a minimum when the valence band offset is about three times the conduction band offset. Meanwhile, the radiative rate does not depend on the final state of any excited carrier, since it is a two-body process, so the radiative rate can remain comparable with that in a type I QW systems [55].

## 8. Conclusion

The development of quantum structures in NWs systems has shown the potential to extend the concept of band-gap engineering to optimise the building blocks of such systems, as well as allowing integrating these systems into the leading CMOS technology, providing a promising future for nanotechnology in optics and electronics. This chapter has presented InAsSb MQWs heterostructures within InAs NWs which exhibit mid-infrared emission at room temperature. The type II QWs provided quantum confinement and spatial localisation of the carriers combined with a suppression of Auger recombination, resulting in enhanced PL emission with respect to the bulk InAs NWs. Furthermore, having characterised the effect of charging on the type II QW, the flat-band transition energy was found to be in good agreement with calculations for both samples. These new quantum-structured NWs will allow novel nano-photonic and quantum light sources to be developed for the technologically important mid-infrared spectral range. They can exploit both the general advantages of site-controlled NWs, such as integration with silicon substrates, and also enhance light-matter coupling based on their dimensions and geometry, opening the way for a wide range of applications.

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# Parasitic Capacitances on Scaling Lateral Nanowire

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Uttam Kumar Das and Tarun Kanti Bhattacharyya

Additional information is available at the end of the chapter

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## Abstract

The gate-all-around silicon nanowire transistor (GAA-NW) has manifested itself as one of the most fortunate candidates for advanced node integrated circuits (ICs). As the GAA transistor has stronger gate control, better scalability, as well as improved transport properties, the device has been considered as a potential alternative for scaling beyond FinFET. In recent publications, the basic feature and scalability of nanowire have been widely explored primarily focusing on intrinsic device characteristics. Although the GAA-NW has superior gate control compared to other architectures, the device is surrounded by huge vertical gate metal line and S/D contact metal lines. The presence of this vast metal line forms a strong parasitic capacitance. While scaling down sub-7 nm node dimensions, these capacitances influence strongly on the overall device performances. In this chapter, we have discussed the effects of various parasitic capacitances on scaling the device dimensions as well as their performances at high-frequency operations. TCAD-based compact model was used to study the impacts of scaling GAA-NW's dimensions on power performance and area gain perspective (PPA).

**Keywords:** CMOS scaling, GAA-NW, parasitic capacitances, compact modeling, TCAD, PPA

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## 1. Introduction

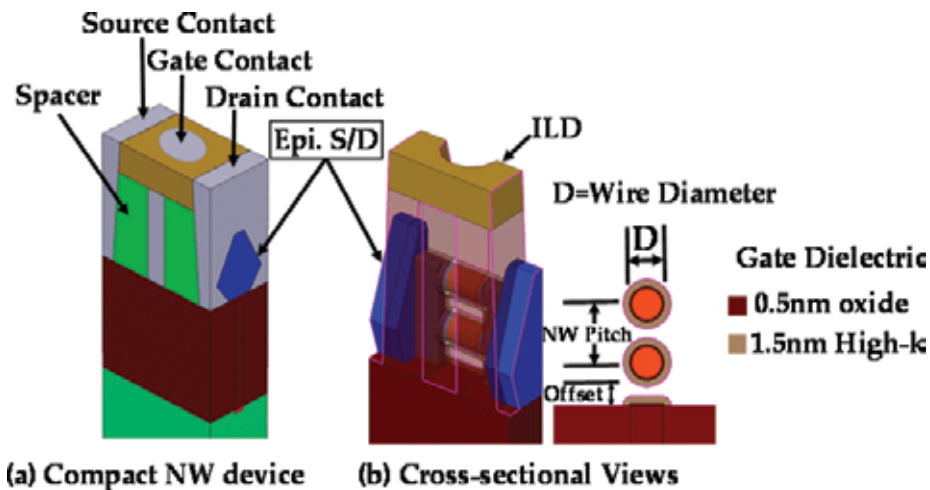
Since the beginning of solid-state technology, a continuous reduction of transistor size has delivered the smaller and faster electronic technology in every new generation. Since the last few decades, Moore's law [1] was gifted with many prime movers, such as the mobility boosted by strained silicon [2] and reduced gate leakage using the high-k/metal gate [3] and most importantly, planar MOSFET was replaced by the FinFET to achieve better leakage control [4]. Since then, the major industries followed FinFET technology for their 16/14 nm and 10 nm node, respectively [5–7]. However, while scaling down below the 10 nm node, again

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the short channel effects (SCE) such as subthreshold leakage rise significantly and became a major concern in scaling FinFET architecture. As per international technology roadmap for semiconductor (ITRS 2015) prediction, the contacted gate pitch (CGP) in 7 nm node transistor would be ~42 nm, making a gate length of less than 15 nm [8]. Although the FinFET has gate wrapping around the channel, at these shorter dimensions, a much stronger gate control is required. Therefore, gate-all-around (GAA) architecture has emerged as an alternative to FinFET [9]. The gate in NW covered all over around silicon channel providing a stronger control, therefore preventing more unwanted leakages. However, reduction of effective width ( $W_{eff}$ ) in NW reduces the current driving capacity significantly. Although the drive current in NW can be increased by stacking multiple wires per fin, however, a taller fin device increases parasitic capacitances which may limit the benefit of scaling [10]. Though numerous studies have been carried out for analyzing the intrinsic and parasitic capacitance [11–14], however, there is still a requirement for an extensive analysis to model the GAA-NW's major parasitic components. Thus, this chapter deals with the capacitance model of a GAA-NW transistor as well as the overall scaling performances at ring oscillator circuits.

## 2. GAA-NW device

To continue Moore's law, transistor sizes are scaled down to the 7 nm node (N7) and 5 nm node (N5) specifications [15, 16]. A contacted gate pitch (CGP) of 42 and 32 nm were used in both the N7 and N5 devices. Gate length ( $L_g$ ) of 14 and 10 nm with a wire diameter of 7 (D7) and 5 nm (D5) was considered in all the N7 and N5 specifications [10]. Channel material for n-channel and p-channel GAA-NW was considered with Si and  $Si_{50}Ge_{50}$ , respectively. An epitaxial-shaped source (S) and drain (D) regions were used as contacts. The S/D regions were doped with an active doping concentration of  $3 \times 10^{20}/cm^3$  for both n-channel and p-channel



**Figure 1.** Compact GAA-nanowire transistor with cross-sectional views. (a) Compact NW device. (b) Cross-sectional views.



devices with a specific contact resistivity of  $5 \times 10^{-9} \Omega\text{cm}^2$ . Gate dielectric of 0.5 nm oxide and 1.5 nm high-k ( $\text{HfO}_2$ ) layers was used. A gate spacer of 5 nm thickness was applied to both the gate-to-source and gate-to-drain regions (relative permittivity,  $\epsilon_r = 4.4$ ). A midpoint work function value for metal gate was used in both the devices during initial simulation. Considering a fixed NW pitch (NWP) of 14 and 10 nm offset, the two-stacked-NW device creates a fin height of 31 and 29 nm, respectively. Other specifications and setup for each device were considered similar to the reference [10, 15]. The simulated compact NW is shown in **Figure 1(a)** and a cross-sectional view showing inside details in **Figure 1(b)**.

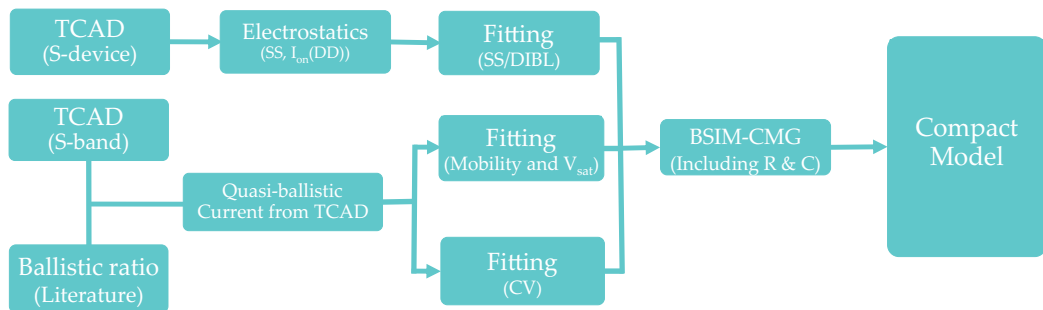
### 3. Simulation methodology

To estimate various parasitic capacitances in a GAA-NW and their impacts on overall scaling performances at a higher frequency, a TCAD-based compact model study was performed. **Figure 2** shows a flow diagram used to analyze the DC and AC performances of the scaled GAA-NW transistor.

A two-stacked GAA-NW transistor was implemented using the Sprocess module of TCAD tool Sentaurus [17]. Given a continuous channel in FinFET, the selective region was etched away to form a round-shaped NW channel [18]. The 7 nm width-based FinFET transformed into the 7 nm diameter-based GAA-NW. Then the diameter and the gate length of NW were scaled down further to 5 and 10 nm, respectively [10]. All the parameters for device simulation were considered similar to the default 7 nm FinFET model [17].

#### 3.1. Subthreshold current estimation

Electrical performances were estimated separately for both the off-state and for the on-state conditions. The Sdevice [19] simulation with Shockley-Read-Hall (SRH), auger, band-to-band tunneling (BTBT) recombination, bandgap narrowing, anisotropic density gradient, interface charge, mobility model with multivalley correction, thin inversion layer correction with high-k dielectric and quantum correction for the inversion layer were used to obtain the properties such as subthreshold slope (SS) and drain-induced barrier lowering (DIBL). Then, these



**Figure 2.** Flow diagram describes the TACD-based compact model of GAA-NW.

intrinsic properties (SS and DIBL) for different gate lengths, wire diameters and vertical pitches were fitted into the BSIM-CMG model [20]. Thus the fitted model provided subthreshold characteristics of a NW used for circuit-level simulations.

### 3.2. The drive current estimation

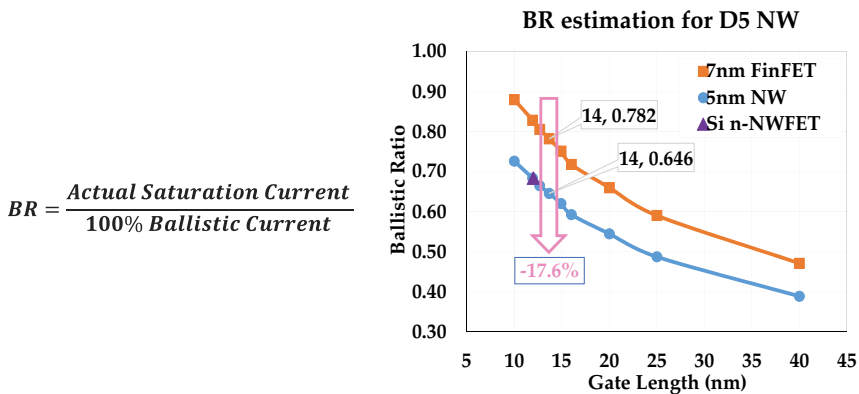
To estimate the on-state drive current, the ballistic flow has been considered along with drift-diffusion currents. As the channel length in N7 and N5 devices was scaled down to the carrier’s mean free path range [21], the total drive current in NW was considered to be quasi-ballistic in nature [21]. The scattering-free ideal ballistic current is defined by Eq. (1), where  $q$  is the electronic charge,  $V_{inj}$  is the carrier’s injection velocity and  $N_{inv}$  is the number of inversion charges. Sband [22] simulation provided these pure ballistic current characteristics for the different wire diameter and applied voltages

$$I_{Bal} = q \times V_{inj} \times N_{inv} \tag{1}$$

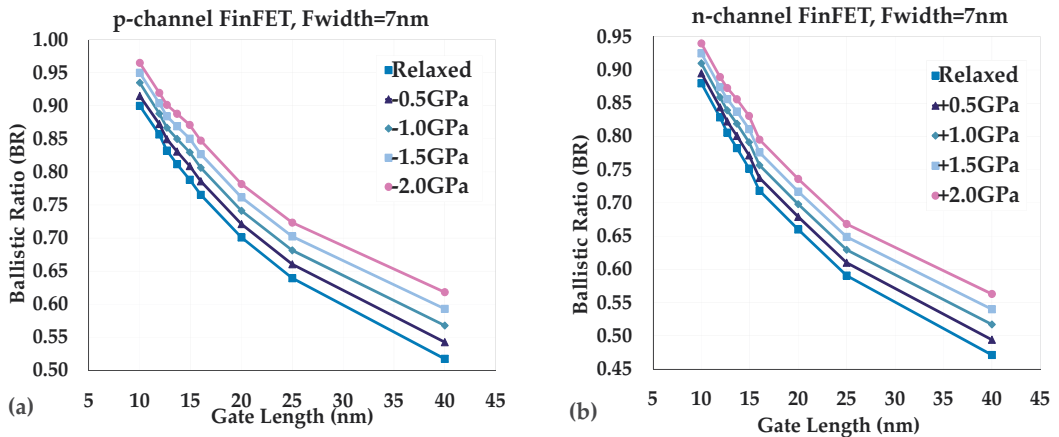
The quasi-ballistic current is represented by Eq. (2). Quasi-ballistic current is the product of pure ballistic current times the ballistic ratio. The ballistic ratio (BR) is the ratio between actual saturation current and ideal ballistic currents.

$$I_{quasi-ballistic} = I_{Bal} \times BR \tag{2}$$

The ballistic current is independent of gate lengths, but BR is strongly dependent on the gate length, wire diameter and channel stress [21]. The BR for D7 NW was assumed to be similar to a 7 nm width-based FinFET although the NW might have lower BR than FinFET; since it strongly depends on the body configuration of the device, this possible small error on BR was further screened by electrostatics and access resistance [10]. The BR for D5 device (**Figure 3**) was extrapolated because it is expected to have significantly lower BR than the D7, similar to [10]. The variations of BR with the applied channel stress and channel length reduction for



**Figure 3.** Ballistic ration (BR) considered for the 7 nm diameter (D7) based of LNW and extrapolated BR for the 5 nm diameter (D5) GAA-NW.



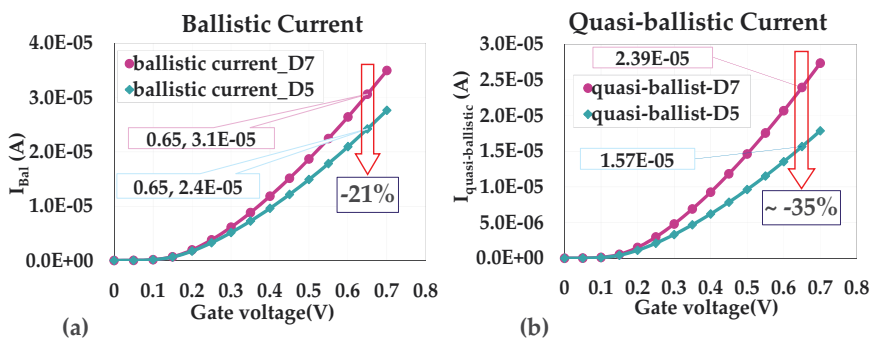
**Figure 4.** BR variation on varying channel stress in both the n-channel and p-channel FinFETs.

both the n-channel and p-channel FinFET (tensile for NMOS and compressive for PMOS) are shown in **Figure 4(a)** and **(b)** [23–26].

Next, for both the D5 and D7 devices, Sband simulated results such as carrier’s injection velocity ( $V_{inj}$ ) and the number of inversion charges ( $N_{inv}$ ) were multiplied with electronic charge ( $q$ ) to obtain the  $I_{Bal}$  as plotted in **Figure 5(a)** [10].

The variations of pure ballistic current ( $I_{Bal}$ ) with the gate voltages are plotted in **Figure 5(a)** and after multiplying with BR, the obtained  $I_{quasi-ball}$  is shown in **Figure 5(b)**. Then, these quasi-ballistic characteristics were fitted into the BSIM-CMG model by fitting the mobility and carrier’s velocity equations similar to [10]. As well as the intrinsic capacitances, interface trap and other device properties for each NW were fitted into the BSIM-CMG model.

The final drive current was obtained after including the front end of the line/mid of the line (FEOL/MOL) R&C parasitics into the BSIM-CMG model file [20]. Then, SPICE simulation was performed with this fitted model to obtain high-frequency properties for a 15th-stage ring



**Figure 5.** Ballistic characteristics: (a) simulated ideal ballistic currents for both the D7 and D5 n-channel GAA-NW, (b) quasi-ballistic currents considering the extracted BR.

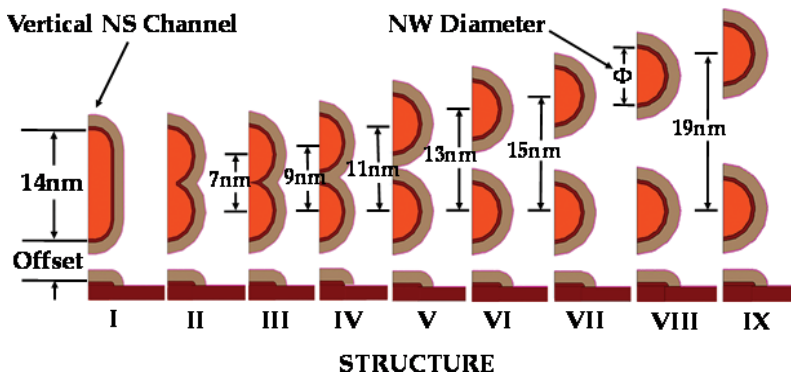
oscillator (RO) inverter circuits [27]. The ring oscillator setup is discussed in Section 6. All the simulations were performed at a fixed saturation drive voltage of 0.65 V and targeted off-state current ( $I_{\text{off}}$ ) of 3.5 nA per fin.

## 4. Parasitic capacitance estimation

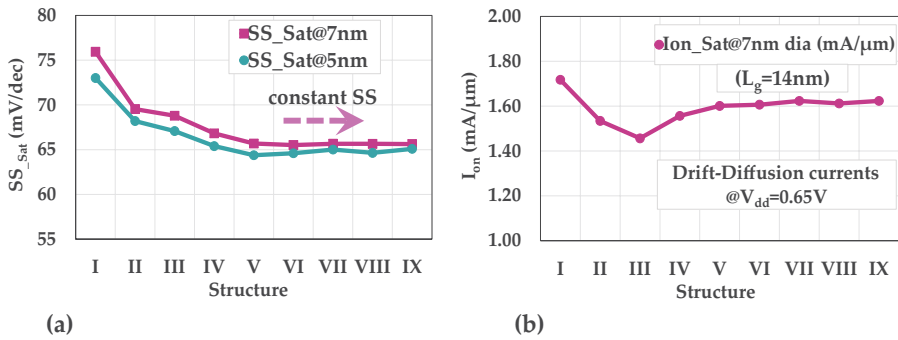
In this section, various parasitic capacitances associated with the metal gate of the NW transistors are discussed. We start with a basic fin channel architecture and then the formation of vertical nanosheet and then finally a GAA-NW with a higher vertical pitch. The vertical stacking of GAA-NW is essential for higher drive currents, but then again, the area associated with the wrapping metal lines is also increased as the fin height is increased, thus by doing so expected stronger parasitic capacitances.

### 4.1. Vertical pitch variation (fin to NW)

The transition from fin channel to GAA channel enables much stronger gate control. The bottom region of the channel fin was released and covered with the gate dielectric layers, as shown in **Figure 6(I)**, as a result of making the channel gate-all-around vertical nanosheet. Next, the single nanosheet was gradually transformed into two isolated NWs. Subsequently the vertical pitch between two NWs was increased from 7 to 19 nm (**Figure 6(III)–(IX)**). The transition from a single NS to multiple NW improves the sub-threshold slope (SS) characteristic due to a stronger control in all-around architecture. The variations of subthreshold slope (SS) for all these structures have been plotted in **Figure 7(a)**. The SS values are improved by shifting from **Figure 6(I)–(V)** but remain constant after **Figure 6(VI)–(IX)**. This is because, once a uniform metal layer is processed between two NWs, the work function fluctuations might be stabilized, thus providing a maximum SS improvement. However, the variations of active channel area in **Figure 6(I)–(IV)** also affect the drive currents. The deviation of  $I_{\text{on}}$  (drift-diffusion) for all the structures was



**Figure 6.** The channel regions cross-sectional view for GAA transistors: Fin to vertical nanosheet (NS) to higher pitch nanowires (NW).

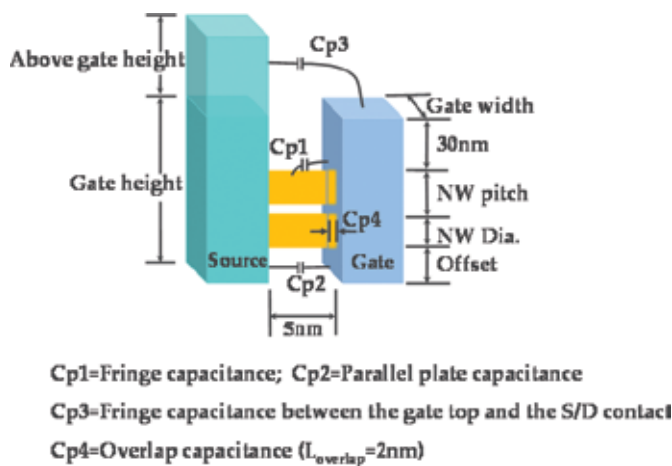


**Figure 7.** Electrostatics for all the structures shown in **Figure 6**: (a) sub-threshold slope (SS) variations, (b) change in currents flows due to the change in channel area.

plotted in **Figure 7(b)**. Thus it can be concluded that up to a certain fin height with higher vertical pitch, NW stacking improves SS compared to a continuous fin channel; however, SS benefit may be limited by the lowered active channel area along with the increased parasitic capacitances.

#### 4.2. The capacitance model

To analyze the overall parasitic capacitances on increasing the fin height, a schematic model depicting gate and source contact lines of a NW is shown in **Figure 8**. This model represents half of the NW architecture as a simplified model for calculating major capacitances associated with gate-to-source sidewall only. Similarly, the gate-to-drain sidewall capacitances can be calculated by reflecting this model as gate and drain contact sidewalls. At this point, the model represents four different major capacitances  $Cp1$ ,  $Cp2$ ,  $Cp3$  and  $Cp4$ . The capacitance  $Cp1$  represents the

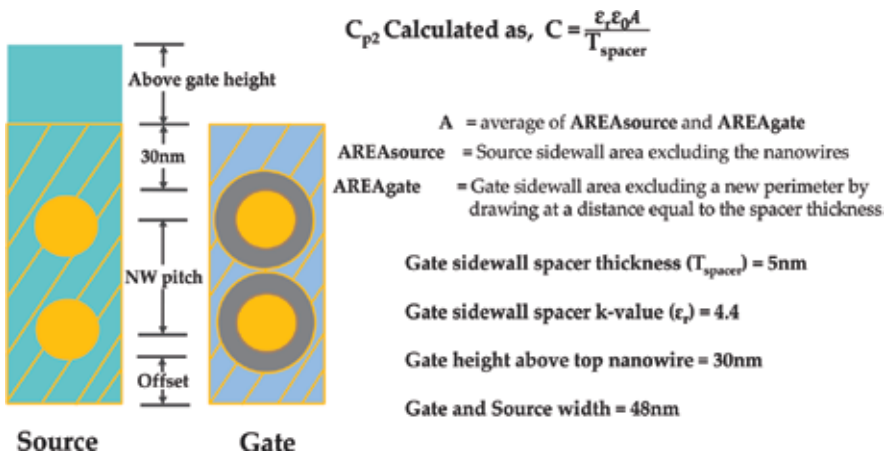


**Figure 8.** The capacitance model representing various parasitic capacitances.

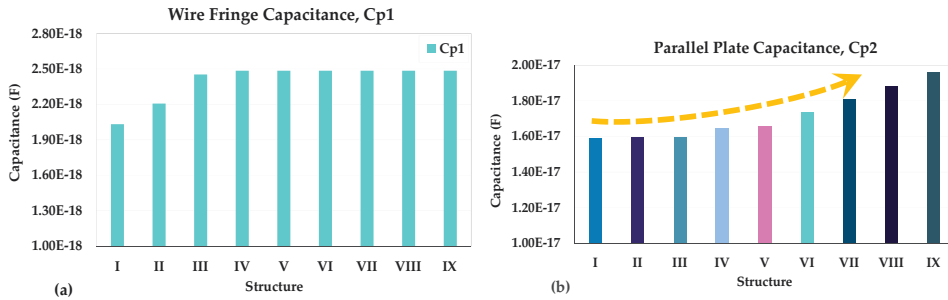
wire fringe capacitances between the gate sidewall and all the nanowire surfaces. The spacer between the source contact and gate contact line was considered to be ( $T_{\text{spacer}}$ ) 5 nm; this narrow spacing forms a strong parallel plate capacitance between gate and source. Therefore, the Cp2 represents the major parasitic capacitance between source sidewall and gate sidewall. The Cp2 is expected strongly dependent on NW's vertical pitch. The Cp3 is a fringe capacitance between the top gate surface and the source contact sidewall. And, Cp4 is an overlap (around 2 nm) capacitance between the nanowire and gate metal line. As we have seen previously, achieving a better electrostatic control with higher drive performance, the multiple stacking with higher vertical pitches are essential. However, both of these requirements increase the fin height as well as the Cp2. These two conflicting points need to be carefully optimized in both the process variation and for the best electrical performances. Among these capacitances, the Cp2 has a major contribution on overall device performance; thus, a detailed calculation methodology of Cp2 is presented here.

### 4.3. Parallel plate capacitance (Cp2)

In calculating the values of Cp2, a conventional parallel plate capacitance formula ( $\epsilon A/d$ ) was used, wherein  $d$  and  $\epsilon$  are the spacer width and permittivity, respectively. However, the active area ( $A$ ) was calculated by considering a special development. Area ( $A$ ) is the average of AREAsource and AREAgate wherein the AREAsource is the cross-marked source sidewall region after excluding the wire diameters as shown in **Figure 9**. And AREAgate is the cross-marked gate sidewall regions after excluding wire regions as well as by drawing a new perimeter related to the spacer width ( $T_{\text{spacer}} = 5 \text{ nm}$ ). This is mainly to exclude the contributions of continuous electric flux shared by two parallel plates through the connected NWs. The NW with two different wire diameters (7 nm = D7 and 5 nm = D5) was varied for different vertical pitches. Values of all the capacitances are calculated and presented in the next section.



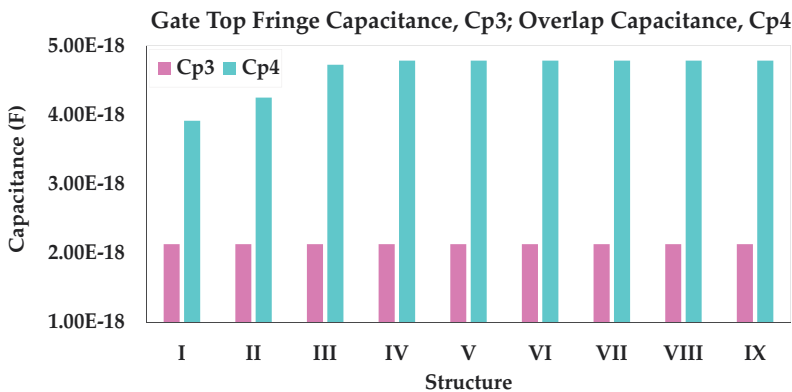
**Figure 9.** The cross-sectional side view of source and gate contacts regions for calculating the active area in Cp2.



**Figure 10.** Parasitic capacitance values: (a) the initial increasing trend of Cp1 but remains same after structure IV, (b) Cp2 trends to increase continuously with the vertical pitch.

#### 4.4. The capacitance values

The comprehensive model formula presented in papers [11, 12] is used to calculate all the parasitic capacitances. Considering the initial values from [12] the capacitances, Cp1, Cp2, Cp3 and Cp4 are plotted for all the architectures presented in **Figure 6**. **Figure 10(a)** shows the Cp1 variations with the vertical pitches. As per the active area shape changes in **Figures 6(I)–(IV)**, a continuous increase of wire fringe capacitance Cp1 was observed; however, it remains constant after **Figure 6(V)–(IX)** for the higher vertical pitches. The values of parallel plate capacitance Cp2 were calculated after considering the effective area of S/G sidewalls and have been plotted in **Figure 10(b)**. It was observed that, unlike Cp1, the values of Cp2 experience a continuous rise with increasing the vertical pitch from **Figure 6(I)–(IX)**. The overlap capacitance Cp3 remains constant as the top surface of gate metal and source contact is similar for all the structures (**Figure 11**). Cp4 shows increases till **Figure 6(IV)** and then remains constant for **Figure 6(V)–(IX)** (similar to the trend of Cp1). Although the overlapped region of 2 nm was considerably minor, the values of Cp4 show marginally higher than the values of fringe capacitance Cp1. Among all these



**Figure 11.** Fringe capacitance Cp3 and overlap capacitance Cp4 plotted in the same axis.

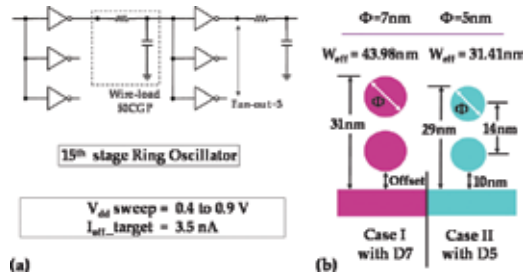
capacitances,  $C_{p2}$  witnessed the most significant parasitics which is strongly dependent on fin height (vertical pitch). The value of  $C_{p2}$  in **Figure 6(IX)** experienced a 22% higher capacitance than in **Figure 6(I)**.

## 5. Ring oscillator simulation

To benchmark the power performance and area gain for both the 7 nm diameter (D7)- and the 5 nm diameter (D5)-based GAA-NW, ring oscillator-level circuit simulations were performed [10]. Considering an inverter-based ring oscillator (RO), the RC delay and active power consumption as well as leakage power loss were calculated for each device. The ring oscillator (RO) with a 15-stage inverter was simulated using the SPICE simulation setup [27]. All the inverter stages had a fan out three and loaded with an optimum back end of the line (BEOL) load [28]. A median value of interconnect wire length for the critical path was loaded as the BEOL load including all parasitics in each inverter stage (50 CGP long) [28]. Then, a minimum delay optimization technique for a critical path was used to optimize the benefits of scaling D5 NW with various device configurations such as tighter vertical pitches and reduced gate lengths. The schematic of RO chain setup is shown in **Figure 12(a)**.

**Figure 12(b)** shows two-stacked D7 and D5 NW devices. Moving from D7 to D5 NW provides a 26% reduction in  $W_{\text{eff}}$  as well as a 2 nm reduction in fin height (with an equal vertical pitch of 14 nm). Reducing the wire diameter from D7 to D5 offers only 2 mV/decade and 4 mV/V SS and DIBL improvements [10]. However, reduction in  $W_{\text{eff}}$  delivers a significant reduction in overall drive current ( $\sim 35\%$ ) [10]. This reduction in drive current might be improved by reducing the NW's gate length from 14 to 10 nm as well as by stacking more numbers of NW per fin with a reduced vertical pitch. On the other hand, stacking multiple NW increases parasitic capacitances along with the fin height. In this section, we have discussed the benefit of scaling on power and speed performance for both the 7 nm node (D7/D5 @N7) and the 5 nm node (D5@N5) GAA-NW.

$$P_{\text{dyn}} = f C_{\text{eff}} V_{\text{dd}}^2 \quad (3)$$



**Figure 12.** Ring oscillator testbench: (a) the inverter chain with FO three (b) cross-sectional NW showing the wire diameters and various specifications.



### 5.1. Power-delay optimization at 7 nm node (N7) dimensions

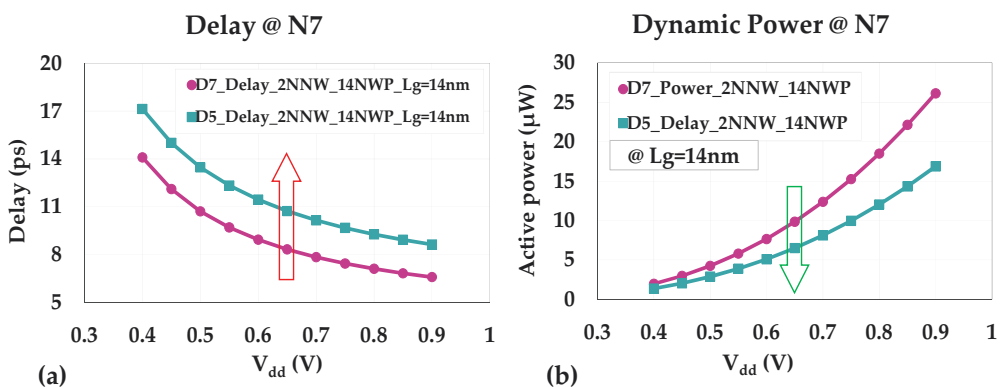
For all the 7 nm node (N7) specifications, a 42 nm CGP and 14 nm gate length were considered [10]. The dynamic power consumptions in an oscillator circuit were calculated based on the formula presented by Eq. (3), where  $C_{eff}$  is the effective load capacitance,  $f$  is the operating frequency and  $V_{dd}$  is the drive voltage. The RC delay was calculated in both the D7 and D5 devices based on the values obtained in [10].

The change in ring oscillator performances (RC delay and power loss) with the variation of  $V_{dd}$  from 0.4 to 0.9 V is displayed in **Figure 13**. The optimum delay versus drive voltage variation has been presented in **Figure 13(a)**. **Figure 13(b)** shows the active power loss for both the D7 and the D5 devices. Nearly a 20% rise in delay was observed for the D5 device compared to the D7 device, largely due to the reduction of drive currents. This reduced drive current (active area) in D5 device at the same applied voltage consumes less active power than the D7 device. However, the actual benefit can be visualized by plotting the power-delay product (energy) and leakage power loss for both the devices at a targeted frequency.

**Figure 14(a)** shows a change in energy consumption with the variations of frequency for both the devices. While operating at the same frequency, a significant rise in energy consumption was observed for the D5 device in comparison to the D7 device. To achieve an equal drive current in the D5 device always require a higher drive voltage in comparison to the D7 device, therefore consuming more energy leading to overall degraded performances. Besides that, while operating at the same frequency, the D5 device consumes more leakage power than the D7 device (**Figure 14(b)**).

### 5.2. Power-delay optimization at 5 nm node (N5) dimensions

Although the electrostatics were improved marginally by reducing the nanowire’s diameter, the overall performance was degraded at the same gate length. Therefore, in order to



**Figure 13.** Performance at 42 nm CGP: (a) delay for D7 and D5, (b) power for both D7 and D5 NW.

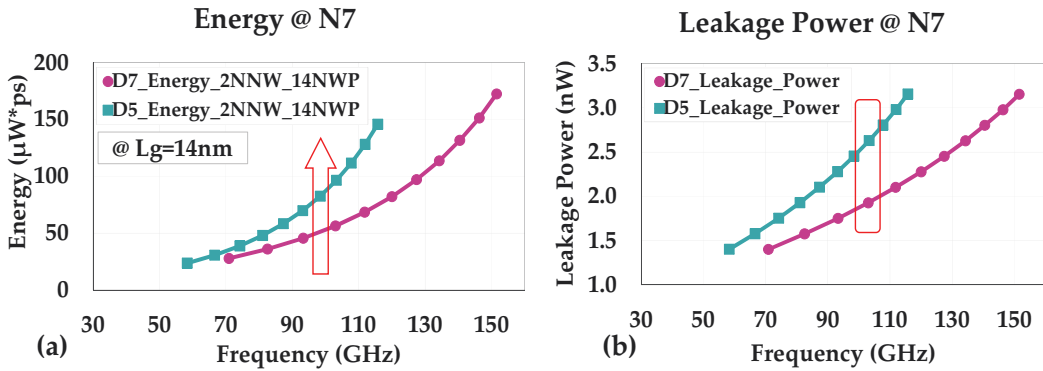


Figure 14. Speed estimation for both the D7 and D5 NW with: (a) energy, (b) leakage power.

understand the overall benefit of scaling the wire diameter, we scaled the gate length from 14 to 10 nm. At the 10 nm gate length, the D5 showed a significant SS improvement compared to the D7 device ( $\sim 10$  mV/decade) [10]. A 10 nm gate length and 32 nm CGP were defined in D5 NW for the N5 specifications. Furthermore, the vertical pitch between two D5 NWs was reduced to 12 nm. To equalize the drive current, a four-stacked D5 NW at N5 specifications was compared with the two-stacked D7 NW at N7 specifications with increased BEOL load. In this case, the D5 shows an improved delay performance compared to the D7 device (Figure 15(a)). It should be noted that the D5 consumes more active power than the D7 device, as shown in Figure 15(b). These minimal improvements in delay and power were further normalized by the energy versus speed performances, as shown in Figure 16(a). Figure 16(b) shows the comparable leakage power characteristics for both the devices. Though, after considering taller fin in the D5 case, the power-delay product shows a comparable trend for low-frequency operation, however, it starts consuming more energy as it goes toward high-frequency operation. Now, with these NW configurations, a fin height in four-stacked D5

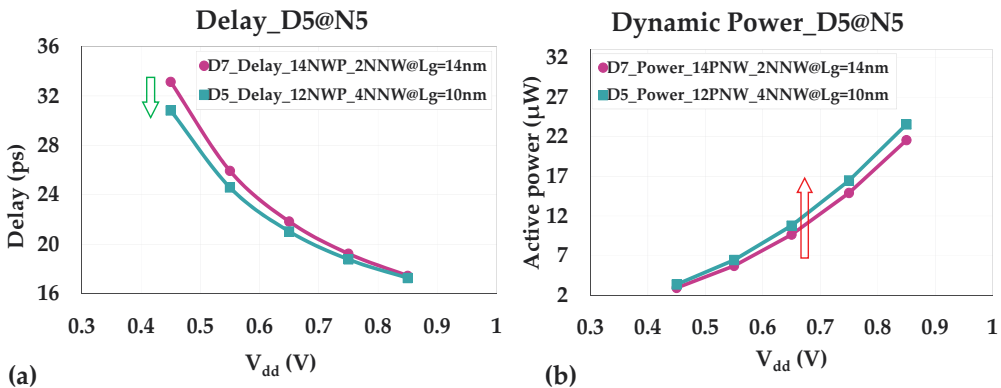
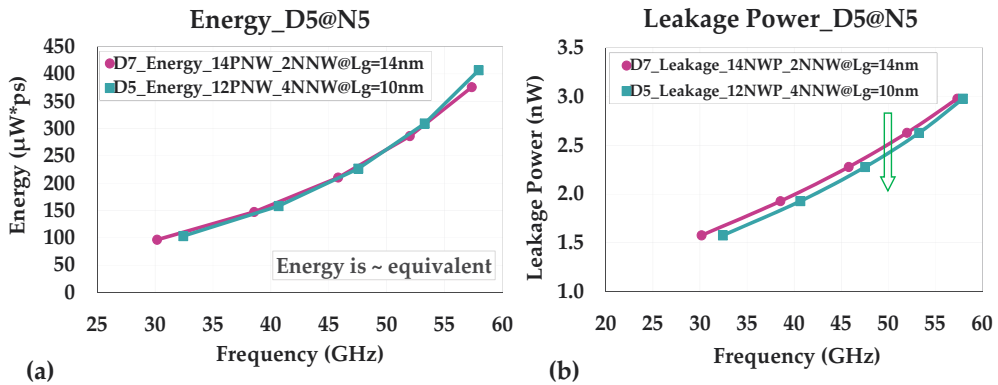


Figure 15. Performances (D5@32 nm and D7@42 nm CGP): (a) delay versus  $V_{dd}$ , (b) power versus  $V_{dd}$ .



**Figure 16.** Performance (D5@32 nm and D7@42 nm CGP): (a) energy consumed with intensifying frequency and (b) leakage power loss with the frequency deviations.

devices grows an additional 14 nm taller than a two-stacked D7 device adding an additional parasitic capacitance as expected.

This flexibility shows the various aspects of scaling the D5 NW at a reduced wire pitch along with a greater number of NW (NNW) stacking. The overall performance benefits for a D5 device at N5 specifications and D7 device at N7 specifications were benchmarked with the variations in fin height. In the next section, we discussed the energy versus frequency for different NW alignments.

### 5.3. Benchmarking D5 NW at N5 and D7 NW at N7

A three-stacked D5 device delivers nearly equivalent drive currents with a two-stacked D7 device (only 6% loss) [10]. Subsequently, using D5 NW the drive current can be increased by stacking a greater number of wires per fin as the D5 may provide an area benefit with a 32 nm CGP. On the other hand, stacking more numbers of D5 NW will increase the parasitic capacitance (mainly Cp2) in comparison to the rise in gate capacitances (FEOL). The fin height variation with different NW stacking is shown in **Figure 17**. When moving from two-stacked to five-stacked NW D5 NW device, a 91% rise in drive current has been observed (18.03–34.44 μA), but this increases the total fin height by 90% (from 29 to 55 nm). On the other hand, moving from two-stacked to three-stacked D7 NW device (14 nm wire pitch) improves the drive currents by 30.5% (25.95–33.87 μA), but the fin height is increased by 45% (from 31 to 45 nm) only. The three-stacked D7 NW delivers almost equal drive currents with the five-stacked D5 NW at much lower fin height (33.87 and 34.44 μA). Thus, after considering all the parasitic impacts, a fair comparison is needed to observe the actual benefit of scaling wire diameter (D5).

Optimizing the fin height in a NW transistor remains very sensitive to overall device design. Keeping this in mind, the total energy consumptions per device with increased fin height were plotted in **Figure 18** for both the D5 and D7 devices with multiple stacking. For both

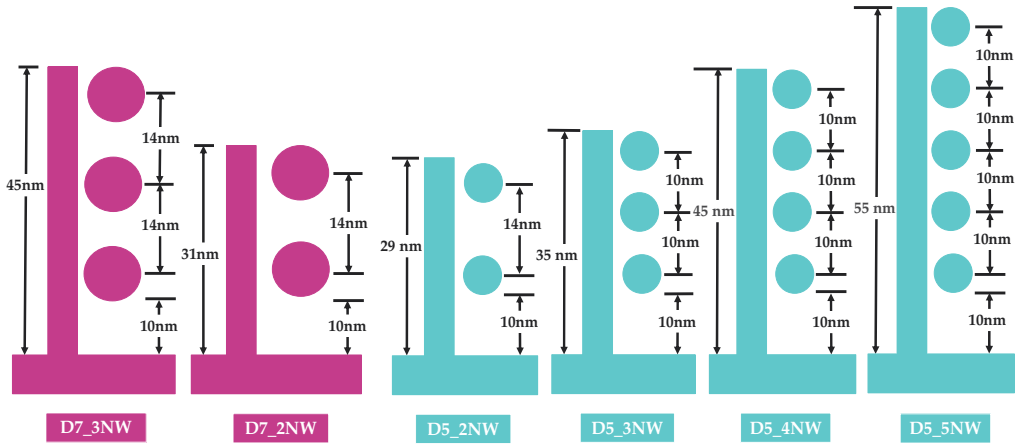


Figure 17. The cross-sectional NWs schematic showing upswing in fin heights with multiple stacking of NWs.

the D7 and D5, a single-stacked NW to a five-stacked NW was considered. Along with the multiple stacking, three different vertical pitches (16, 14 and 12 nm) were varied for the D7 device and four different pitches (16, 14, 12 and 10 nm) for the D5 device. A continuous rise in energy consumptions was observed while going from single NW to multiple NW in both the D7 and D5 devices. The fin height has been reduced by reducing the NW vertical pitch, thus shrinking the energy loss. It was observed that the D7 NW always consumes higher energy than the D5 NW at specific fin height. However, with a lowered fin height, D5 NW delivers lower drive currents which further reduces the overall device speed.

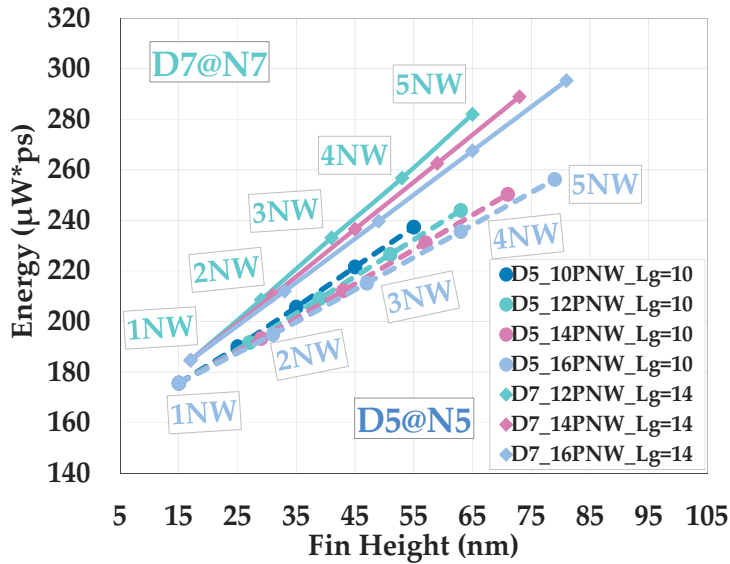
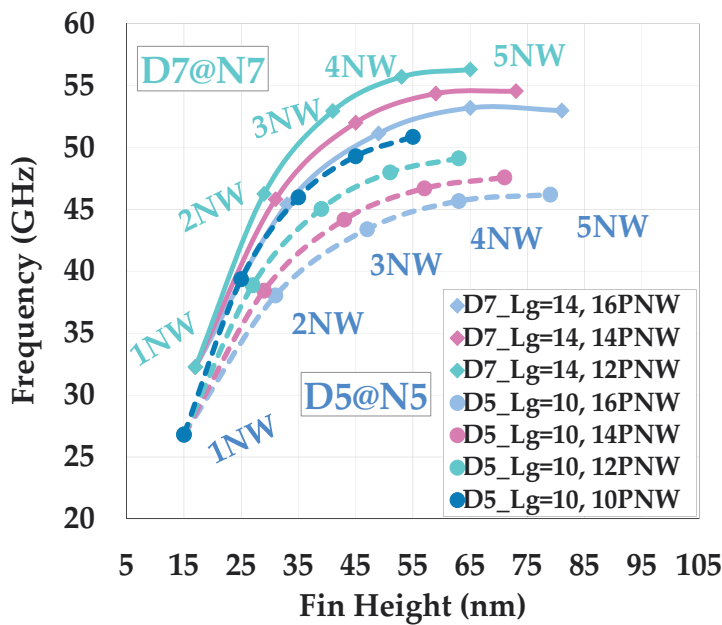


Figure 18. Variations of energy with varying fin height from single stacked to five stacked NW.

The variations of speed (frequency) with increasing the fin height for both D7 and D5 devices are plotted in **Figure 19**. At an identical fin height, the D7 device always delivers higher speed than the D5 device. Hence, considering both the D7 and D5 NW transistors, the fin height was increased by stacking single wire to five multiple wires. Until a three-stacked design for both the D7 and D5 NWs, a linear rise in frequency with fin height was observed. However, the frequency gets saturated with increasing fin height beyond the three-stacked NW design. Further rise in fin height will start degrading the frequency performance. This is mainly caused by the rise in parasitic capacitances. Generally, the capacitance loaded with a transistor is the sum of both intrinsic gate capacitance and FEOL parasitic capacitances. Increasing the fin height increases the total area of gate metal stack positioned next to a source and a drain contact lines. This effect remains hidden in both the D7 and D5 NWs up to a certain fin height. At the same fin height, the D7 drives have slightly higher speed in comparison to the D5 device as it has lower parasitic influence. Furthermore, increasing the fin height started degrading the total speed performance of D5 device quite early. Hence multiple stacking is also a major limiter for providing a higher speed.

Along with the speed, the total energy consumption with different frequency operations is also plotted in **Figure 20**. The energy consumptions are largely affected by the escalation of parasitic capacitances. Due to that reason, it starts consuming more energy while raising the operating frequency. Moving from single NW to five-stacked NW experiences an exponential rise in energy loss in both the devices. Although the single-stacked D5 NW consumes 27% lower energy than the single-stacked D7 NW, it also provides 20% reduction in speed performance. This gap however is quickly normalized while operating at higher



**Figure 19.** Frequency variations with the fin height from single stacked to five stacked NW.

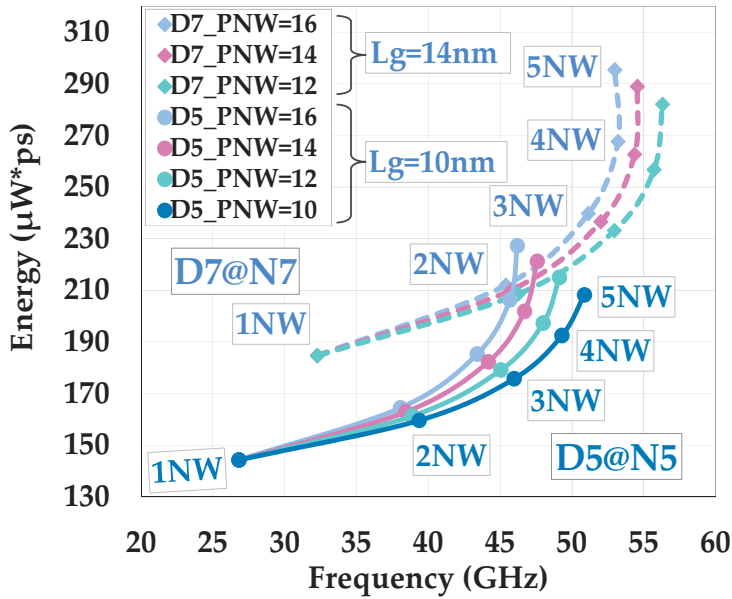


Figure 20. Energy versus frequency variations for both the D7 and D5 NW transistors.

frequency. Around 45–50 GHz operation, the D5 device requires four- to five-stacked NW and starts consuming more energy than the two- to three-stacked D7 NW. We have observed that the D5 NW at N5 specification consumes lower energy at the low-frequency operations, but it starts consuming higher energy than the D7 device at high-frequency operation. Multiple stacking increases both on currents as well as parasitic losses. Thus, the NW with shorter fin height should be considered to achieve the benefit of scaling at low-frequency performance.

## 6. Conclusion

The GAA lateral nanowire is a promising candidate for scaling beyond the FinFET technology. It provides superior electrostatic benefits such as better SS and DIBL at the shortest gate length. However, it has a very complex process as well as huge impacts of parasitics. The study conducted here was mainly focused on the effects of all the parasitic capacitances on overall device performance with scaling beyond the 7 nm node dimensions targeting the 5 nm node technology. The TCAD study compared the electrostatic performance and compact modeling which shows a basic inverter operation connected as a ring oscillator. A capacitance model was discussed considering the major parasitic components and their impacts on advance dimensional scaling. The compact modeling showed that reducing the wire diameter improved the electrostatics marginally but degraded the overall performance. Therefore, a deep analytical and experimental study is required to conclude the overall scaling benefits of GAA-NW transistor.

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# Magnetoresistance and Structural Characterization of Electrospun $\text{La}_{1-x}\text{Sr}_x\text{MnO}_3$ Nanowire Networks

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Additional information is available at the end of the chapter

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## Abstract

Nanowire network fabrics of  $\text{La}_{1-x}\text{Sr}_x\text{MnO}_3$  (LSMO) with different doping levels  $x = 0.2, 0.3, \text{ and } 0.4$  were fabricated by means of electrospinning. The resulting nanowires are up to  $100 \mu\text{m}$  long with a mean diameter of about  $230 \text{ nm}$ . The nanowires form a nonwoven fabric-like arrangement, allowing to attach electric contacts for magnetoresistance (MR) measurements. The resistance in applied magnetic fields and the MR effect were measured in the temperature range  $2 \text{ K} < T < 300 \text{ K}$  in magnetic fields up to  $10 \text{ T}$  applied perpendicular to the sample surface. An MR ratio of about  $70\%$  is obtained for  $x = 0.2$  at  $10 \text{ T}$  applied field and  $T = 20 \text{ K}$ . The highest low-field MR of  $5.2\%$  ( $0.1 \text{ T}$ ) is obtained for the sample with  $x = 0.2$ . Magnetization measurements reveal the soft magnetic character of the samples. A thorough analysis of the microstructure of these nanowire networks is performed including scanning electron microscopy (SEM) and transmission electron microscopy (TEM).

**Keywords:** LSMO, electrospinning, nanowires, magnetoresistance, microstructure

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## 1. Introduction

Colossal magnetoresistance (CMR) is a property of some materials, mostly manganese-based perovskite oxides that enables them to dramatically change their electrical resistance in the presence of a magnetic field, i.e., magnetoresistance (MR) [1–3]. To bring the CMR materials toward applications, it is still necessary to further optimize the sample processing to find the optimal microstructure, especially concerning grains in the nanometer range. The low-field

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magnetoresistance response (LFMR) of such manganese perovskite oxides like  $\text{La}_{1-x}\text{Sr}_x\text{MnO}_3$  (LSMO) with different doping levels  $x$  is closely connected to the existence of interfaces and grain boundaries (GBs) within the samples [4–7], and the high-field magnetoresistance (HFMR) was found to increase progressively on decreasing the grain size [8]. Therefore, the measurements of nanostructured or nano-sized samples are an important issue to provide a deeper understanding and further improvement of the MR effect, especially for the improvement of the behavior of devices based on the MR effect in reduced dimensions.

Commonly, the manganese perovskite samples studied in the literature are prepared as bulks or as thin films, mainly on  $\text{SrTiO}_3$  substrates. Nanometer-sized bridges are then prepared using lithography techniques or the focused ion-beam technique [9, 10]. The use of a substrate in the case of thin-film samples is always causing strain effects within the functional layer due to the lattice mismatch, which may play an important influence on the resulting magnetic properties [11]. The situation may be considerably different in nanostructures without substrate, being recently investigated in several types of nanoscale composites [12–14]. These nanostructures include nanorods, nanowires, nanotubes, and nanobelts; all of them having specific physical properties depending on the chosen preparation route.

In the present contribution, we have fabricated nanowire network fabrics of  $\text{La}_{1-x}\text{Sr}_x\text{MnO}_3$  (LSMO) with different doping levels  $x$  by means of the electrospinning technique [15–17]. This technique is common for the fabrication of organic polymer nanostructures but can be modified by employing different precursors to deliver inorganic compounds. Up to now, only a small number of reports are dealing with magnetic nanostructures prepared in this way [17–19]. In the case of LSMO, exhibiting the CMR effect, the resulting nanowires are of polycrystalline nature with a high-aspect ratio (length up to 100  $\mu\text{m}$  and diameters of about 230 nm) and show a large number of grain boundaries (GBs) within each individual nanowire. In Ref. [19], individual nanowires were separated from the as-spun networks and placed on a pre-patterned substrate. Electric contacts were prepared using Ti/Cu electrodes, and the nanowires could be measured individually, allowing to observe a dependence of the MR on the nanowire diameter. Single nanowires of CMR materials may be employed as sensitive gas sensor elements or electrodes [20–22].

However, the as-spun nanowires form a nonwoven fabric-like network, where numerous interconnects between the individual nanowires are formed in the final heat treatment step. As a result, the current flow through such a nanowire network fabric shows percolative character, and several sub-loops can be formed. The interconnects between the individual nanowires add additional crossover points for the currents and can enhance the tunneling transport across the interfaces, together with the GBs. This additional scattering of the electrons at the interfaces provided by the interconnects is lost when measuring only extracted parts of the nanowires as done in Ref. [19]. Furthermore, no information on the LSMO grain size of their nanowires was presented. An analysis of the grain sizes within our nanowires showed values ranging between 10 and 32 nm. Therefore, it is obvious that the LSMO grains within the present nanowires are smaller as compared to, e.g., Ref. [8].

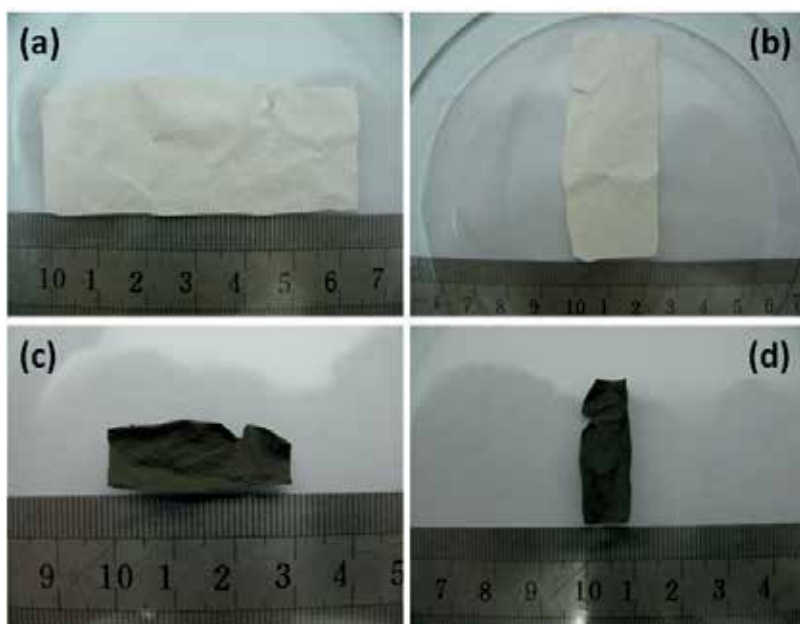
Therefore, we may expect interesting new properties of this new class of magnetic material. Furthermore, the nanowire network fabrics are an extremely lightweight material with a

density of about  $0.084 \text{ g/cm}^3$ , which is considerably less than the theoretical density of  $6.5 \text{ g/cm}^3$  [23]. Furthermore, there is no sample size limitation imposed by the fabrication technique, as electrospinning may produce very large sample sizes [24]. This makes such fabrics interesting for applications in bulk form, whenever the weight of the sample counts.

In order to achieve a better understanding of the transport properties through these nanowire network fabrics, we also performed a thorough microstructure analysis including scanning electron microscopy (SEM) and transmission electron microscopy (TEM).

## 2. Experimental procedures

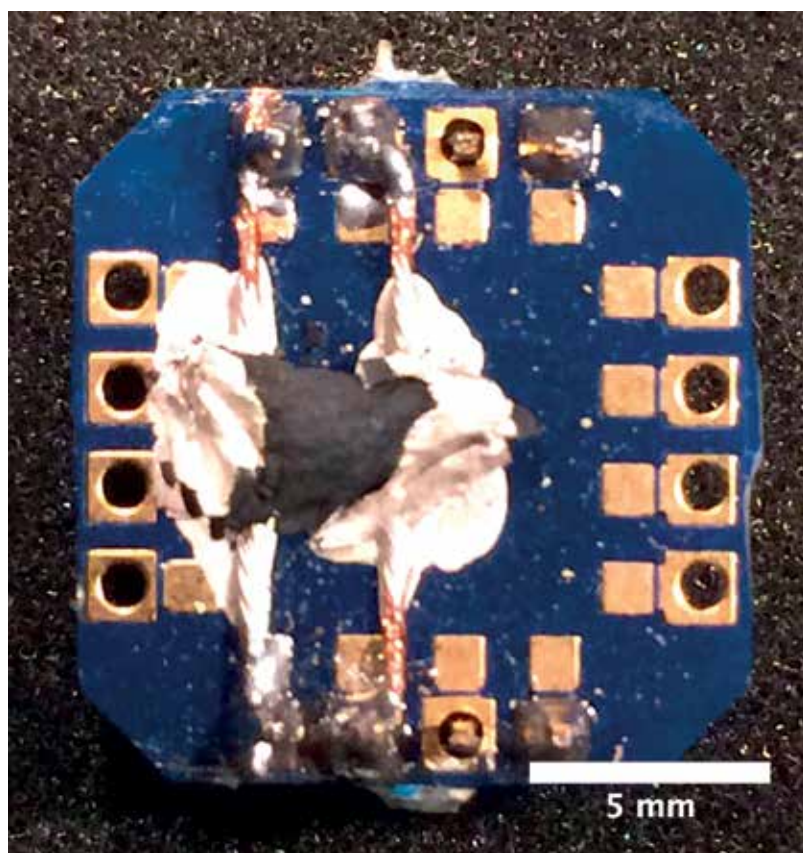
The electrospinning precursor is prepared by dissolving La, Sr, and Mn acetates in PVA (high-molecular-weight polyvinyl alcohol). The PVA is slowly added to the acetate solution with a mass ratio of 2.5:1.5. This solution is stirred at  $80^\circ\text{C}$  for 2 h and then spun into cohering nanofibers by electrospinning. To remove the organic compounds and to form the desired LSMO phase, the sample is subsequently heat treated in a lab furnace. An additional oxygenation process is required to obtain the correct phase composition. The constituent phase was checked by means of X-ray diffraction (XRD) and EDX analysis. Further details about the electrospinning process of ferromagnetic and superconducting nanowires are given elsewhere [25–28].



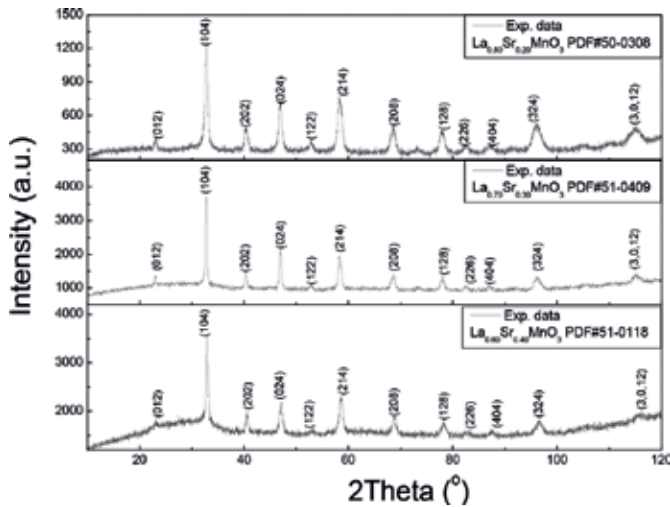
**Figure 1.** Images of the electrospun samples fabricated from the  $\text{La}_{0.8}\text{Sr}_{0.2}\text{MnO}_3$  precursor. Images (a) and (b) present a view of an as-prepared sample before thermal treatment, whereas images (c) and (d) give a  $\text{La}_{0.8}\text{Sr}_{0.2}\text{MnO}_3$  sample after the whole annealing process applied. As can be directly seen from the images, the size (area) of the sample shrinks to one sixth as compared to the original one after the thermal treatment.

**Figure 1(a, b)** presents photographs of an as-grown  $\text{La}_{0.8}\text{Sr}_{0.2}\text{MnO}_3$  nanowire fabric. The nanowire fabric consists of polymer nanowires containing the ceramic precursor material. The as-grown fabric has a white color, and the entire fabric sample is fully flexible. **Figure 1(c, d)** finally presents the fully reacted sample after having received the full thermal treatment. The reacted sample shows a fully black color, indicating the completed chemical reaction. As a result, the final nanowire network fabric is extremely thin and brittle. Here, it is important to note that the sample size shrunk to about one sixth of its original size. This shrinkage has to be considered for the application of such fabric-like materials. In the thermal treatment, numerous interconnects between the individual nanowires are formed, which are essential for the resulting current flow through the sample.

The entire nanowire network was electrically connected by means of silver paint and Cu wires (50  $\mu\text{m}$  diameter) to the sample holder. Due to the high fragility of the ceramic sheet, a pseudo four-point configuration is realized where the current and voltage links connect immediately on the sample contacts. This arrangement is presented in **Figure 2**. The magnetoresistance is measured in a 10/12 T bath cryostat (Oxford Instruments Teslatron) with a Keithley source meter (model 2400) as a current source, and the voltage is recorded using a Keithley 2001 voltmeter.



**Figure 2.** Nanowire network sample with electrical contacts for the quasi four-point measurement.



**Figure 3.** XRD measurements on all three types of LSMO nanowire fabric samples.

The constituent phases of the samples were determined by means of a high-resolution automated RINT2200 X-ray powder diffractometer using  $\text{Cu-K}\alpha$  radiation (40 kV, 40 mA) **Figure 3**. SEM imaging was performed using a Hitachi S800 scanning electron microscope operating at a voltage of 10 kV, and the TEM analysis was performed by a JEOL JSM-7000F transmission electron microscope (200 kV,  $\text{LaB}_6$  cathode). For TEM imaging, pieces of the nanowire network fabrics were deposited on carbon-coated TEM grids. High-resolution TEM and EBSD were performed on selected nanowire sections being thin enough for electron transmission (**Figure 6**).

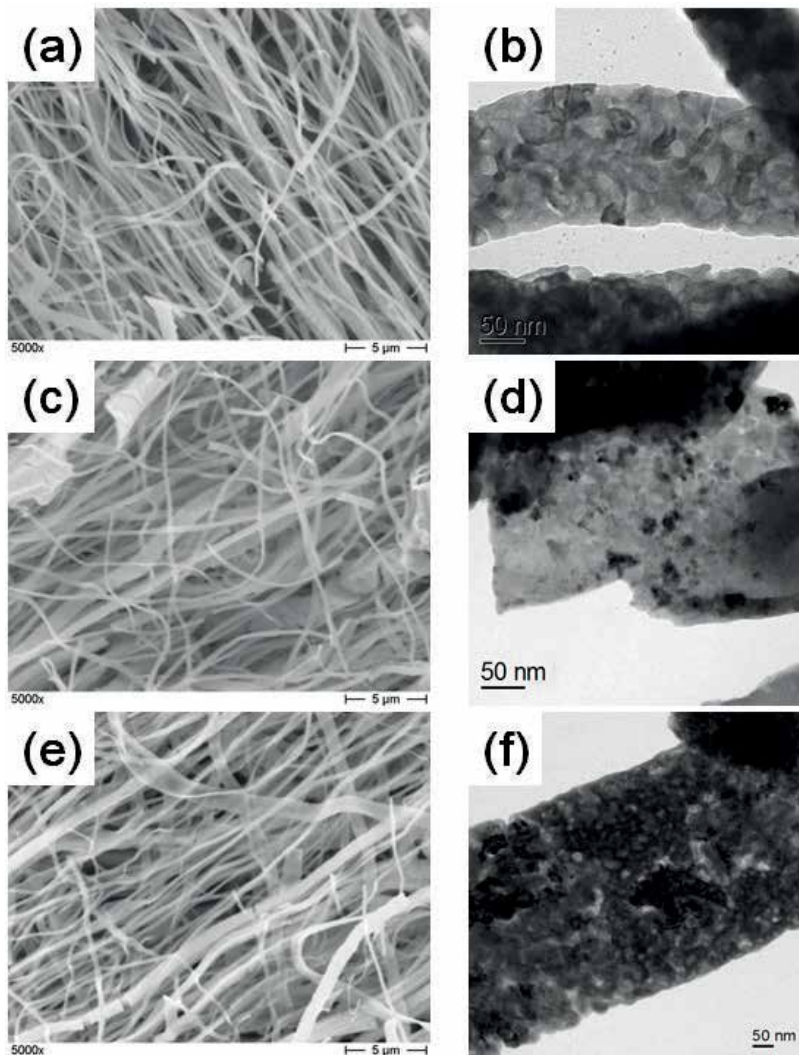
The magnetization of the nanowire networks was measured using a SQUID magnetometer (Quantum Design MPMS3) with  $\pm 7$  T magnetic field applied perpendicular to the sample surface, using a piece of the nanowire network fabric with a size of  $14.86 \text{ mm}^2$ .

### 3. Results and discussion

#### 3.1. Microstructure

Scanning electron microscopy revealed an average diameter of the resulting nanowires of around 220 nm and a length of more than 100  $\mu\text{m}$ . Fabric-like nanowire networks with numerous interconnects are formed after the heat treatment. The individual nanowires are polycrystalline with a grain size of about 10–30 nm, which corresponds to the dimensions obtained via transmission electron microscopy and electron backscatter diffraction (EBSD) analysis.

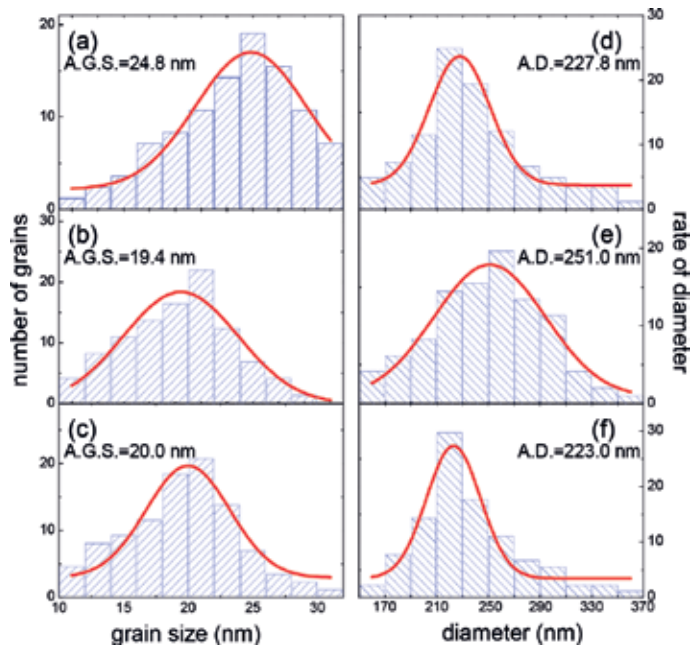
This is presented in **Figure 4** giving SEM images of the nanowire network fabrics at  $5000\times$  magnification (first column) and at higher magnification ( $10,000\times$ , second column) for all LSMO samples studied here. **Figure 4(a)** and **(b)** shows the sample  $x = 0.2$ , **(c)** and **(d)** the



**Figure 4.** SEM images of the nanowire network fabrics at 5000 $\times$  magnification (first column) and at higher magnification (10,000 $\times$ , second column) for all LSMO samples studied here. Images (a) and (b) show the sample  $x = 0.2$ , (c) and (d) the sample  $x = 0.3$ , and (e) and (f) the sample  $x = 0.4$ . The low-magnification images (a, c, and e) present the individual nanowires forming the network fabrics and the numerous interconnects between them. The images with higher resolution (b, d, and f) reveal the polycrystalline structure of the LSMO nanowires. Several small LSMO grains form stacks building up a nanowire.

sample  $x = 0.3$ , and (e) and (f) the sample  $x = 0.4$ . The low-magnification images (a, c, and e) present the individual nanowires forming the network fabrics and the numerous interconnects between them. These interconnects, formed during the final heat treatment step, are essential for the current flow through the entire sample perimeter and provide additional scattering at their interfaces. The images with higher resolution (b, d, and f) reveal the polycrystalline structure of the LSMO nanowires. Several small LSMO grains form stacks building up a



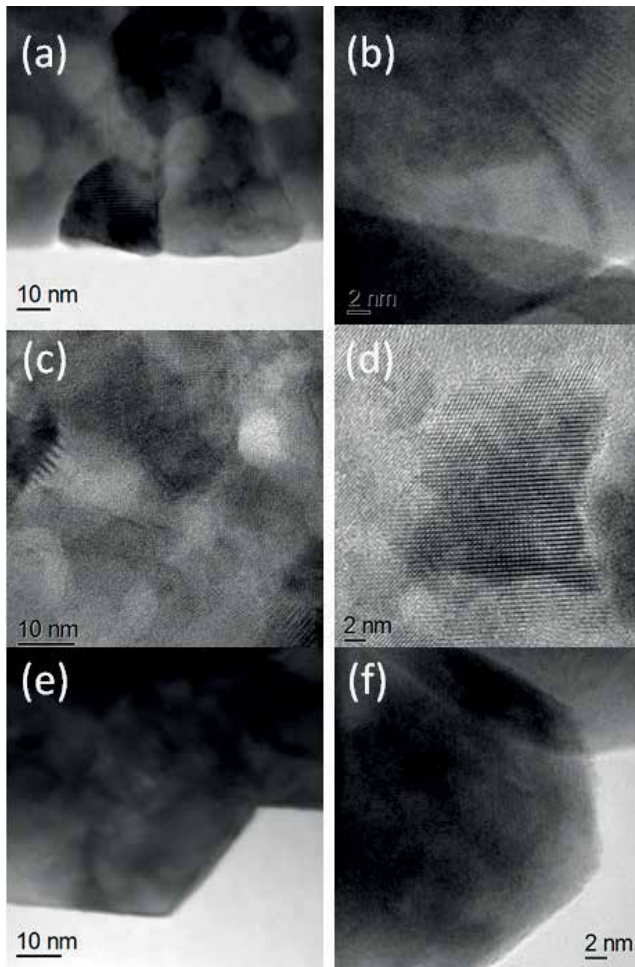


**Figure 5.** Statistics of the nanowire average grain size (AGS) and the diameter (AD): (a) and (d)  $\text{La}_{0.8}\text{Sr}_{0.2}\text{MnO}_3$ , (b) and (e)  $\text{La}_{0.7}\text{Sr}_{0.3}\text{MnO}_3$ , and (c) and (f)  $\text{La}_{0.6}\text{Sr}_{0.4}\text{MnO}_3$ .

nanowire. Furthermore, it is obvious from these images that the LSMO grains do not show a texture but are randomly oriented with high-angle grain boundaries between them.

**Figure 5** presents the detailed analysis of the nanowire diameters and the LSMO grain size determined from several SEM and TEM images. Graphs (a), (b), and (c) show the grain size analysis. The average values were determined using a Gauss fit to the data (indicated by a red line). For the sample  $x = 0.2$ , the grain size determined from TEM images varies between 10 and 32 nm with an average of 24.8 nm. Sample  $x = 0.3$  shows grain sizes ranging between 10 and 30 nm with an average of 19.4 nm, and sample  $x = 0.4$  shows an equal distribution of grain sizes with an average grain size of 20 nm. For all samples, the grain sizes determined from the TEM images are larger than the average grain size estimated from XRD, indicating that there are multiply connected domains in the sample. On the sample  $x = 0.2$ , we also performed an EBSD analysis of the grain orientations [29], demonstrating the presence of a large number of high-angle grain boundaries within an individual nanowire. The graphs of the nanowire diameters (d, e, f) reveal an average diameter of 227.8 nm for the sample  $x = 0.2$ , 251.0 nm for sample  $x = 0.3$ , and 223.0 nm for sample  $x = 0.4$ . Note that also here the sample  $x = 0.4$  exhibits a much narrower size spectrum as compared to the other samples studied here.

**Figure 6** shows finally some high-resolution TEM images of all three LSMO samples studied here. **Figure 6(a)** and **(b)** gives grains and their grain boundaries of sample  $x = 0.2$ , (c) and (d) images with the same magnification of sample  $x = 0.3$ , and (e) and (f) those of sample  $x = 0.4$ . For all samples, the LSMO grain size is around 20 nm, and the grains are randomly oriented, resulting in the presence of high-angle grain boundaries between the grains. Furthermore,

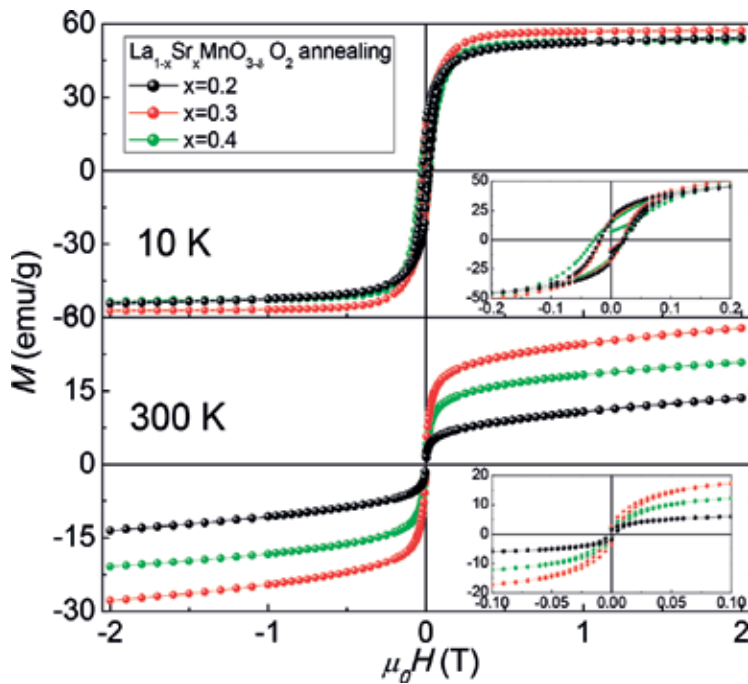


**Figure 6.** High-resolution TEM images with two magnifications of all three types of samples studied here. (a, b) sample  $x = 0.2$ , (c, d) sample  $x = 0.3$ , and (e, f) sample  $x = 0.4$ . The LSMO grains of all samples have sizes of  $\sim 20$  nm, and the grains are randomly oriented with high-angle grain boundaries between them.

some grain clusters connected with low-angle grain boundaries are also visible, which explains the differences between the grain sizes determined from X-ray data and from TEM images and EBSD [29]. The nanowires are built up from stacks of several individual LSMO grains.

### 3.2. Magnetization data

**Figure 7** presents the magnetization data obtained for all three compositions. The soft magnetic character of the LSMO fabric samples is clearly revealed. From the Sr-doping level  $x = 0.2$  to  $x = 0.4$ , the saturation magnetization ( $M_s$ ) is at 10 K 52.12, 58.13, 55.03 emu/g, and at 300 K, and the results are 18.10, 33.09, and 24.62 emu/g, respectively. These values are lower than the ones from the corresponding bulk materials (56 emu/g [30], 33.09 emu/g [31], 24.62 emu/g [32]). The  $x = 0.3$  sample shows the highest  $M_s$  among the three compositions, while the  $x = 0.2$  sample shows the lowest ones at both 10 and 300 K. This matches the conclusion drawn from



**Figure 7.**  $M(H)$  measurement of the LSMO nanowires at  $T = 10$  and  $300$  K for the LSMO nanowire network fabrics with  $x = 0.2, 0.3,$  and  $0.4$ . The insets present the low-field part of the  $M(H)$  loops.

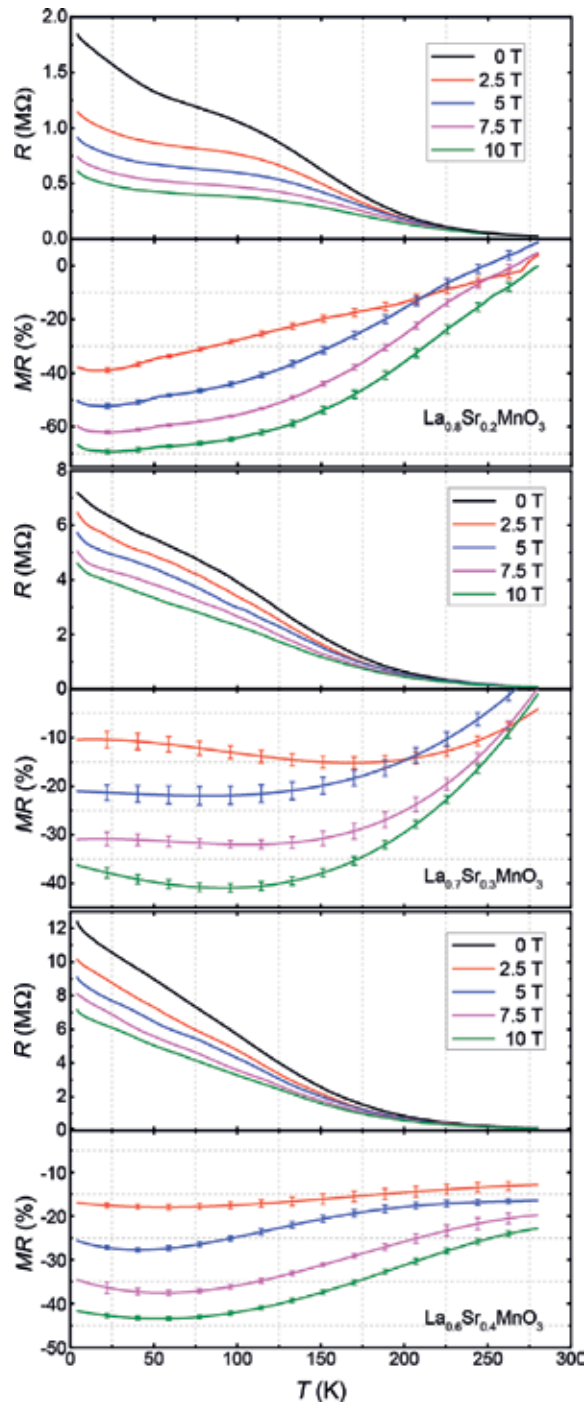
bulk materials that the optimized doping level around  $x = 0.33$  supplies the strongest magnetism. The  $M_s$  value of all our samples being lower than the bulk values can be attributed to the smaller LSMO grain size of our nanowire network fabric samples.

### 3.3. MR data

With respect to the literature, the MR data can be divided into two different regimes. According to Ref. [8], the high-field magnetoresistance (HFMR) behavior sheds light on the influence of the sample microstructure via the interface response. Therefore, analyses of the electronic transportation properties and of the magnetoresistive effects of the nanowire networks were carried out by four-probe measurements in external magnetic fields up to  $10$  T. Firstly, we have a look at the high-field regime. **Figure 8** presents the resistance measurements for the samples  $x = 0.2, 0.3,$  and  $0.4$  with the temperature swept from  $5$  to  $300$  K. The magnetic field is applied perpendicular to the surface of the nanowire network, i.e., the current flow is mainly perpendicular to the applied field. Magnetoresistance plots are calculated from the data obtained by these sweep measurements at different field strengths using the relation

$$MR[\%] = \frac{R_H(T) - R_0(T)}{R_0(T)}. \quad (1)$$

As result, we find a maximum MR for the sample  $x = 0.2$  at low temperatures and  $10$  T applied magnetic field of  $70\%$ .



**Figure 8.** Resistance and MR ratio of all LSMO samples measured up to 10 T applied magnetic field ( $H \perp$  sample surface) in the range  $2 \text{ K} < T < 275 \text{ K}$ : (a)  $x = 0.2$ , (b)  $x = 0.3$ , and (c)  $x = 0.4$ .

Doping level	MR(%) at 1 T		
$x$	100 K	270 K	MR max.
0.2	10.24	130	20.66 (22 K)
0.3	6.23	5.98	14.54 (203 K)
0.4	23.58	7.69	26.01 (63 K)
	MR(%) at 2.5 T		
0.2	27.69	1.81	39 (14 K)
0.3	13.2	6.32	15.18 (169 K)
0.4	17.48	13.00	17.97 (57 K)
	MR(%) at 10 T		
0.2	64.12	4.62	69.28 (25 K)
0.3	40.88	5.84	40.93 (92 K)
0.4	41.99	23.47	43.45 (54 K)

The last row gives the maximum MR obtained, together with the respective temperature.

**Table 1.** MR comparison of all LSMO samples measured here at 1, 2.5, and 10 T applied magnetic field.

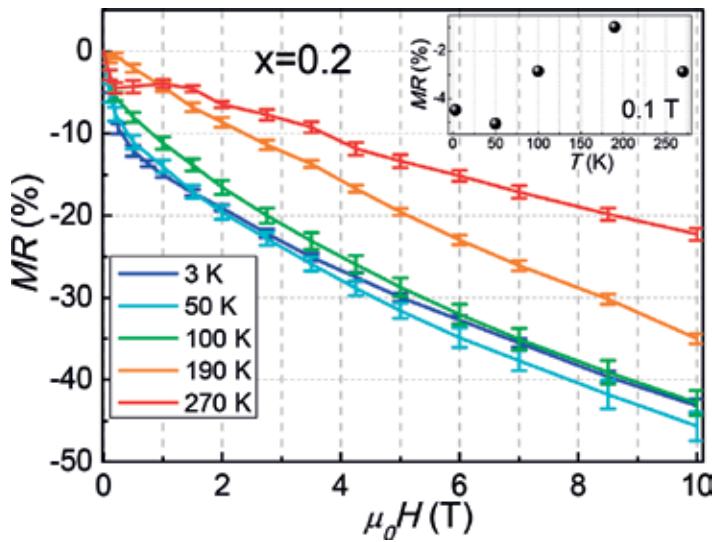
**Table 1** summarizes our findings at three selected magnetic fields for the three doping levels studied here, and the last row of **Table 1** gives the maximum MR obtained, together with the respective temperature.

At 100 K, sample  $x = 0.4$  shows the highest MR of 23.58% (close to the overall maximum of 26%) at 1 T applied field. In higher applied fields, the maximum value of the MR shifts to sample  $x = 0.2$ . A maximum MR ratio of 26.01 at an applied field of 1 T (63 K) is obtained for the sample  $x = 0.4$ . At 2.5 T applied field, the maximum MR of 17.97 (57 K) is found in sample  $x = 0.4$ .

At 10 T applied magnetic field, the maximum MR rate of 69.28% (25 K) is found in sample  $x = 0.2$ . Note also that the highest MR of sample  $x = 0.3$  is obtained at a relatively high temperature, ranging from 203 K (1 T) to 92 K (10 T). Secondly, **Figure 9** shows the MR ratio of sample  $x = 0.2$  as a function of applied field for various temperatures. The MR is found to increase on lowering the temperature down to 50 K, whereas the MR at low temperature (3 K) is found to be smaller as compared to the 50 K data. The inset of **Figure 9** presents the LFMR at an applied magnetic field of 0.1 T for the sample  $x = 0.2$ , showing a maximum LFMR of 5.2% at  $T = 50$  K.

### 3.4. Discussion

From the graphs and the tables presented here, three main features can be deduced: Firstly, there is a suppression of the metal-insulator transition. This provides another evidence of the size effect. Nanoscale grains are always accompanied by a large number of grain boundaries, which enhance electron scattering. The metallic behavior is negatively influenced by the Coulomb blockade [13], and as a result, an upturn of the resistivity appears at low temperatures. On the



**Figure 9.** Magnetoconductance of  $\text{La}_{0.8}\text{Sr}_{0.2}\text{MnO}_3$  nanowires as function of the applied field for various temperatures. The inset gives the LFM at 0.1 T applied magnetic field.

other hand, the influence from certain size effects varies with the doping level. So, a step-shape resistance behavior can be observed for the  $x = 0.2$  sample, which indicates that the metal-insulator transition is not completely suppressed. However, in the  $x = 0.4$  sample, the resistance decreases almost linearly from 5 K to 200 K. Secondly, the  $x = 0.3$  sample with the highest saturation magnetization shows the weakest MR effect, and inversely, the  $x = 0.2$  sample exhibits the strongest MR effect at low temperatures. At 2.5 T, the maximum of the MR is already over 40%, which points to an obvious LFM effect. The increase of the MR with reducing magnetization has been reported by Balcells et al. [8]. In their work, the variation of the grain size is the main reason of the behavior observed. In contrast to this, there is no obvious grain size difference between our samples. The dependence of the MR on the magnetization requires further investigation. Thirdly, at around 200 K with the doping level  $x = 0.2$  and 0.3, the MR curves at 2.5 T have a crossover with the curves at other fields, which means that above 200 K, the absolute value of the magnetoconductance does not monotonously increase with the external field.

To enable a comparison with data on other LSMO sample types but with the same chemical composition ( $x = 0.2$ ) published in the literature, we summarize some data at 1 T applied magnetic field in **Table 2** and compare them with our results.

From **Table 2**, we see that the nanowire sample  $x = 0.2$  exhibits a larger MR of 20.66% as compared to the nanoparticles and the powder samples of the same chemical composition studied in the literature, however, at some lower temperature. This points clearly to the important contribution of the much smaller LSMO grains in our nanowire network fabric samples, and the additional scattering at the numerous interconnects provides extra MR.

Jugdersuren et al. [19] reported a large LFM at room temperature in their LSMO nanowires extracted from the networks produced by electrospinning and showed a dependence of the

$\text{La}_{0.8}\text{Sr}_{0.2}\text{MnO}_3$	MR(%) at 1 T		
Type	100 K	270 K	MR max
Nanowires [33]	11.2	3.93	20.66 (22 K)
Nanoparticles [34]	19.36	4.66	18.7 (79 K)
Powder [35]	13.56	6.47	17.66 (50 K)

**Table 2.** Magneto-resistance (MR) comparison of different sample types (nanowires, nanoparticles, and powder) of  $\text{La}_{0.8}\text{Sr}_{0.2}\text{MnO}_3$  samples studied in the literature at 1 T applied magnetic field.

LFMR on the nanowire diameter, but no information on the LSMO grain size was presented. However, the LFMR at 300 K for our sample is comparable to their data, even though the chemical composition is somewhat different. Nevertheless, this demonstrates that the LFMR as well as the HFMR can be considerably enhanced by reducing the nanowire diameter as well as the LSMO grain size.

## 4. Conclusion

Nonwoven nanowire networks of LSMO with three doping levels  $x = 0.2, 0.3,$  and  $0.4$  were fabricated by means of electrospinning. The magneto-resistance effect of these nanowire fabrics was measured up to 10 T applied field between 5 K and 300 K. The reduced dimensions of the nanowires and the large number of interconnects between them were found to increase the MR effect as compared to bulk samples. An MR ratio of 70% was observed for the sample  $x = 0.2$  at low temperatures and 10 T applied magnetic field.

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Nanowires are attracting wide scientific interest due to the unique properties associated with their one-dimensional geometry. Developments in the understanding of the fundamental principles of the nanowire growth mechanisms and mastering functionalization provide tools to control crystal structure, morphology, and the interactions at the material interface, and create characteristics that are superior to those of planar geometries. This book provides a comprehensive overview of the most important developments in the field of nanowires, starting from their synthesis, discussing properties, and finalizing with nanowire applications.

The book consists of two parts: the first is devoted to the synthesis of nanowires and characterization, and the second investigates the properties of nanowires and their applications in future devices.

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